

Compal Confidential

NAV70 Schematics Document

Intel Pineview Processor with Tigerpoint + DDRIII

2010-05-19

REV: 1.0

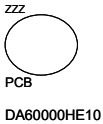
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File Name : LA-6222P

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Power Plane	Description	S1	S3	S5
VIN	Adapter power supply (19V)	N/A	N/A	N/A
B+	AC or battery power rail for power circuit.	N/A	N/A	N/A
+CPU_CORE	Core voltage for CPU	ON	OFF	OFF
+0.9VS	0.9V switched power rail for DDR terminator	ON	OFF	OFF
+VCCP	VCCP switched power rail	ON	OFF	OFF
+1.5VS	1.5V switched power rail	ON	OFF	OFF
+1.8V	1.8V power rail for DDR	ON	ON	OFF
+0.89V	Graphic core power rail	ON	OFF	OFF
+3VALW	3.3V always on power rail	ON	ON	ON*
+3VS	3.3V switched power rail	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON*
+5VS	5V switched power rail	ON	OFF	OFF
+VSB	VSB always on power rail	ON	ON	ON*
+RTCVCC	RTC power	ON	ON	ON

STATE \ SIGNAL	SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+V	+VS	Clock
Full ON	HIGH	HIGH	HIGH	ON	ON	ON	ON
S1 (Power On Suspend)	HIGH	HIGH	HIGH	ON	ON	ON	LOW
S3 (Suspend to RAM)	LOW	HIGH	HIGH	ON	ON	OFF	OFF
S4 (Suspend to Disk)	LOW	LOW	HIGH	ON	OFF	OFF	OFF
S5 (Soft OFF)	LOW	LOW	LOW	ON	OFF	OFF	OFF

	VCC	3.3V				
	Ra	100K				
	ID	BRD ID	Rb	Vab-Min	Vab-Typ	Vab-Max
NAV50	0	R01 (EVT)	0	0V	0V	0V
	1	R02 (DVT)	8.2K	0.216V	0.250V	0.289V
	2	R03 (PVT)	18K	0.436V	0.503V	0.538V
	3	R10A (MP)	33K	0.712V	0.819V	0.875V
NAV60	4	R01 (EVT)	56K	1.036V	1.185V	1.264V
	5	R02 (DVT)	100K	1.453V	1.650V	1.759V
	6	R03 (PVT)	200K	1.935V	2.200V	2.341V
	7	R10A (MP)	NC	2.500V	3.3V	3.3V

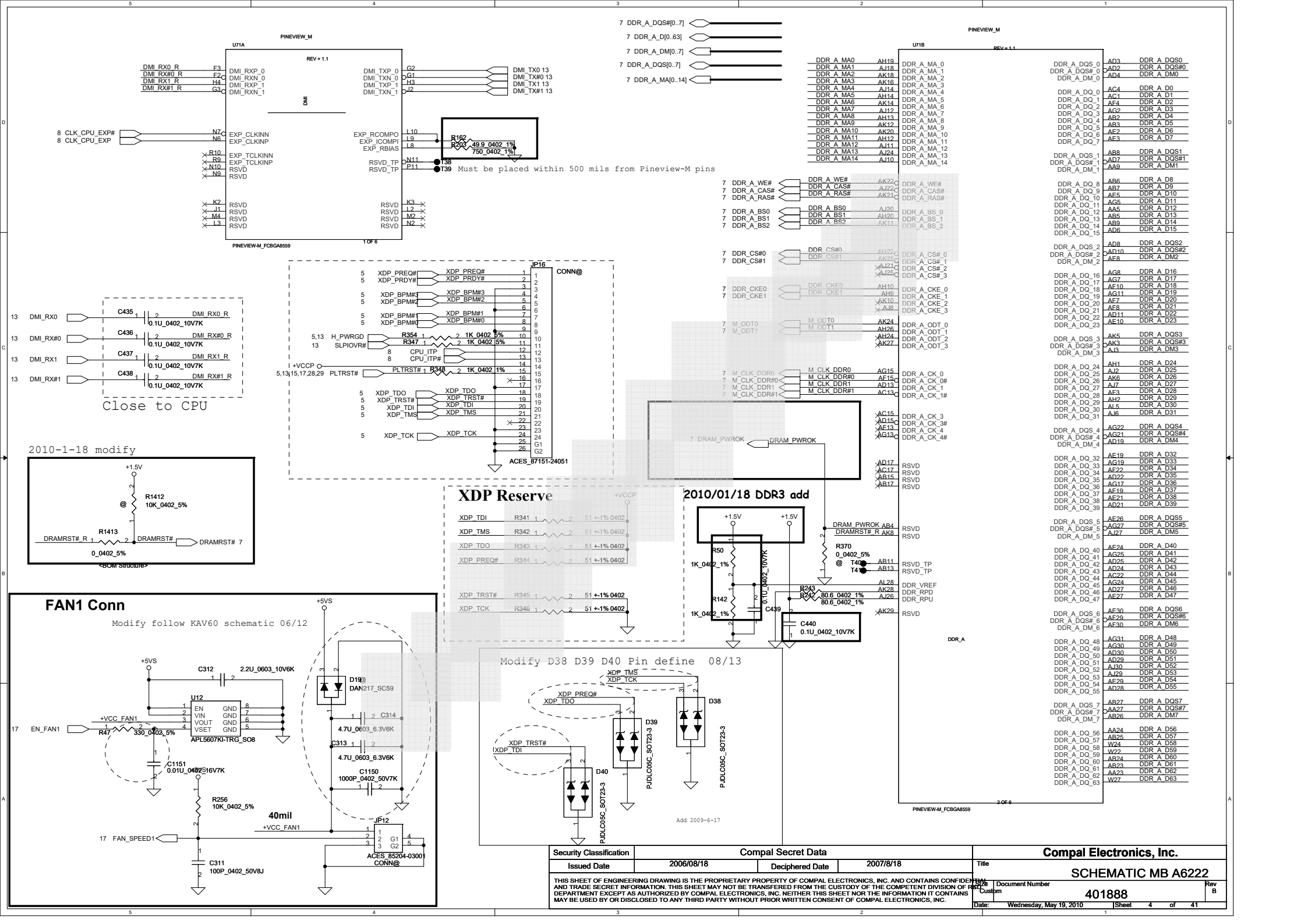
DEVICE	IDSEL #	REQ/GNT #	PIRQ
--------	---------	-----------	------

EC SM Bus1 address

Device	Address	Device	Address
Smart Battery	0001 011X b	EMC1402	100_1100

Device	Address
Clock Generator (SLG8SP556VTR)	1101 001Xb
DDR DIMMA	1010 000Xb

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[illegible][illegible][illegible]

VCCSM
VCCSM
VCCSM
VCCSM
VCCSM
VCCSM
VCCSM

[illegible]

VCCD_A

VCCD_H
VCCSFR
VCCACR
VCC_GIO
VCCRIN
VCCRIN
VCCRIN
VCCRIN
VCC_L

PINEVIEW

Age Group	Number of Respondents
A23	1
A25	1
A27	1
B23	1
B24	1
B25	1
B26	1
B27	1
C24	1
C26	1
D23	1
D24	1
D26	1
D28	1
E22	1
E24	1
E27	1
F21	1
F22	1
F25	1
G19	1
G21	1
G24	1
H17	1
H19	1
H22	1
H24	1
J17	1
J19	1
J21	1
J22	1
K15	1
K17	1
K21	1
L14	1
L16	1
L19	1
L21	1
N14	1
N16	1
N19	1
N21	1

C29	VCCSE
B29	VSSSE
Y2	+VCCPPr

V30	+VCC
W31	+VCC
T1	+VCC
T2	
T3	
P2	
AA1	+DMI F

The schematic diagram illustrates the power supply distribution for the R2 board. The +VCC input is connected to a network of capacitors and resistors. A 0.1U_0402_10V6K capacitor (C160) is connected to the +VCC input. A 1U_0603_10V6K capacitor (C161) is connected to the +VCC input and the +RING EAST output. A 0.1U_0402_10V6K capacitor (C68) is connected to the +VCC input and the +RING WEST output. A 1U_0603_10V6K capacitor (C242) is connected to the +RING EAST output and ground. A 1U_0603_10V6K capacitor (C241) is connected to the +RING WEST output and ground. A 1U_0603_10V6K capacitor (C64) is connected to the +RING WEST output and ground. A 1U_0603_10V6K capacitor (C237) is connected to the +VCC DMI output and ground. A 1U_0603_10V6K capacitor (C68) is connected to the +VCC DMI output and ground. Resistors R20, R21, and R28 are 0.0603_5% resistors. A note 'Close W1.04' is present near capacitor C161.

1.8V5

R25

1 2

MBK1608061Y2F_ZP1

+VCC_CRT_DAC

C239

1U_0603_10V6K

P

+DMI_HMPLL

R18

1 2

0_0603_5%

C69

1U_0603_10V6K

1

2

+VCC_ALVD

R26

1 2

1908-FSLR10J

C56

1

2

C1155

1U_0603_10V6K

22UF 6.3V M XSR0805 H1.25

+VCC_DLVD

R27

1 2

0_0603_5%

C235

1U_0603_10V6K

104A

100NH +5%

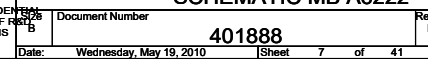
Follow

	UTIF	PINVIEW_M
	A11	REV = 1.1
	A16	VSS
	A19	VSS
	A23	RSVD_NCTF
	A3	RSVD_NCTF
	A30	RSVD_NCTF
	A4	
	AA13	VSS
	AA18	VSS
	AA16	VSS
	AA18	VSS
	AA2	VSS
	AA22	VSS
	AA25	VSS
	AA26	VSS
	AA29	VSS
	AA8	VSS
	AB19	VSS
	AB21	VSS
	AB28	VSS
	AB29	VSS
	AB30	VSS
	AC10	VSS
	AC11	VSS
	AC19	VSS
	AC2	VSS
	AC21	VSS
	AC28	VSS
	AC30	VSS
	AD26	VSS
	AD5	VSS
	AE1	VSS
	AE11	VSS
	AE13	VSS
	AE18	VSS
	AE17	VSS
	AE22	VSS
	AE31	VSS
	AE11	VSS
	AE17	VSS
	AE21	VSS
	AE24	VSS
	AF28	VSS
	AG10	VSS
	AG3	VSS
	AH18	VSS
	AH23	VSS
	AH28	VSS
	AH4	VSS
	AH6	VSS
	AH8	VSS
	AJ1	RSVD_NCTF
	AJ16	VSS
	AJ31	RSVD_NCTF
	AK1	RSVD_NCTF
	AK2	RSVD_NCTF
	AK23	RSVD_NCTF
	AK30	VSS
	AK31	RSVD_NCTF
	AL13	RSVD_NCTF
	AL19	VSS
	AL2	VSS
	AL23	RSVD_NCTF
	AL29	VSS
	AL3	RSVD_NCTF
	AL30	RSVD_NCTF
	AL9	VSS
	B13	VSS
	B16	VSS
	B19	VSS
	B22	VSS
	B30	RSVD_NCTF
	B31	RSVD_NCTF
	B5	VSS
	B9	VSS
	C1	RSVD_NCTF
	C12	VSS
	C21	VSS
	C22	VSS
	C25	VSS
	C31	VSS
	D22	RSVD_NCTF
	E1	VSS
	E10	RSVD_NCTF
	E19	VSS
	E21	VSS
	E26	VSS
	E8	VSS
	F17	VSS
	F19	VSS

[illegible]

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PINEVIEW-M FCBGA8559

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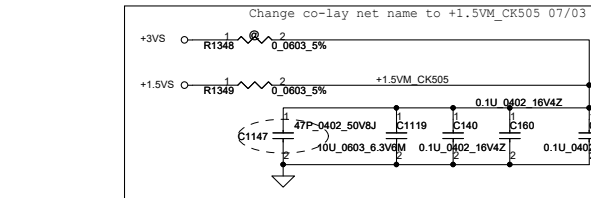
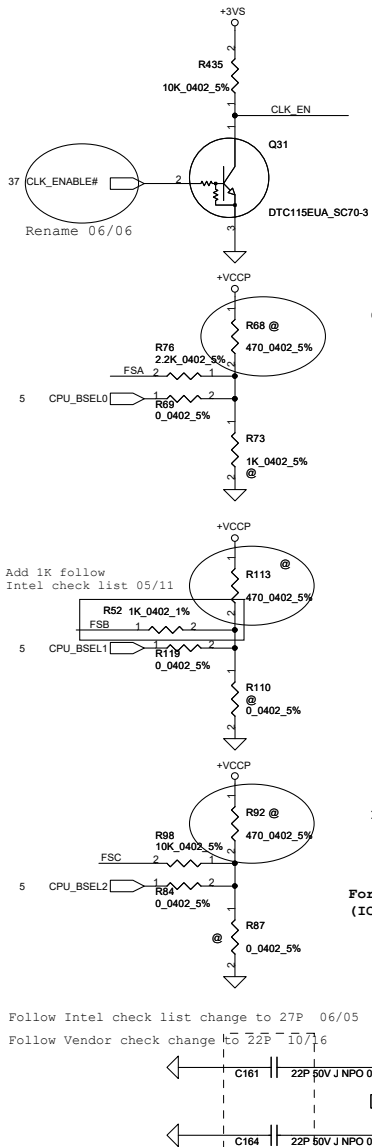


FSC	FSB	FSA	CPU	SRC	PCI	REF	DOT_96	USB
CLKSEL2	CLKSEL1	CLKSEL0	MHz	MHz	MHz	MHz	MHz	MHz
0	0	0	266	100	33.3	14.318	96.0	48.0
0	0	1	133	100	33.3	14.318	96.0	48.0
0	1	0	200	100	33.3	14.318	96.0	48.0
0	1	1	166	100	33.3	14.318	96.0	48.0
1	0	0	333	100	33.3	14.318	96.0	48.0
1	0	1	100	100	33.3	14.318	96.0	48.0
1	1	0	400	100	33.3	14.318	96.0	48.0
1	1	1						
Reserved								

Add C1145 C1146 C1147 for EMI 06/12

IDT: SA00003H610

Realtek: SA00003H730



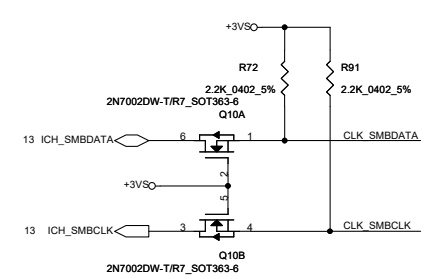
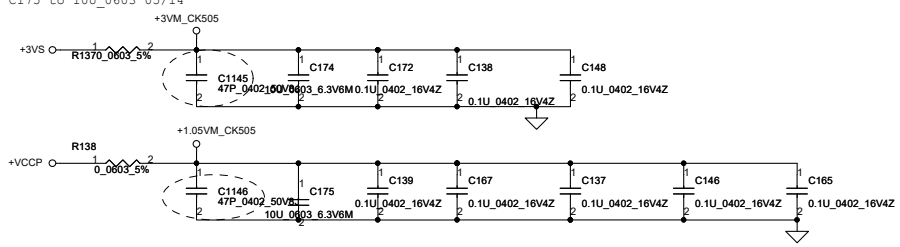
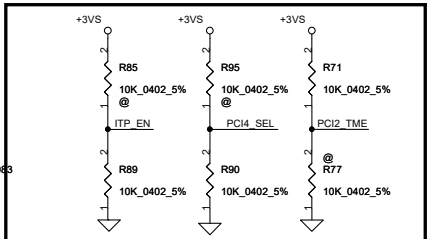
Change C1350 C1351 to 0402 type 06/24

01/21 Modify

01/21 Del

For ITP_EN, 0 =SRC8/SRC6#; 1 = ITP/ITP#
For PCI4_SEL, 0 = Pin24/25 : DOT96 / DOT96#
Pin28/29 : LCDCLK / LCDCLK#
1 = Pin24/25 : SRC 0 / SRC 0#
Pin28/29 : 27M/27M_SS

For PCI2_TIME:0=Overclocking of CPU and SRC allowed (ICS only) 1=Overclocking of CPU and SRC NOT allowed

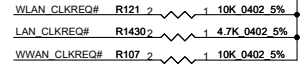


SRC PORT LIST

PORT	DEVICE
SRC1	CPU_SSCDREFCLK
SRC2	
SRC3	
SRC4	PCIE WLAN
SRC6	PCIE SATA
SRC7	PCIE PCH
SRC8	CPU ITP
SRC9	CLK CPU EXP
SRC10	PCIE LAN
SRC11	PCIE WWAN

Modify CLK SRC Port list 05/12

Add R107 05/04



REQ PORT LIST

PORT	DEVICE
REQ 3#	
REQ 4#	PCIE WLAN
REQ 6#	
REQ 7#	
REQ 9#	
REQ 10#	PCIE WWAN
REQ 11#	
REQ A#	

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LCD POWER CIRCUIT

Change R577 to 0402 SIZE 06/16

W=20mils

Change C1106 to 4.7U_0603 05/14

Modify 05/11

CMOS & LCD/PANEL BD. Conn.

Modify JLVDS1 08/04

The diagram illustrates the electrical connections for a CMOS camera and an LCD/PANEL board. It includes a pin header JLVDS1 with 27 pins, a camera module, and various passive components like resistors, capacitors, and inductors.

Pin Header JLVDS1:

- 1: USB20_P3_1
- 2: USB20_N3_1
- 3: (empty)
- 4: (empty)
- 5: (empty)
- 6: (empty)
- 7: (empty)
- 8: LVDS_ACLK
- 9: LVDS_ACLK#
- 10: LVDS_A2
- 11: LVDS_A2#
- 12: (empty)
- 13: LVDS_A1
- 14: LVDS_A1#
- 15: (empty)
- 16: LVDS_A0
- 17: LVDS_A0#
- 18: (empty)
- 19: LVDS_SDA
- 20: LVDS_SCL
- 21: (empty)
- 22: INVT_PWM
- 23: BKOFF#
- 24: (empty)
- 25: +LCDVDD_L
- 26: (empty)
- 27: +LEDVDD

Camera Module:

- 11/27: +3VS
- 11/26: DMIC_CLK 26, DMIC_DATA 26
- LVDS_ACLK, LVDS_ACLK#, LVDS_A2, LVDS_A2#, LVDS_A1, LVDS_A1#, LVDS_A0, LVDS_A0#
- BKOFF# 17, INVT_PWM 5, 17
- +LCDVDD, +3VS

Power Supply and Timing:

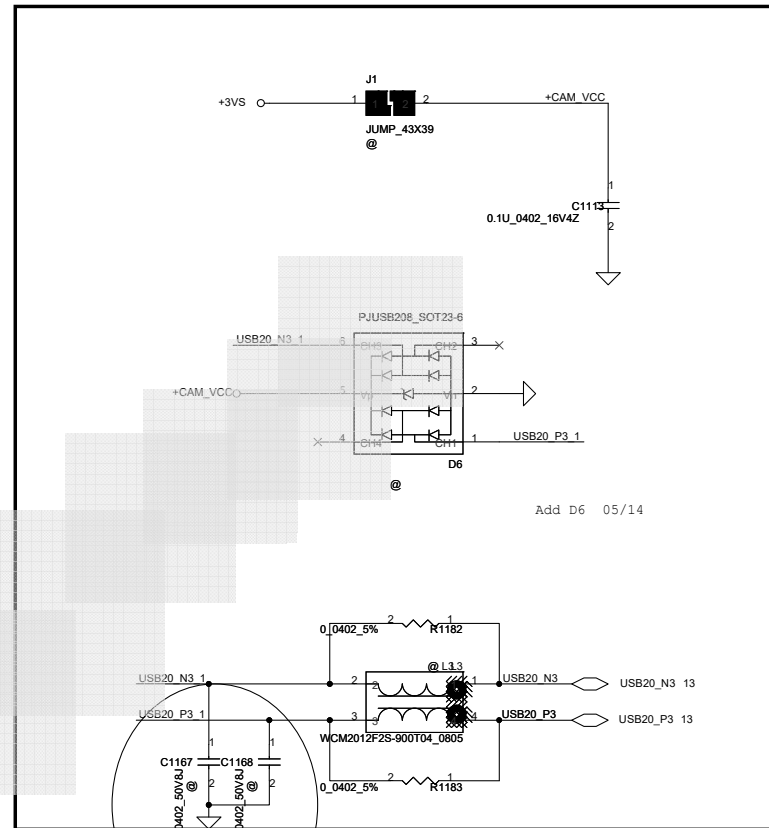
- +3VS: 3V3, 2.2K 0402 5%, R1180, R1181, 2.2K 0402 5%
- INVT_PWM: 220P 0402 50V7K, C1156
- BKOFF#
- +LEDVDD: 330P 0402 50V7K, C1111
- +LCDVDD: 1000P 50V K X7R 04, C1109
- For RF

Inductors and Connectors:

- L1, L2: FBMA-L11-201209-221LMA30T_0805
- L1, L2: FBMA-L11-201209-221LMA30T_0805
- (20 MIL)
- OB+

Other Components:

- ACES_88341-3000B001
- CONN@



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Add R1283 R1284
Change R247 R249 to 10 ohm
Add @ on U10 U11 C301 C298 06/08

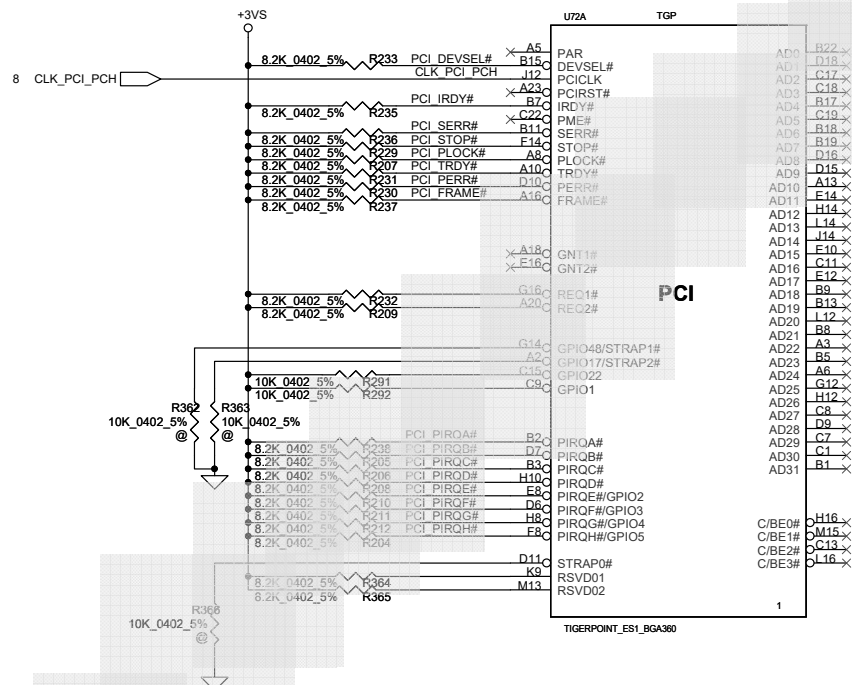
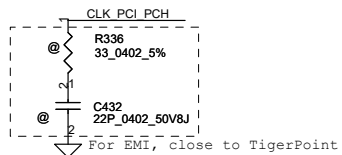
Modify C31- C308 C303 C307 C306 C304 BOM Structure 0615

Change CRT_DET# From Page 13 to Page 10 06/12

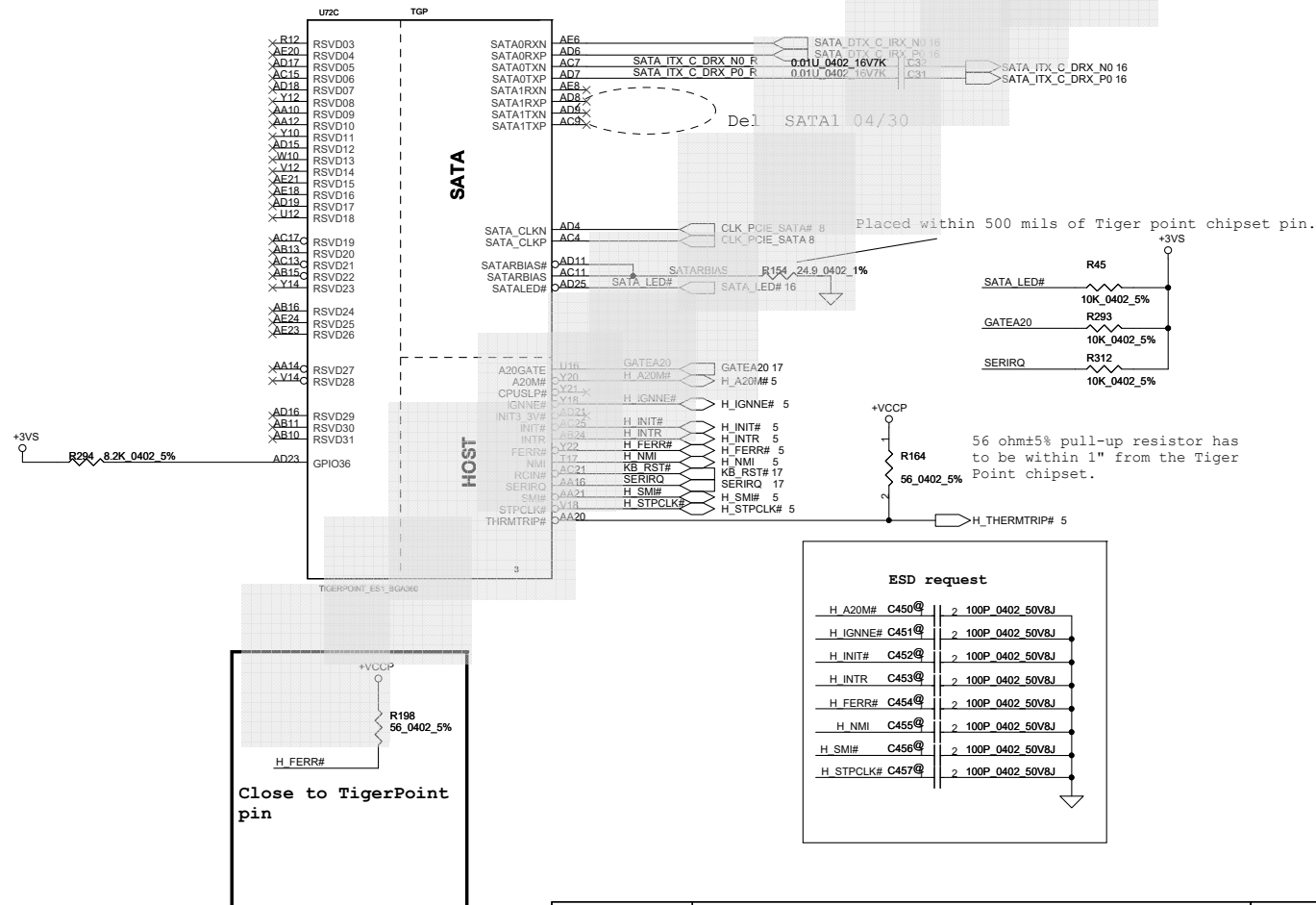
Change JCRT1 P/N to SP010906182 06/22

CRT PORT

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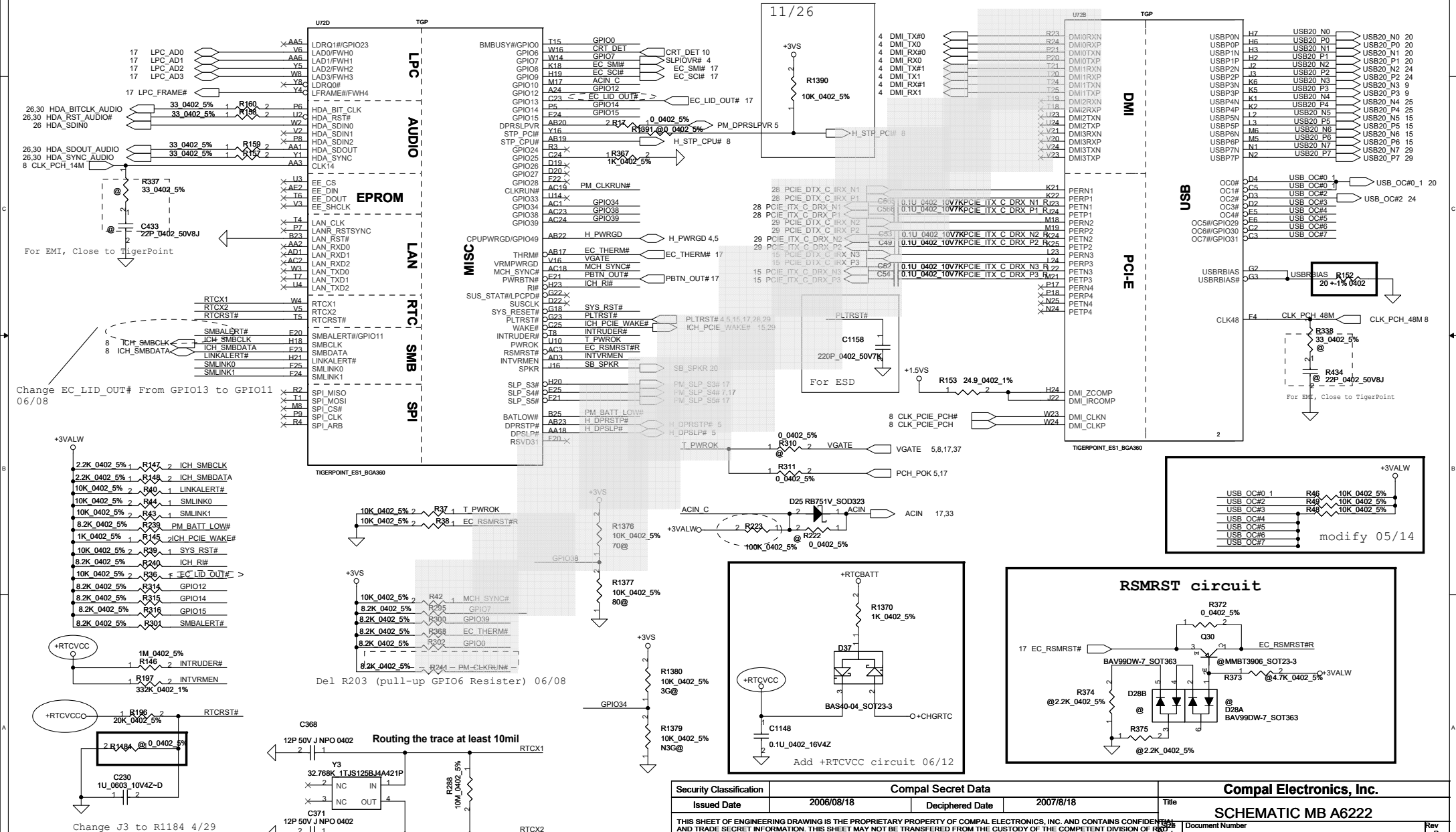
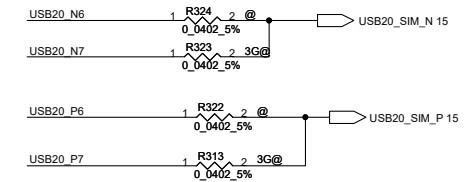
STRAP2# GPIO17	STRAP1# GPIO48	Boot BIOS
0	1	SPI
1	0	PCI
1	1	LPC

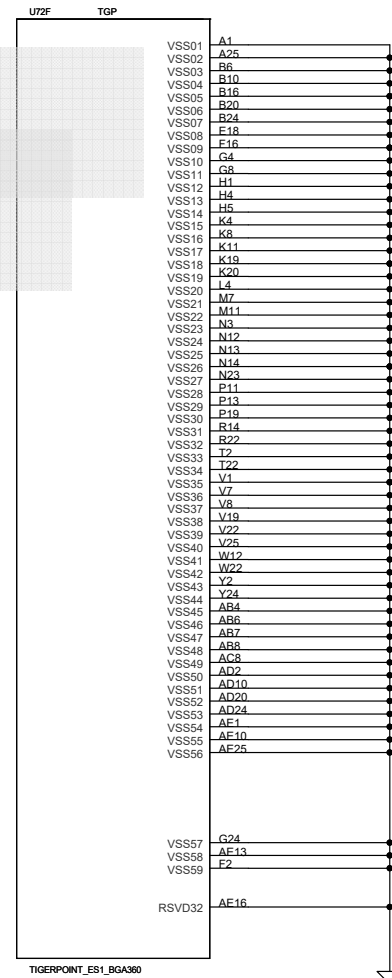
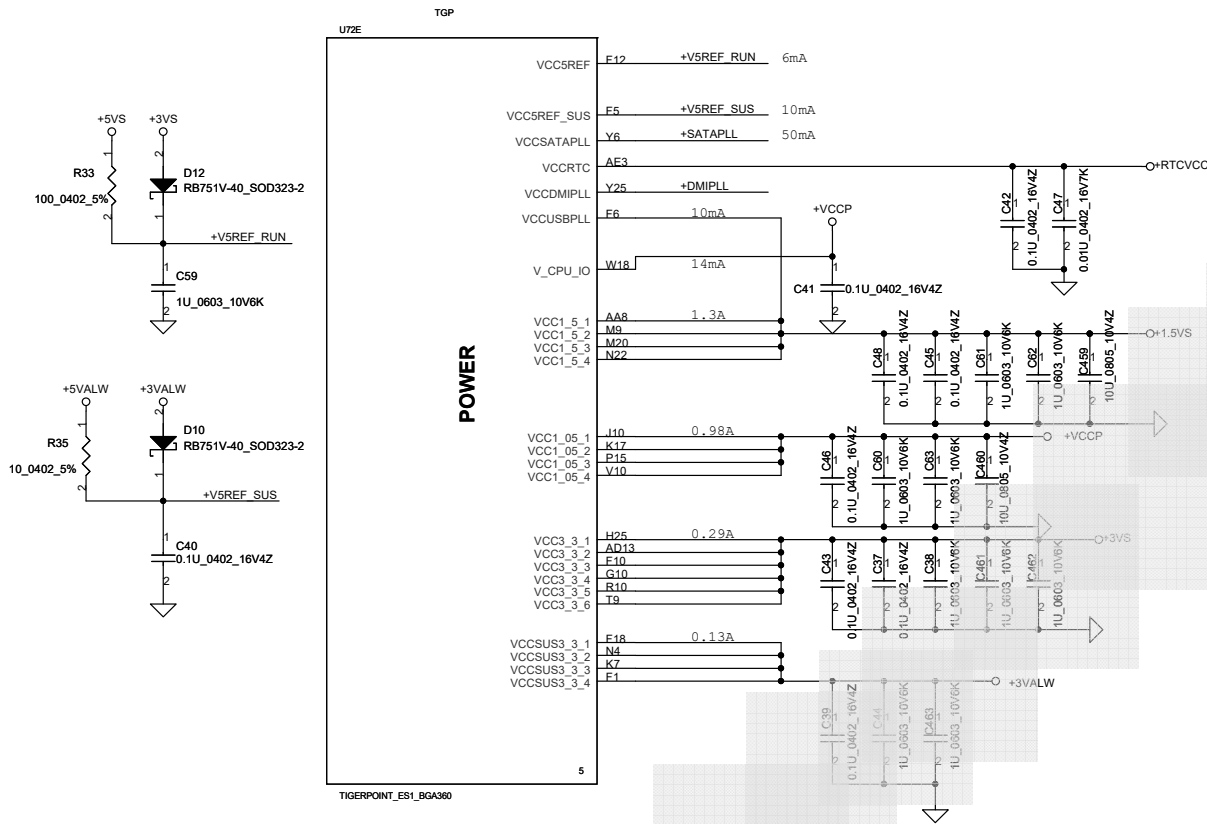


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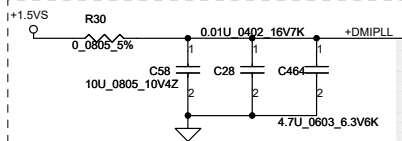
PCIE	Port List
1	LAN
2	WLAN
3	WWAN
4	

USB Port List	
0	USB Left1
1	USB Left2
2	USB Right2
3	CMOS
4	CardReader
5	WWAN
6	BT
7	WIMAX

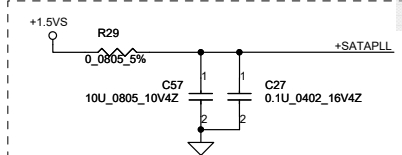




Place closely pin Y25 within 100mlis.



Place closely pin Y6 within 100mlis.



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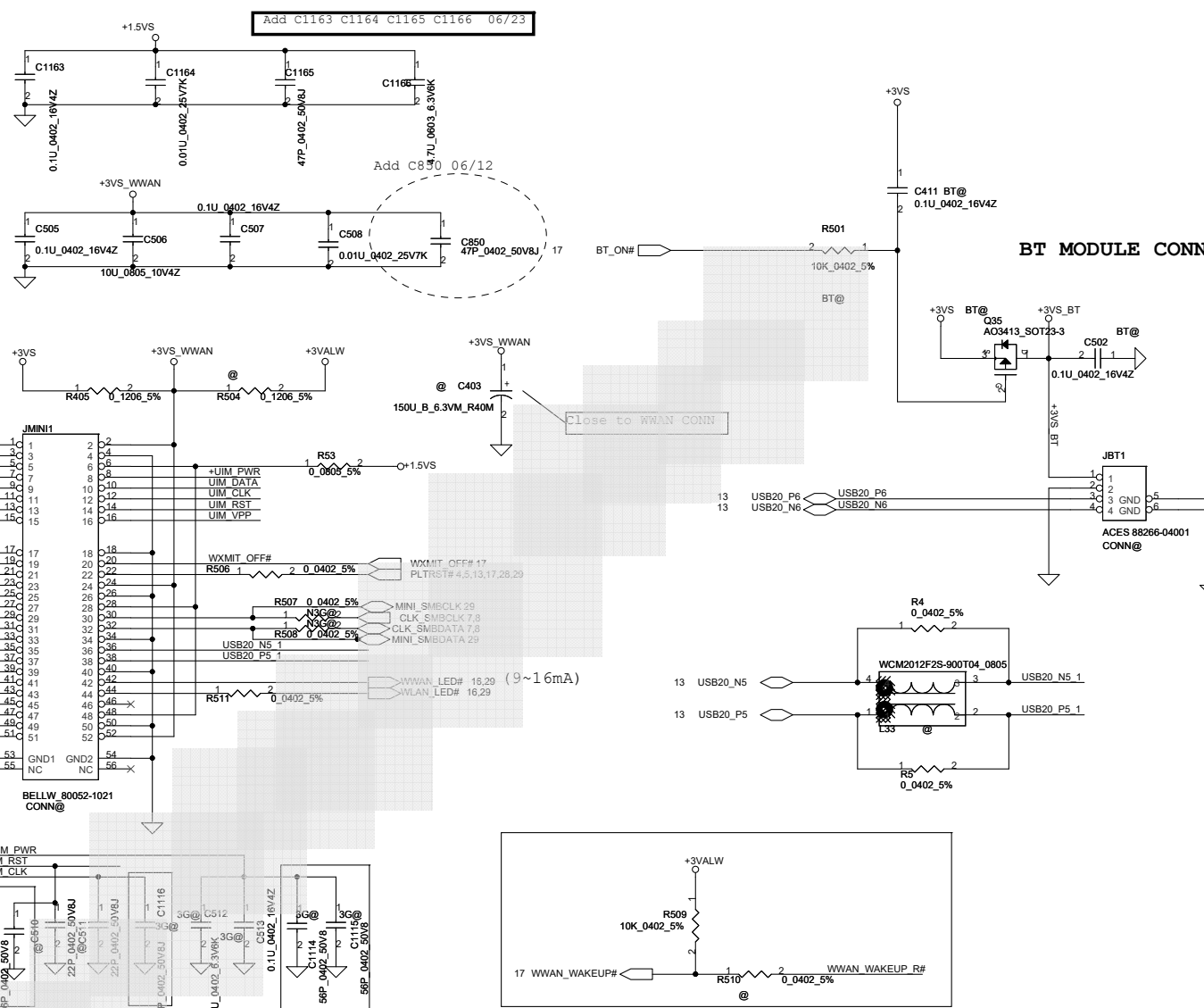
17 EC_TX_P80_DATA EC TX P80 DATA 1 2 EC TX P80 DATA_R
17 EC_RX_P80_CLK EC RX P80 CLK 1 2 EC TX P80 CLK_R
R4020_0402_5% R403 0_0402_5%

The diagram shows two digital signals over time. The top signal, labeled '13,29 ICH_PCIE_WAKE#', is a square wave that transitions from low to high at a point marked with a vertical line. The bottom signal, labeled '8 WWAN_CLKREQ#', is a square wave that transitions from low to high at a point marked with a vertical line. A third signal, labeled '8 CLK_PCIE_WWAN#', is shown below the WWAN_CLKREQ# signal and transitions from low to high at the same time as WWAN_CLKREQ#.

Modify QTIM_PWR

TAITW_PMPAT2:
CONN@

Reserve for SIM card does not meet rise time and pull-up is needed.



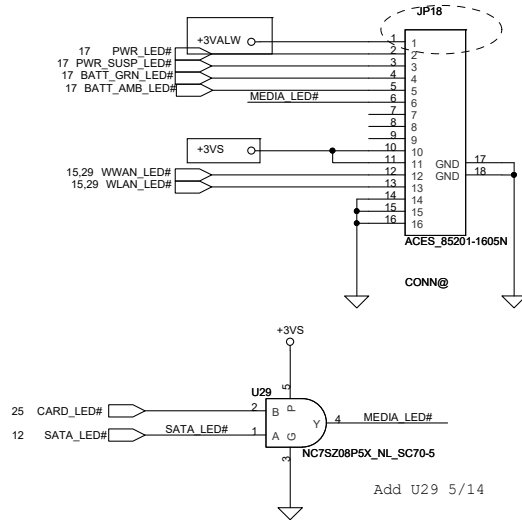
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ADD LED PCB CONN 06/12

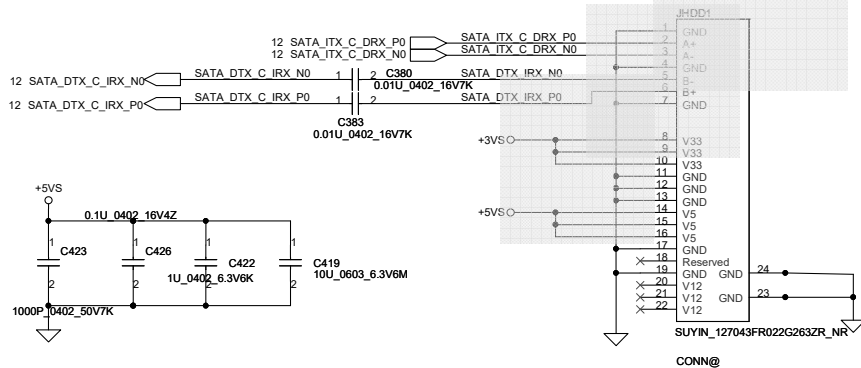
LED PCB CONN

Change JP18 to NEW P/N 06/23

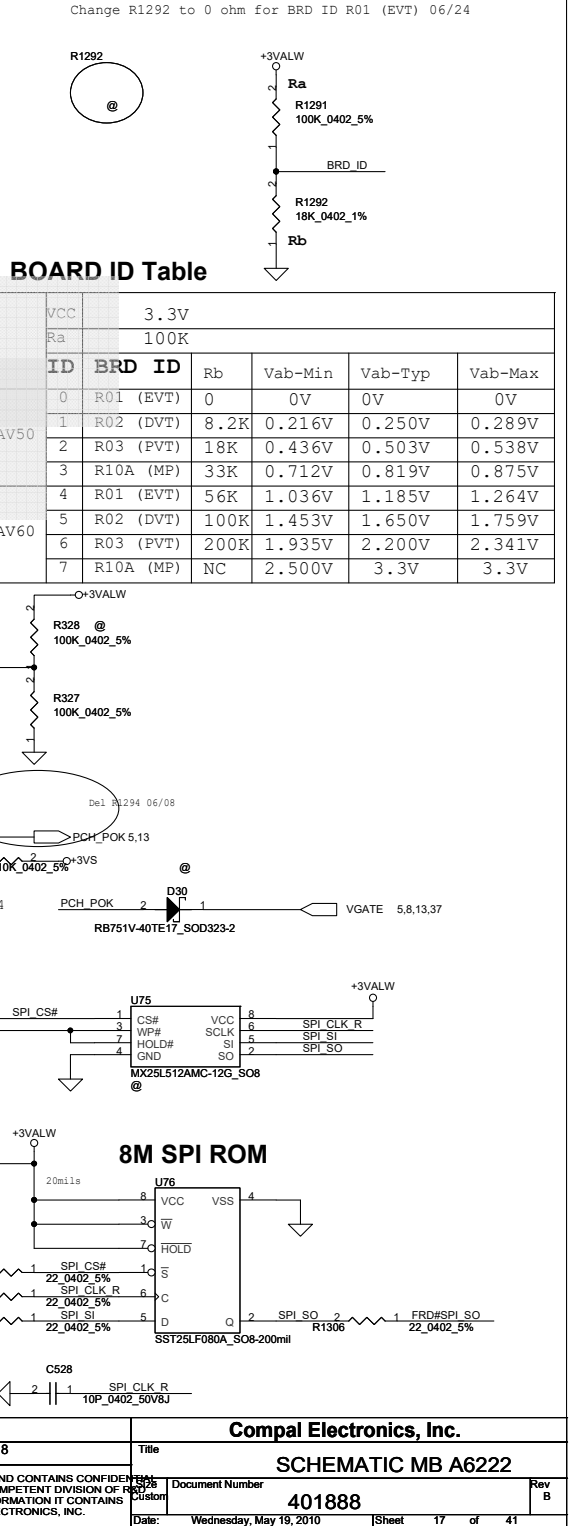
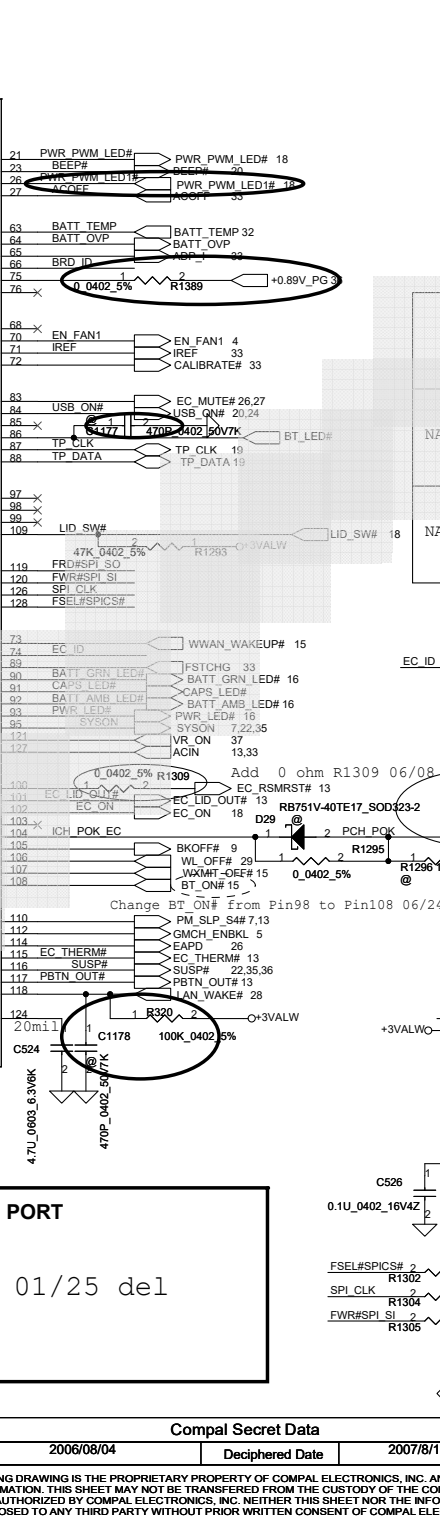
09/03 Change +5VALW , +5VS to +3VALW +3VS



SATA HDD Conn.



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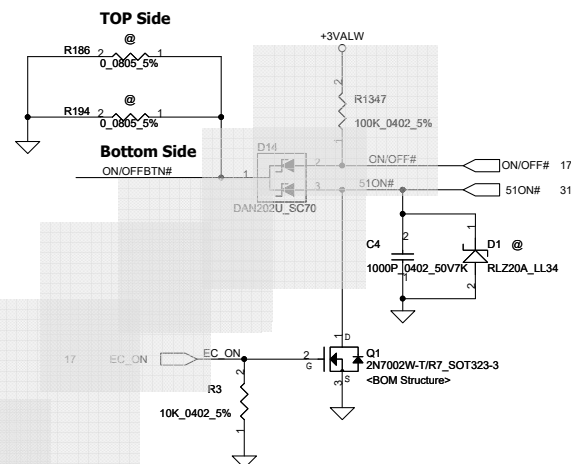
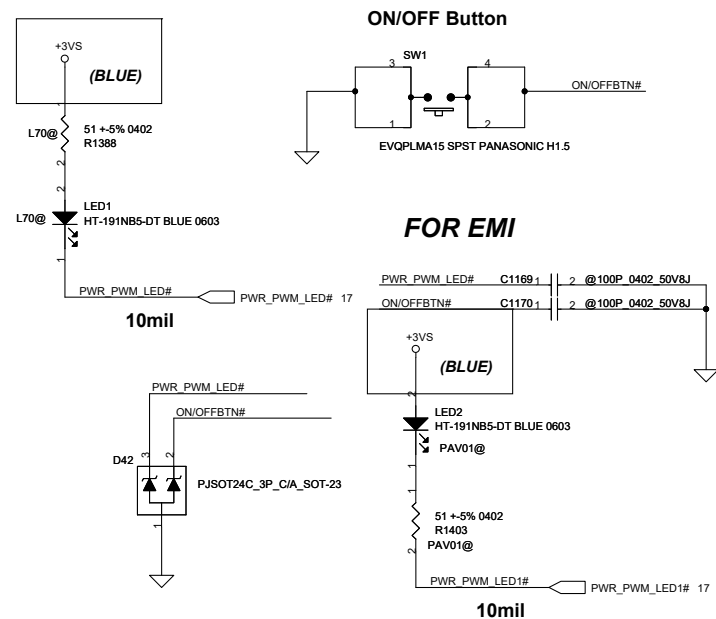


	KSO1
KSI1	WL_BTN#
KSI5	3G_BTN#

NAV50	VCC	3.3V				
	Ra	100K				
	ID	BRD ID	Rb	Vab-Min	Vab-Typ	Vab-Max
	0	R01 (EVT)	0	0V	0V	0V
	1	R02 (DVT)	8.2K	0.216V	0.250V	0.289V
	2	R03 (PVT)	18K	0.436V	0.503V	0.538V
	3	R10A (MP)	33K	0.712V	0.819V	0.875V
	4	R01 (EVT)	56K	1.036V	1.185V	1.264V
	5	R02 (DVT)	100K	1.453V	1.650V	1.759V
	6	R03 (PVT)	200K	1.935V	2.200V	2.341V
NAV60	7	R10A (MP)	NC	2.500V	3.3V	3.3V

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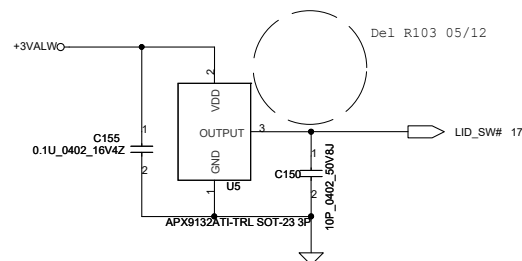
Add For NAV50 07/06
09/03 Change +5VS to +3VS



PWR/B Conn

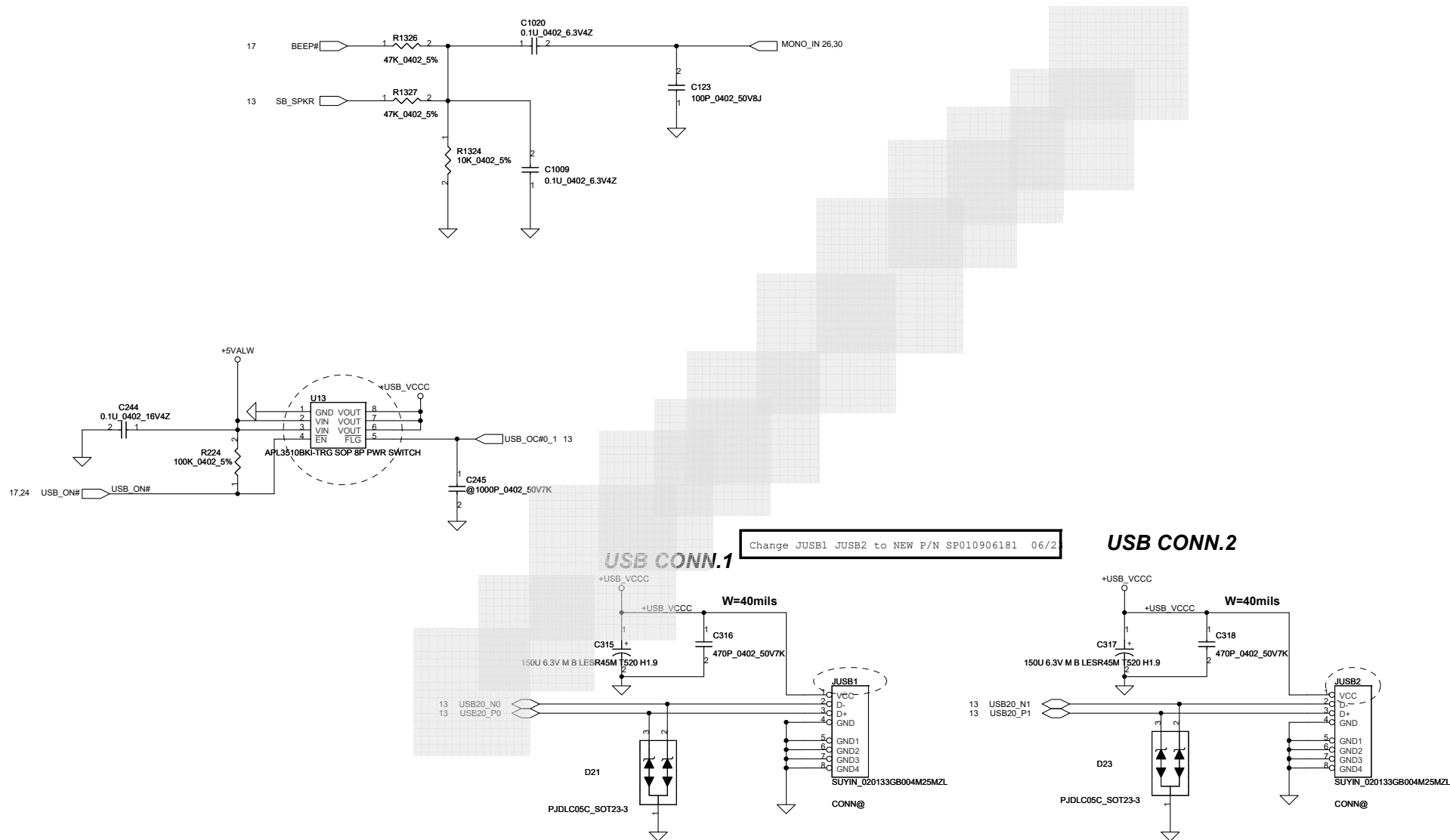
DEL PW/B Conn 0125

LID Switch



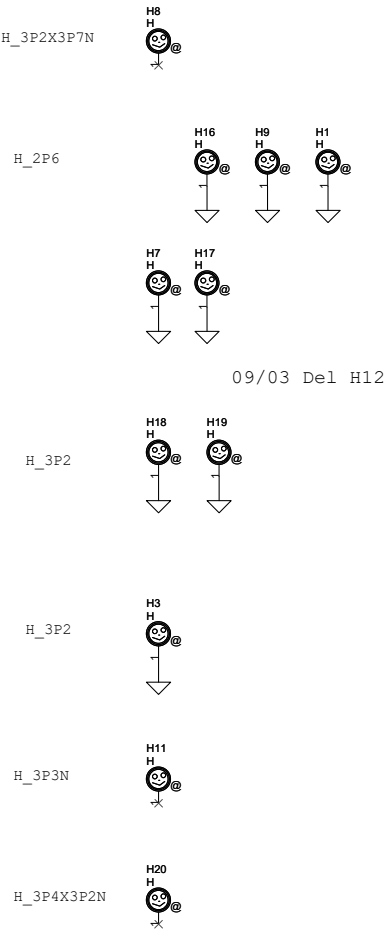
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6/26 Add circuit



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Modify Hole location by (ME Drawing 06/12) 0615



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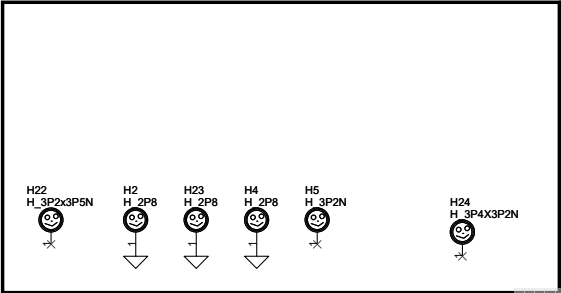
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
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COMPAL CONFIDENTIAL

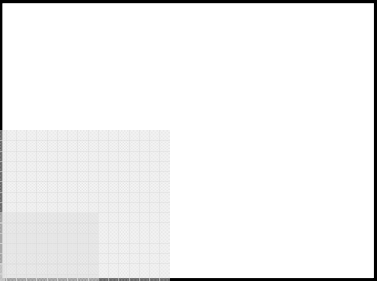
MODEL NAME: NAV50
PCB NAME: LS-5655P I/O Board
COMPAL P/N: DA60000FF30
REVISION: 3.0
DATE: 2009/12/01

5/20 Update connector pin name
6/23 Update connector pin name
7/01 Update pin number 8,10,14,16,20,22
7/02 Update PIN42 to +5VS

8/6 Update information about aperture

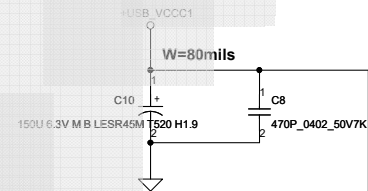
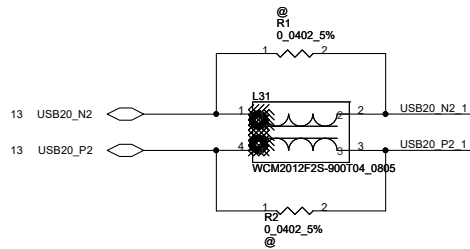
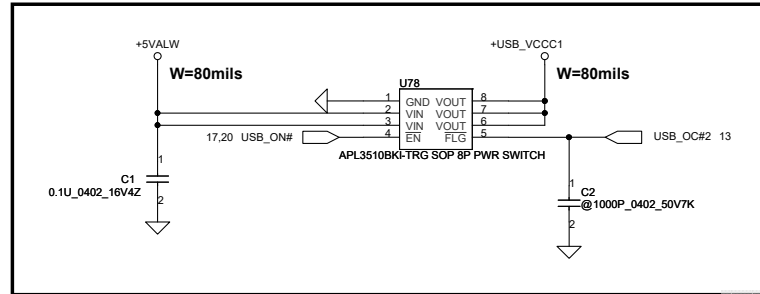


5/5 Update compal number

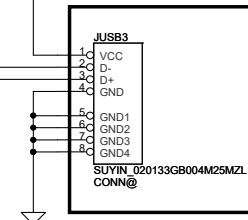


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5/5 Add U2 circuit

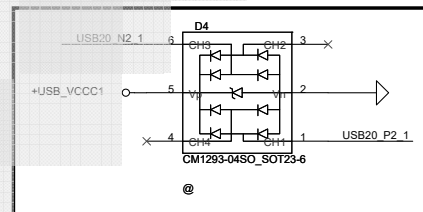


USB CONN. 2



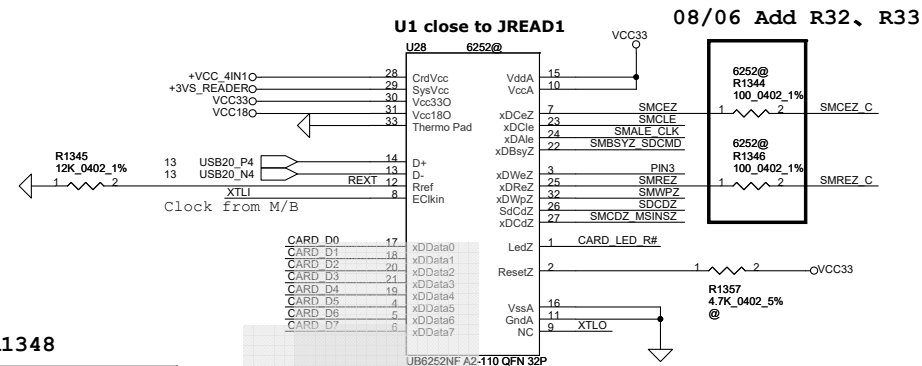
5/12 Revised USB connector
6/23 Update USB connector

5/12 Revised net name

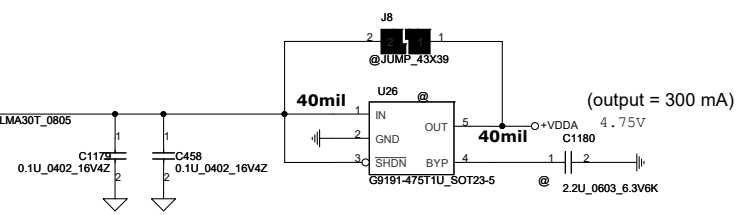
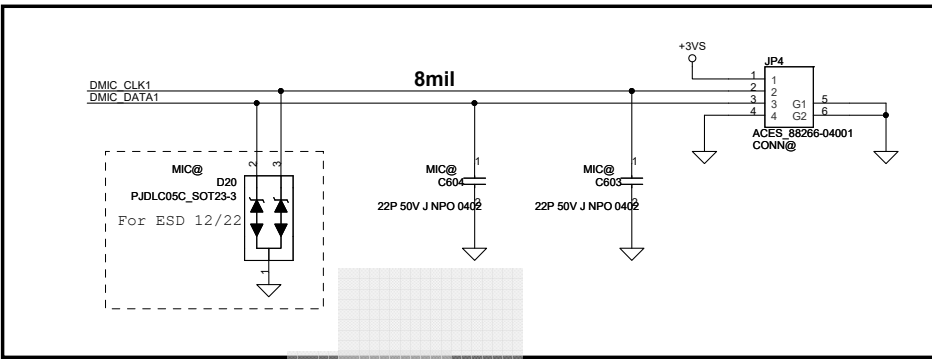
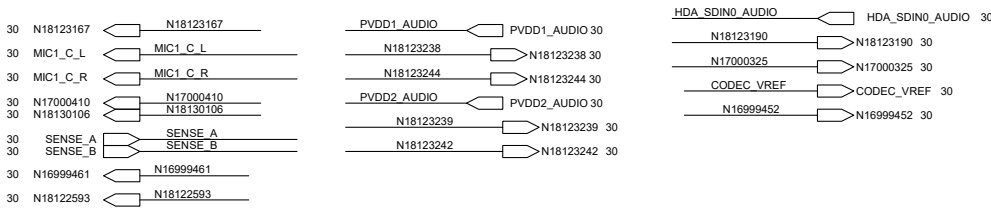


5/14 Deleted JUSB2

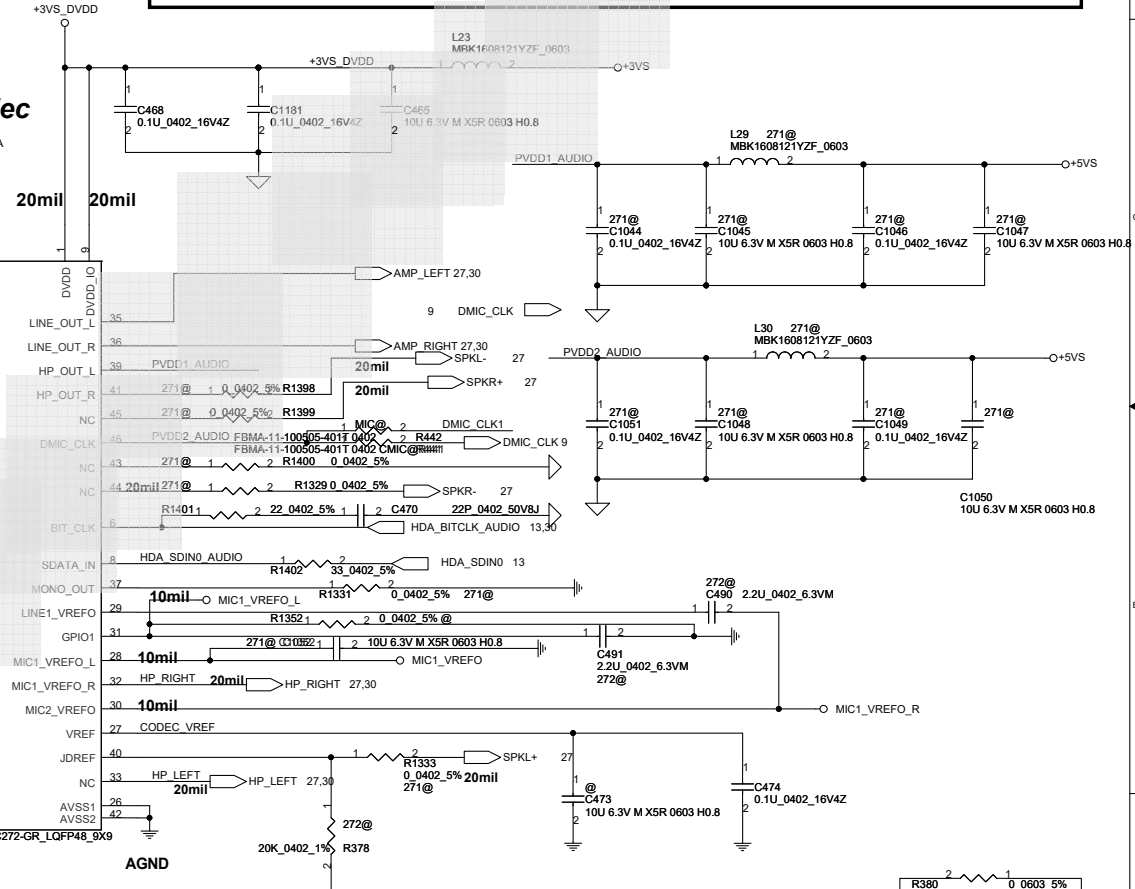
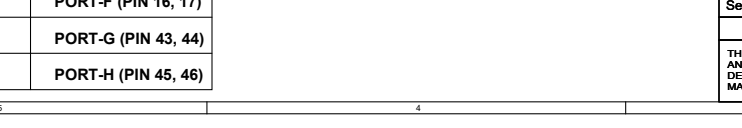
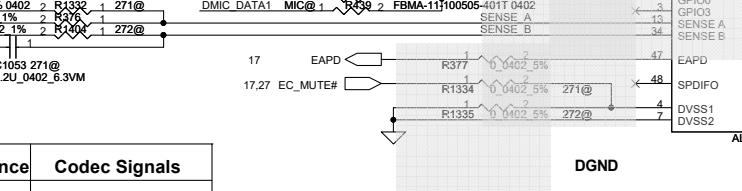
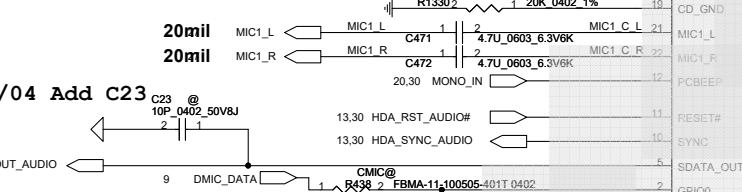
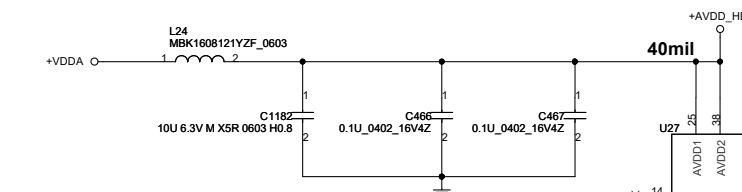
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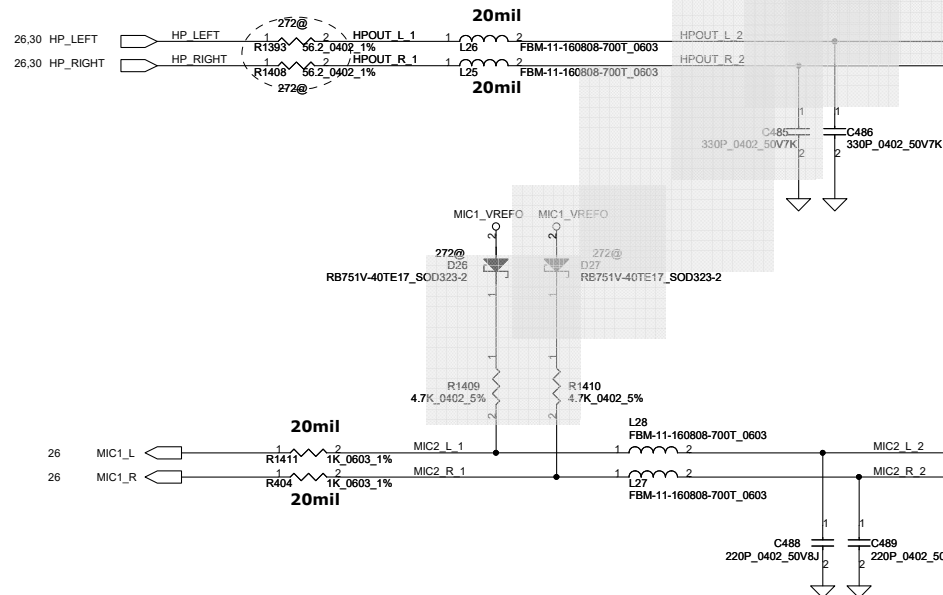
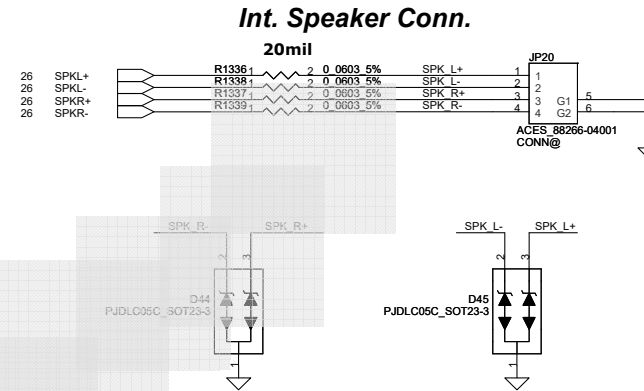
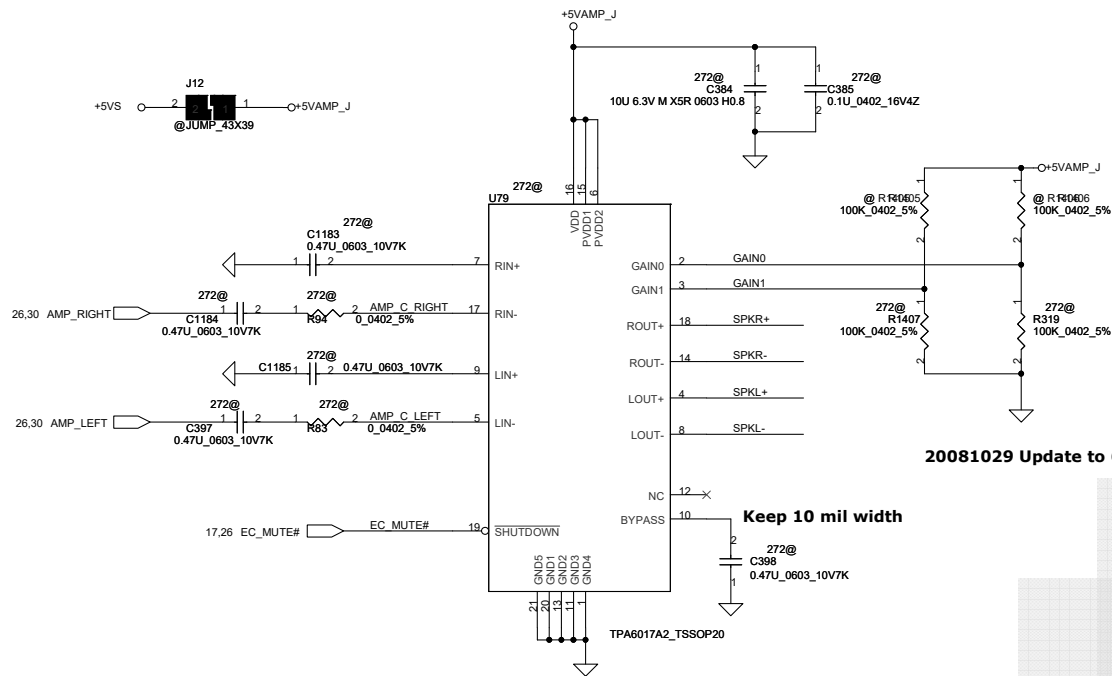
HD Audio Codec



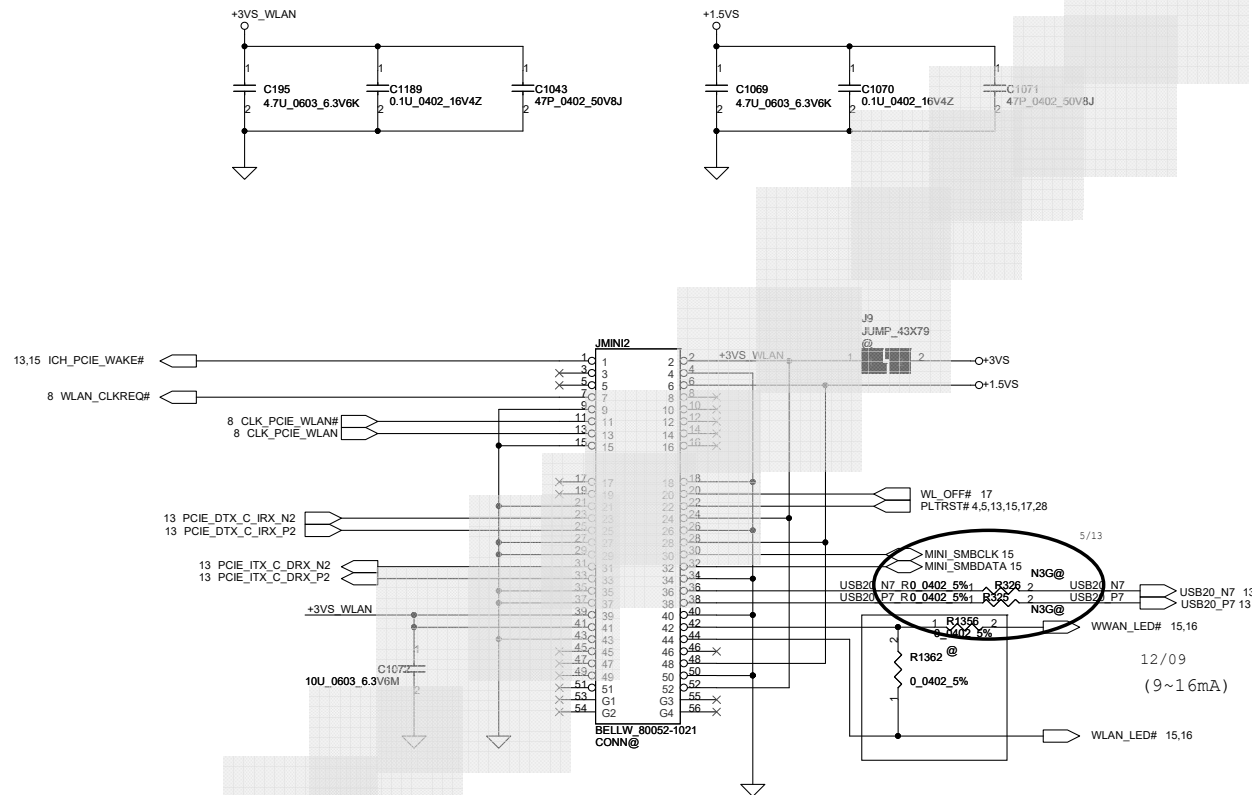
Sense Pin	Impedance	Codec Signals
SENSE A	39.2K	PORT-A (PIN 39, 41)
	20K	PORT-B (PIN 21, 22)
	10K	PORT-C (PIN 23, 24)
	5.1K	PORT-D (PIN 35, 36)
SENSE B	39.2K	PORT-E (PIN 14, 15)
	20K	PORT-F (PIN 16, 17)
	10K	PORT-G (PIN 43, 44)
	5.1K	PORT-H (PIN 45, 46)

Change to SA00002CI20 ALC272-VA2-GR

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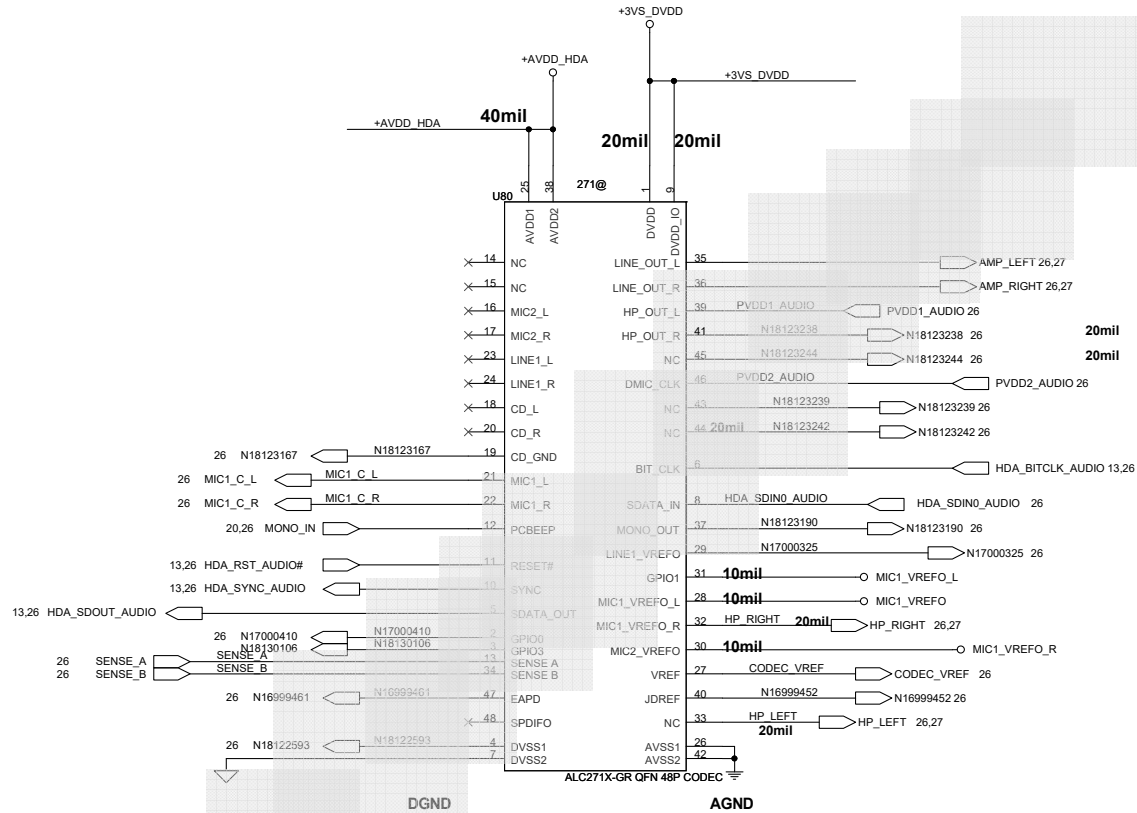
Mini-Express Card for WLAN



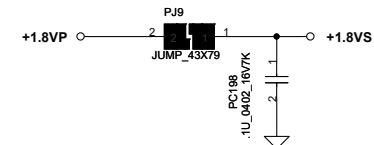
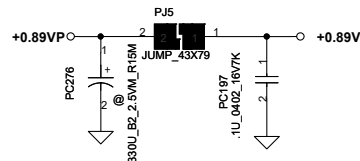
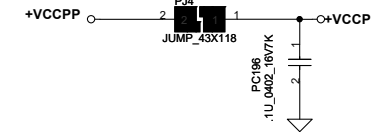
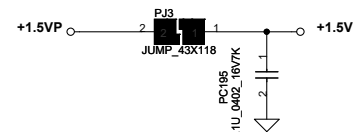
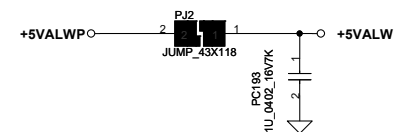
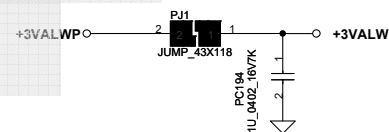
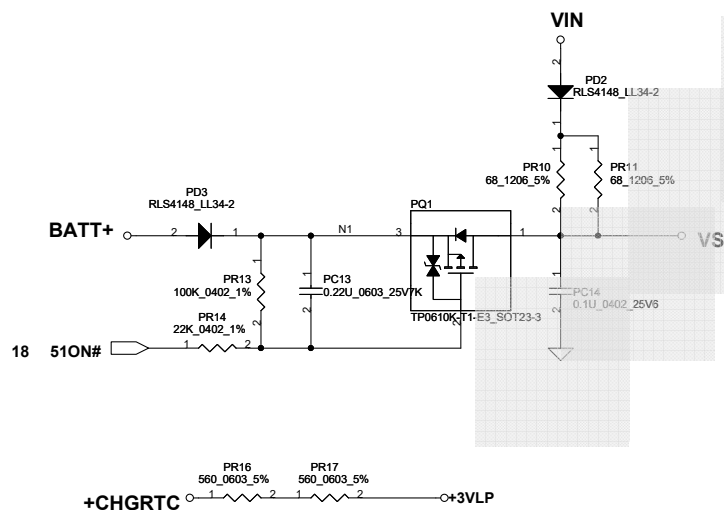
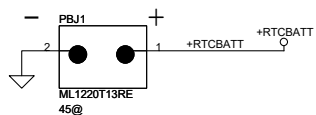
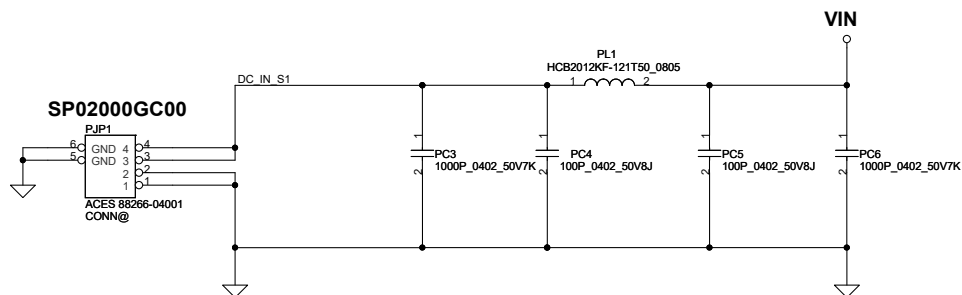
5/12 Update WLAN connector (the same as KAV60)
 6/1 Revised 37, 39, 41, 42, 43 to NC
 6/12 Update connector to DC040006S00
 6/26 Update JMINI1 footprint
 7/01 update pin 23,25,31,33

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HD Audio Codec

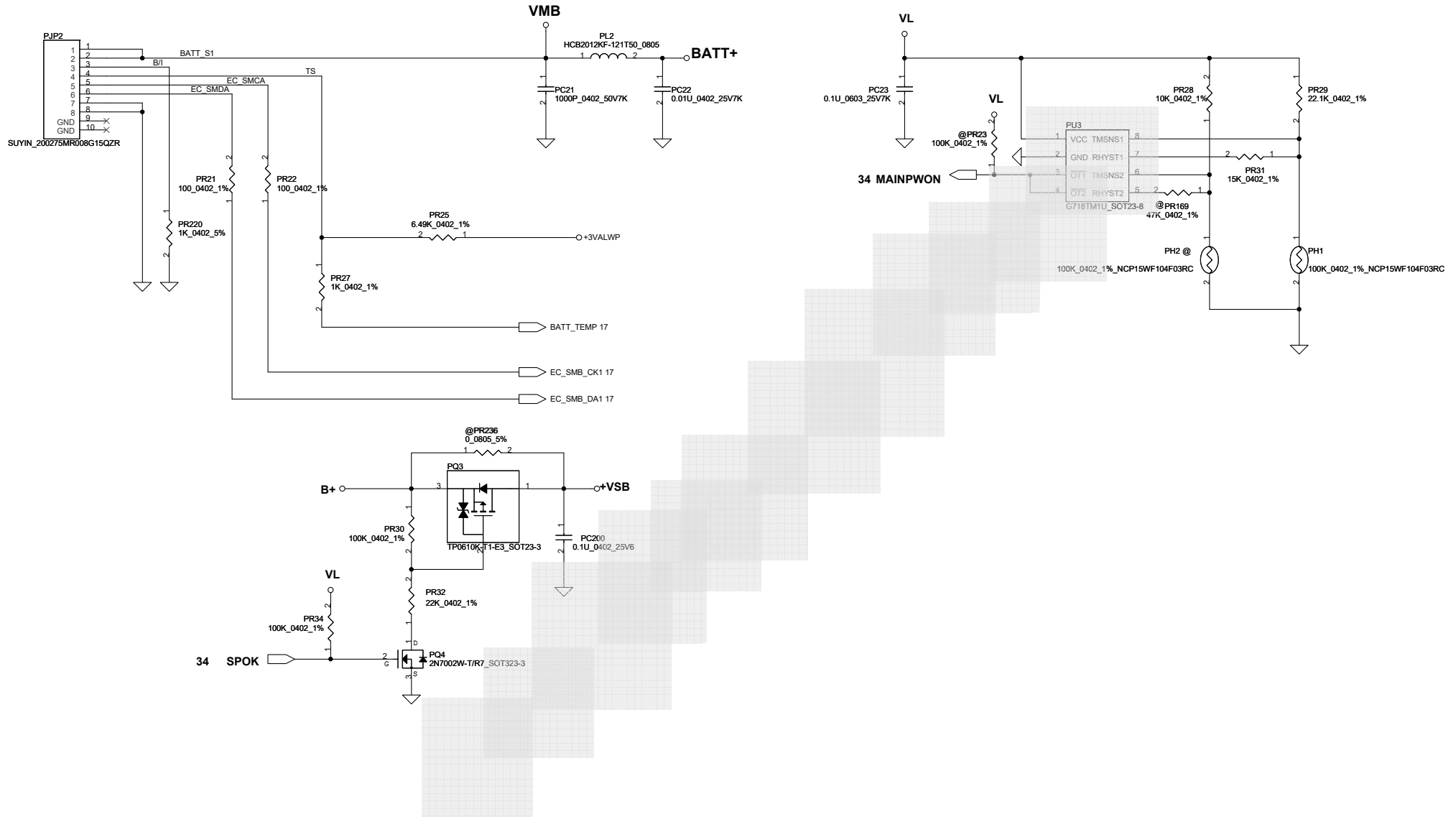


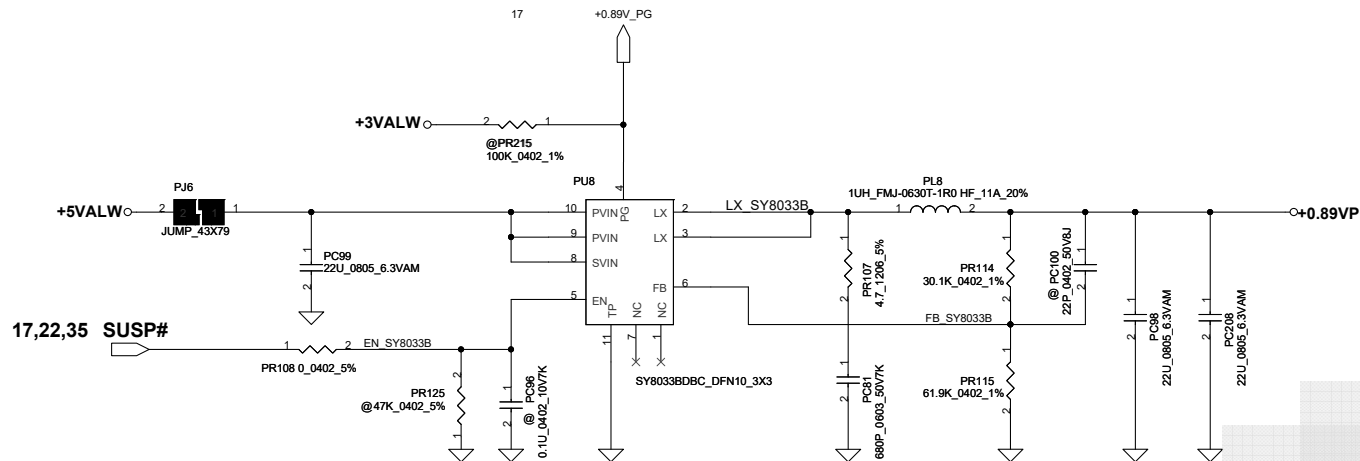
Sense Pin	Impedance	Codec Signals
SENSE A	39.2K	PORT-A (PIN 39, 41)
	20K	PORT-B (PIN 21, 22)
	10K	PORT-C (PIN 23, 24)
	5.1K	PORT-D (PIN 35, 36)
SENSE B	39.2K	PORT-E (PIN 14, 15)
	20K	PORT-F (PIN 16, 17)
	10K	PORT-G (PIN 43, 44)
	5.1K	PORT-H (PIN 45, 46)



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PH1 under CPU botten side :
CPU thermal protection at 92 degree C
Recovery at 72 degree C

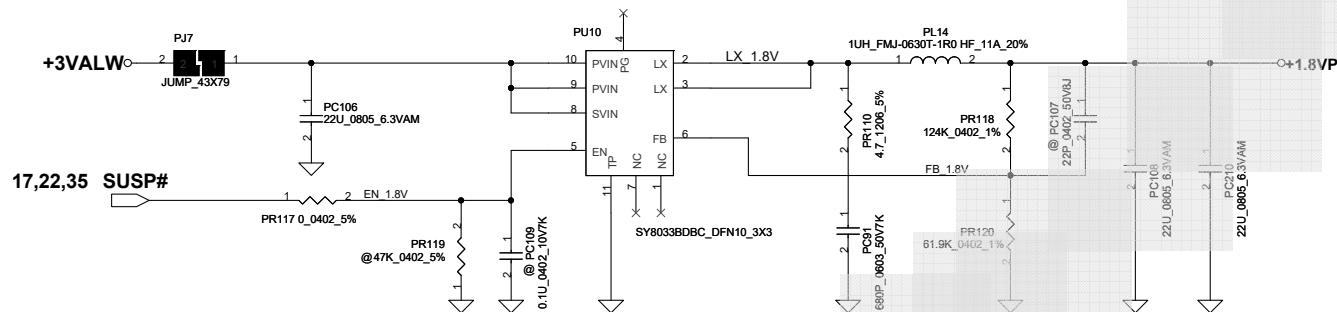




$$\langle V_o = 0.89V \rangle \quad V_{FB} = 0.6V$$

$$V_o = V_{FB} * (1 + PR114 / PR115) = 0.6 * (1 + 30.1K / 61.9) = 0.89V$$

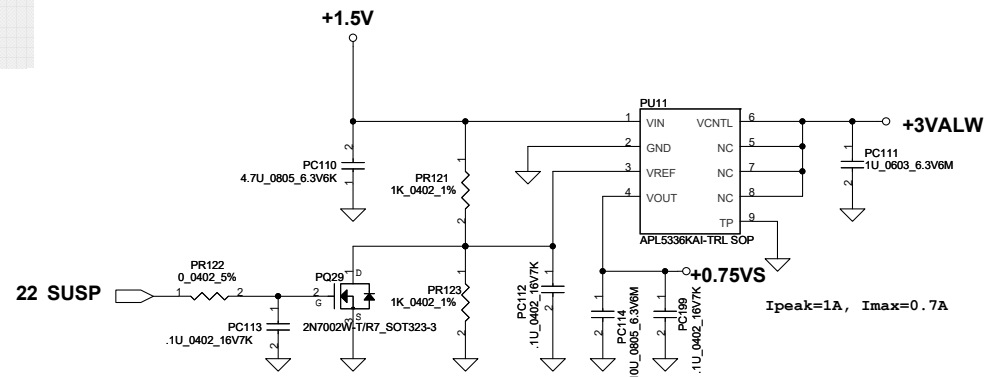
$$I_{peak} = 2.64A, \quad I_{max} = 1.848A$$



$$\langle V_o = 1.8V \rangle \quad V_{FB} = 0.6V$$

$$V_o = V_{FB} * (1 + PR118 / PR120) = 0.6 * (1 + 124K / 61.9) = 1.8V$$

$$I_{peak} = 0.318A, \quad I_{max} = 0.223A$$



$$I_{peak} = 1A, \quad I_{max} = 0.7A$$

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Version change list (P.I.R. List)

Page 1 of 1 for PWR

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	Date	Phase
1	Improve +1.8V efficiency	Fellow acer SPEC	0.1	36	Change PU10 from SA000034S00 (APL5930KAI-TRG_S08) to SA00003RU00 (SY8033BDBC_DFN10_3X3)	2010.1.25	EVT
2	Change +1.5V power good signal pull high voltage and net name	For HW request	0.1	35	Change +1.5V power good signal pull high voltage from 3.3V to 1.5V and net name change to DDR3_PG	2010.1.26	EVT
3							
4							
5							
6							
7							
8							
9							
10							
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16							
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23							

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Version change list (P.I.R. List)

Page 1 of 1 for PWR

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	Date	Phase
1	PU4 Pin25 change to no connect	modify for 3/5V load balance(TI FAE suggest)	0.1	34		2010.1.12	EVT
2							
3							
4							
5							
6							
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<2009/4/28>
Update new power schematic,
release first version NAV50 schematic

<2009/04/29>
· Add R1182 R1183 L3 on page 9
· Change J3 to R1184 on page 13

<2009/04/30>
· Change JDIM1 to SP07F001720 on page 7
· Del SATA1 Port on page 12
· Change R51 R57 R70 R63 R317 R314 R190 to 0402 Size on page 21

<2009/05/04>
· Add WWAN CLKREQ# and R107 pull-high to +3VS on page 8
· Add CRT_DET# on page 10
· Add CRT_DET# circuit on page 13
· Add 3 LEDS on page 16
· Add BT/BTN Board CONN. on page 16
· Update TP/B CONN. to SP01000LB00 on page 19

<2009/05/11>
· Add INVT_PWM on Page 5
· Del R323 on page 5
· C74 change to 2.2U_0603 on page 6
· C267 change to 22U on page 6
· C391 change to 0.1U on page 6
· Del C67 C35 C33 C36 on page 6
· Del +LGI_VID and U71.A21 direct connect to +VCCP on page 6
· Follow Intel checklist, add R52 on FSB on page 8
· Add D5 D7 D8 on page 4
· Add R174 on page 9
· Add PCI_RST# on page 11
· Add C1115 C1114 C1116 C1117 C1118 on page 15

<2009/05/12>
· Follow Intel Layout Checklist, Add C141 on VDDSPD on page 7
· Modify SRC_CLK_PORT_LIST on page 8
· Del CLKREQ_LAN# on page 8
· Change PCIE Port list on page 13
· Change USB Port list on page 13
· Add W/L 3G SW on page 16
· Del R103 on page 18

<2009/05/13>
· Change JMINI1 to PCIE Port 3 on page 15

<2009/05/14>
· Page8 Change C174 C175 to 10U_0603

<2009/05/14>
· Update New Power schematic
· Del R376 R377 on page 8
· Del D5 D7 D8 on page 4
· Change JLVDS1 to SP010006810 on page 9
· Add D6 for EMI on page 9
· Change C1106 to C_0603 type on page 9
· Change USB_OC# on page 13
· Add USB Port2 on page 20
· Change JP11 Pin define & Add D22 on page 19
· Change C512 to 1u 0402 on page 15
· Add U29 (MEDIA_LED#) on page 16

<2009/05/19>
· Update new clock GEN co-lay schematic on page 8

<2009/06/05>
· Update new clock GEN co-lay schematic on page 8
· Follow Intel check list change C161 C165 to 27P on page 8
· Follow Intel check list change C56 to 22uF on page 6

<2009/06/08>
· Update New Power schematic 06/06 version
Page 13- a. Del R203 (pull-up GPIO6 Resister)
b. Change R1184 NU
Page 17- a. Add VGATE
b. Del R1294
c. Change D30 NU
d. Change R1295 to 0 ohm
e. Add R1309 0 ohm on EC RSMRST#
f. Pull-up LAN_WAKE# +3VALW
g. ICH_POK change to PCH_POK
h. Pull-up KB_RST# to +3VS
Page 10- a. Add R1283 R1284 , Change R247 R249 to 10 ohm
b. Add @ on U10 U11 C301 C298
c. Del C302 C300 R1281 R1287

<2009/06/10>
· Page 7- Add C116 @
· Page 22- Modify USB_OC#1_2 to USB_OC#2
· Page 17- Modify PLTRST# to PCI_RST#
· Page 17- Add @ on R1311

<2009/06/12>
· Page4 Add C314 C313 C1150 D19 on +VCC_FAN1
· Page8 Add C1145 C1146 C1147
· Page10 Move CRT_DET# from Page13 to Page10
· Page13 Add +RTCVCC circuit

<2009/06/15>
· Update New Power schematic (change PBJ1 to PJP3)
· Page 10 modify C310 C308 C303 C307 C306 C304 Bom Structure
· Page 22 Modify Hole location by (ME drawing 06/12)

<2009/06/16>
· Page7 Modify DDR Command Control Pin pull-high Resister location
· Page9 Change R577 to 0402 type

<2009/06/17>
· Update New Power schematic 06/17
· Page9 modify LVDS Conn. Pin define
· Page9 Del C1110
· Page4 Add EMI solution D38 D39 D40

<2009/06/18>
· Update New Power schematic 06/18
· Page8 modify U4 Pin define and Q31
· Page13 Add R1376, R1377
· Page15 Modify C403
· Page23 Modify H11

<2009/06/19>
· Page4 Add new signal CPU_ITP , CPU_ITP#
· Page5 ADD R1378
· Page6 ADD C1152,C1153,C1154 C1160,C1161,C1162
· Page7 DDR A D8與DDR A D9互換
· Page8 ADD R1379,R1380,U77,R1381,C1157,R1382,R1383,R1384,C1157
· Page8 DEL C390
· Page9 ADD C1156
· Page11 DEL R1322, R1154
· Page13 DEL U77, ADD C1158
· Page17 ADD C1159

<2009/06/22>
· Page22 change IO Conn. pin34 from 48M to USB_ON#
· Page10 change JCRT1 P/N to SP010906182

<2009/06/23>
· Page15 Add C1163 C1164 C1165 C1166
· Page18 change PWR/B Conn. P/N to SP01000H300
· Page22 change JUSB1 JUSE2 P/N

<2009/06/24>
· Page8 Change C1350 C1351 to 0402 type
· Page10 Add R1385 R1386 on JVGA_HS JVGA_VS

<2009/06/25>
· Page22 move some parts to I/O Board , Add the MONO_IN_R on M/B
·

<2009/06/29>
· Page16 Change JP24 to ACES 88266 05001
· Page15 Change JMINI1 to FOX_AS0B246-S50U-7F_52P-T

<2009/06/30>
· Page18 Change PWR_LED# to PWR_PWM_LED#
· Page17 Add PWR_LED_DETECT_PIN on Pin97

<2009/07/02>
· Update New Power schematic 07/02
· Page9 Add C1167 C1168 for RF request.
· Page13 Change R223 to 100K
· Page16 change JP24 to ACES 85201-0505N
· Page17 Del R1387 R1388 on EC Pin97
· Page17 Add New Board ID to separate NAV50 NAV60
· Page17 Change 展頻 IC to SA00003J400 (New)
· Page18 Add D41 for ESD

<2009/07/03>
· Page18 Add D41.2 to PWR_PWM_LED#
· Page8 Change co-lay net name to +1.5VM_CK505
· Page20 Change JP2 Pin42 to +5VS

<2009/07/06>
· Page18 Add pwr switch for NAV50

<2009/07/08>
· Page5 Add 470pf on H_SMI# for known issue.

<DVT START>

<2009/08/04>
· Page5 CLK_CPU_HPLCLK CLK_CPU_HPLCLK# exchange
· Page9 Change JLVDS1 to P/N ACES 88341-3001 30P
· Page17 del PM 1.8V(U6.82) , Del R1310 R1311
· Page18 Del D41

<2009/09/03>
· Page7 Change C112 to 0402 type
· Page8 Add T6 on CLK_48M_CR
· Page16 Modify JP18 Pin define change +5VALW +5VS to +3VALW +3VS
· Page20 Change Pin 18, 23 to +1.5VS change Pin7 , 9 to USB20_P7 N7
· Page21 Del H12

<2009/09/08>
Update Power schematic 0904
· Page18 Change R1388 to 100 ohm 0402
· Page18 Change LED1 to SC591NB5A00

<2009/09/10>
Update Power schematic 0910
· Page22 unmount Q6 Q8

<2009/10/07>
· Page4 U71 Change to SA00003M800
· Page6 R26 Change to SHI00009C00
· Page13 R152 Change to SD034200A80
· Page18 R1388 Change to SD028510A80

<2009/11/26>
· Page13 H_STP_PCI# Pull high to +3VS
· Page9 change JLVDS.27 to disconnect
· Page9 Add DMIC_CLK & DMIC_DATA in JLVDS
· Page20 Add DMIC_CLK & DMIC_DATA in JP2.10 & JP2.12
· Page15 Change R504 Bom structure to @ .

<2009/11/30>
· Page21 change H20, H11 footprint

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If I have revised anything ,
I will high light by pink word and pink frame.

04/30

- (1) P2 Add CLK_SD_48M

05/05

- (1) P2 update compal number
- (2) P2 Add USB_OC#1_2 and USB_ON# to net
- (3) P3 Add U2 circuit
- (4) P3 Add IC(U2)
- (5) P4 Revised 0 ohm
- (6) P5 Add digital MIC

05/12

- (1) P2 Update information about aperture
- (2) P2 Update USB connector
- (3) P3 Revised net name
- (4) P5 Revised circuit
- (5) P7 Upate RJ45 connector (the same as KAV60)
- (6) P8 Update WLAN connector(the same as KAV60)

05/14

- (1) P3 Deleted JUSB2
- (2) P7 Update RJ45 connector(the same as NAV20)

05/20

- (1) P2 Update connector pin name

06/01

- (1) P7 Update circuit
- P8 Revised 37、39、41、42、43 to NC

06/12

- (1) P8Update connector to DC040006S00

06/19

- (1) P6 Update Headphone connector to SP010906161
- (2) P6 Update MIC connector to SP010906161

06/23

- (1) P3 Update USB connector to SP010906181
- (2) P2 Add connector(JP21)_delete

06/26

- (1) P2 Add circuit
- (2) P8 Update JMINI1 footprint

07/01

- (1) P2 Update pin number 8,10,14,16,20,22
- (2) P8 update pin 23,25,31,33

07/02

- (1) P2 Update PIN42 to +5VS
- (2) P7 Add R42

07/02/B

- (1) P4 Update JP1

07/04

- (1) P5 Add C23

08/06

- (1) P4 Add R32、R33
- (2) P4 Add R1347、R1348

10/07

- (1) P3 DEL R1, R2 ; ADD L1
- (2) P4 Change C1065, C1068 to SE071270J80
- (3) P5 Change R441, R438 C1068 to SM01000FO00
- (4) P6 D29, D30 NU

12/01 delete JP4.2、JP4.3

12/01 move D17 C603 C604 to page 2

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