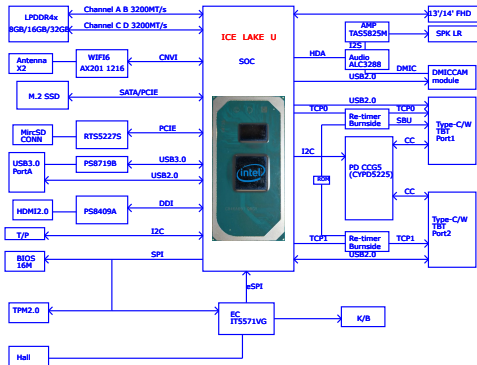
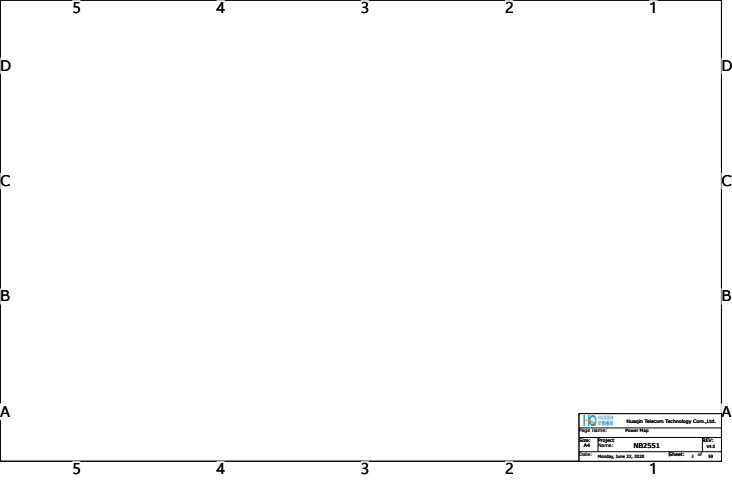



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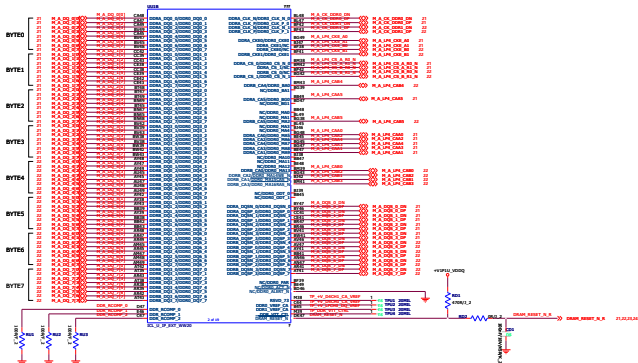
NB2551_M/B Schematics Document
Intel ICL Lake U-Processor with LPDDR4x
REV3.0
2020-01-19

UX325JA/425JA Block Diagram

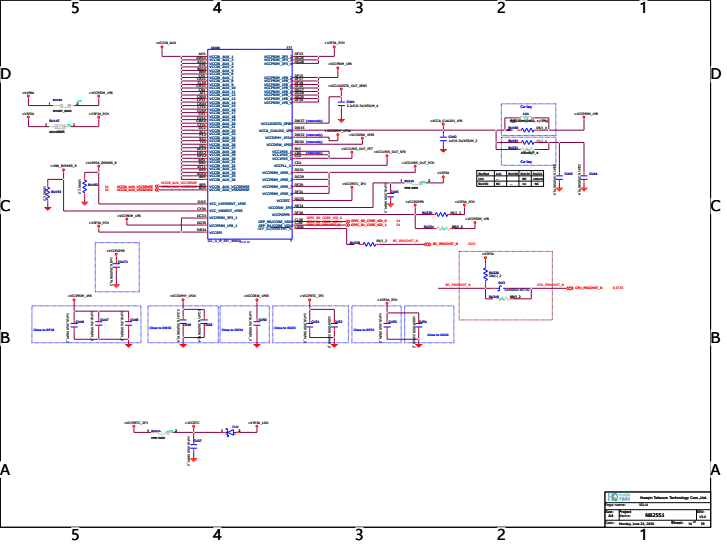


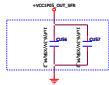
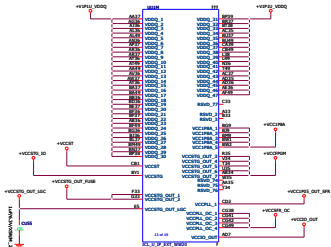


		Huaqin Telecom Technology Co., Ltd.	
Page Title:		Power Map	
Size: A4	Project Name:	NB2551	REV: V4.0
DATE: Monday, June 23, 2020	Sheet: 1 of 10		









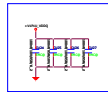
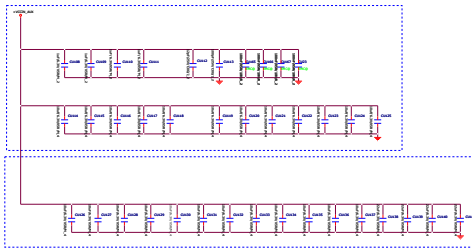
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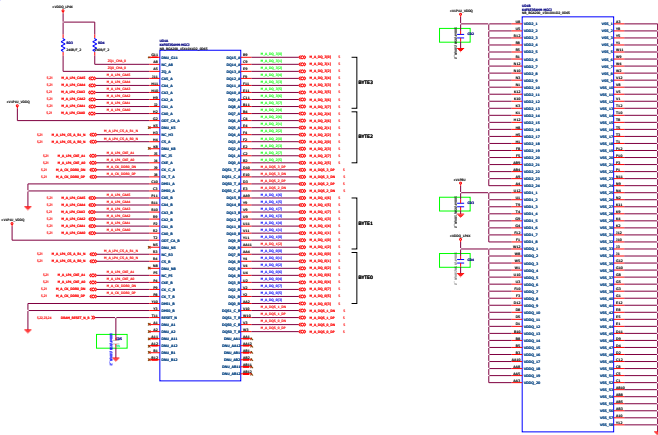
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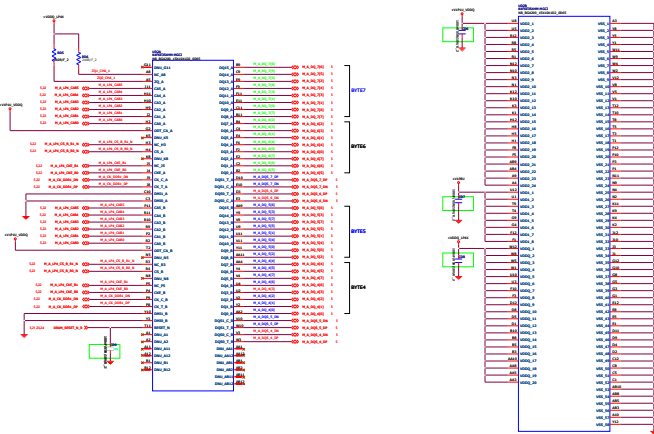
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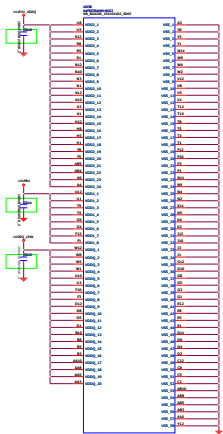
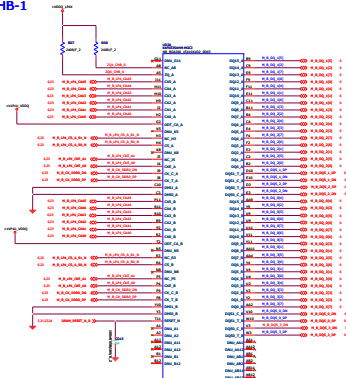
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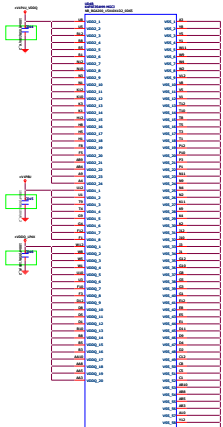
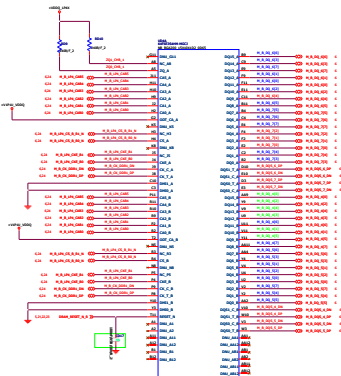
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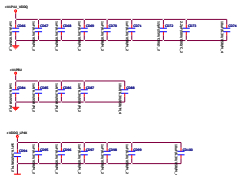
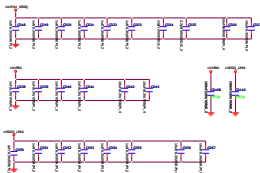




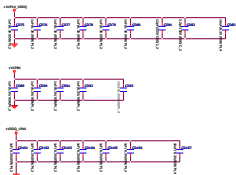
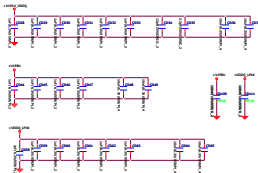


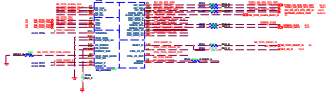
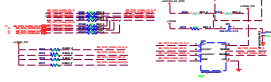
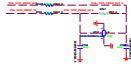
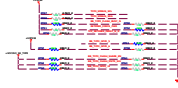
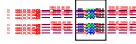


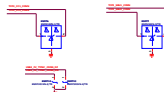
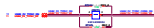
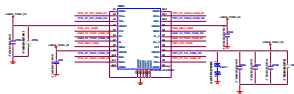
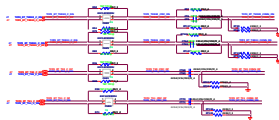
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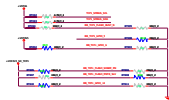
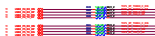
RD12 for LPDDR4x
RD11 for LPDDR4

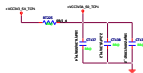
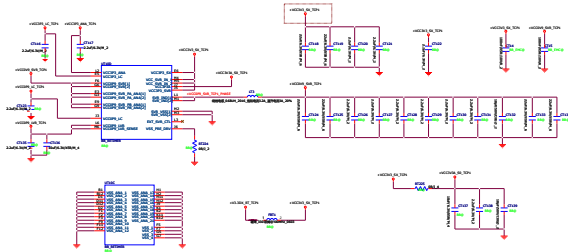
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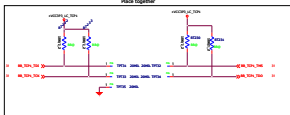


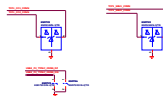
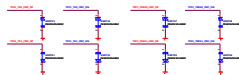
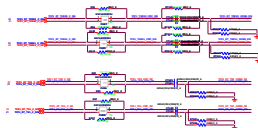


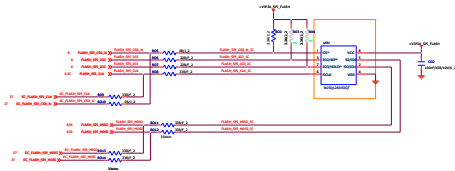


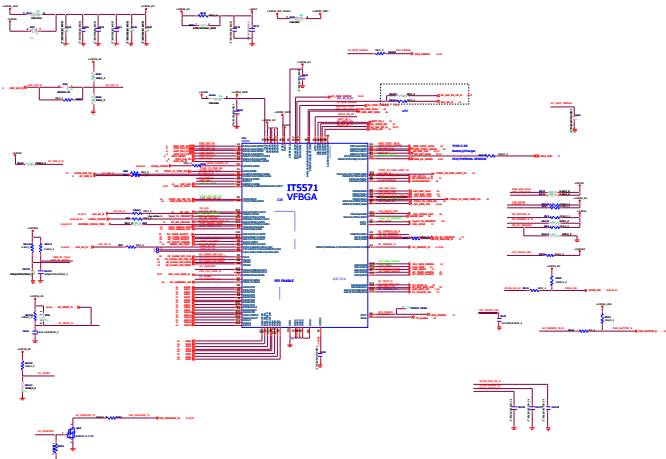


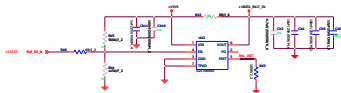
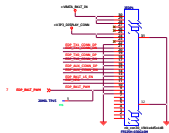
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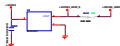




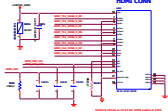




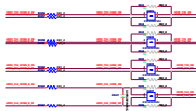
Power 1



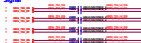
HDMI CONN



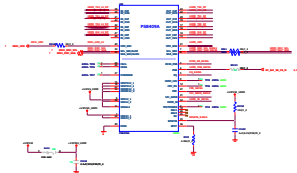
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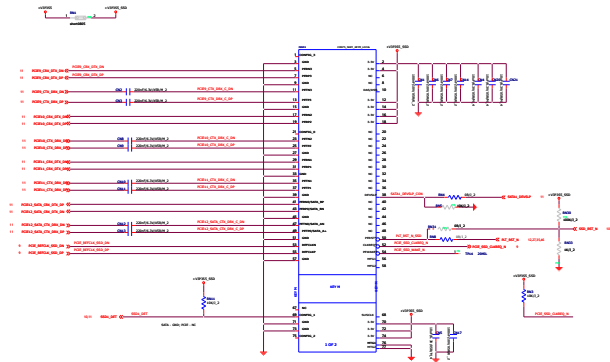


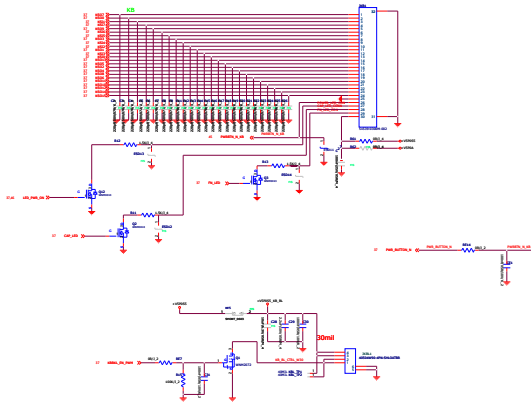
Signal



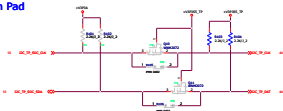
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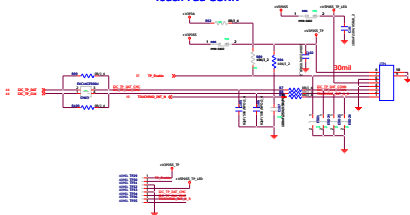


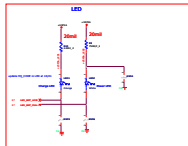
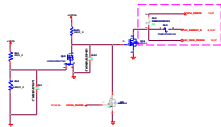
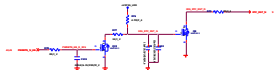


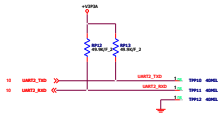
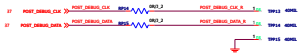
Touch Pad



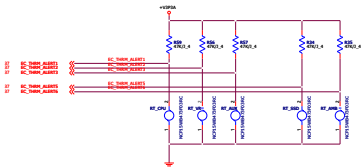
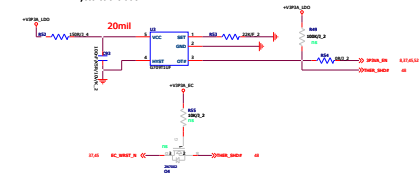
Touch Pad CONN



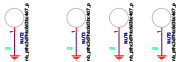




Rset(Kohm)=0.0012T*T-0.9308T+96.147, Shut down on 8dgree
Hysteresis is 30C



CPU NUTS*4



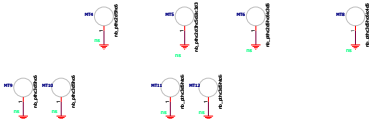
PCIE SSD

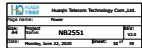


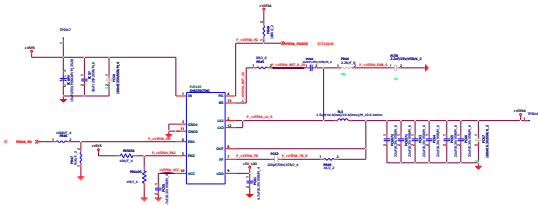
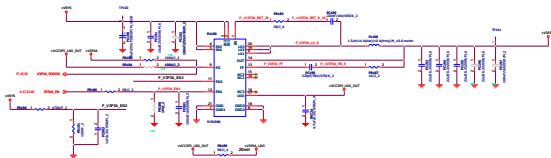
USB TO MB NUT

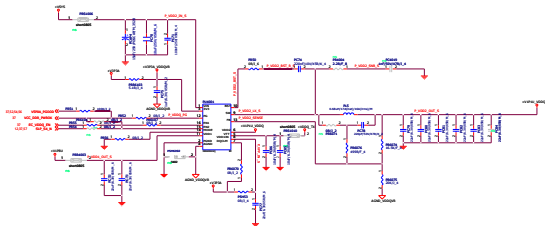


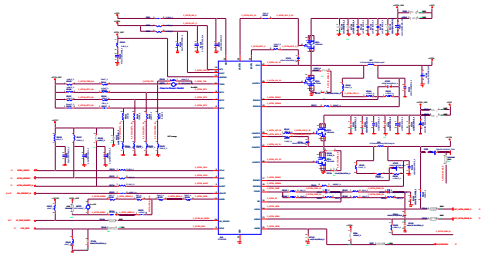
HOLE *12



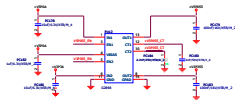
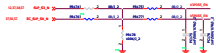
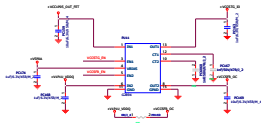
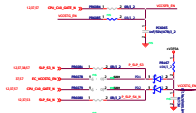
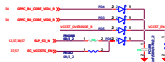
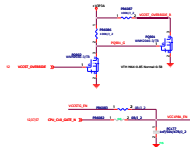
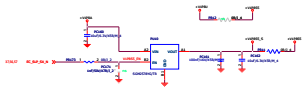


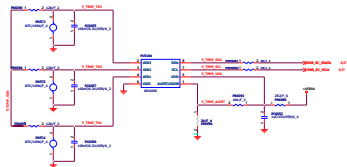












V1---V2 Change list

1. remove power button;
2. Shift K054 for EC header code;
3. CUS9 change to 0201*2 for ME request;
4. CE46 ns;
5. Update DB con pin assignment;
6. Add I24, R45;
7. R1155--R1153 Change to 0ohm;
8. Add Page 4 --Power Map Power up sequence
9. Remove CPU M01;
10. Change DB M01 P/N;
11. Delete M07 con;
12. PC452, PC454 ns;
13. Modify VC1 pin setting;
14. C5055 change to 0 ohm;
15. C5061 change to 0401;
16. Change R15 R17, R18 P/N;
17. Delete M11, M12, M13;

V2---V3 Change list

1. Shift CUS for EMC request
2. Shift C010 for EMC request
3. Reserve CUS for RF request
4. Reserve DMIC level shift
5. B2 value change to 4-ohm for LED brightness
6. Shift R1104 for HDMI St test

V4---V5 Change list

1. V3P3A Power IC change to Silergy SY8288BRAC
2. DDR Power IC change to MPS NB688AGQ-Z