

# B550 AORUS MASTER

PAGE	TITLE	Revision : 1.01
01	COVER SHEET	
02	BOM & PCB MODIFY HISTORY	
03	BLOCK DIAGRAM	
04	CPU DDR4 MEMORY	
05	CPU CONTROL	
06	CPU GFX, GPP, SB, GND	
07	CPU ACPI/GPIO/USB/AUDIO	
08	CPU POWER & GND	
09	CPU CLK/SPI/USB	
10	DDR4 CHANNEL A	
11	DDR4 CHANNEL B	
12	PM CLK/GPIO/FAN	
13	PM USB	
14	PM UMI/GPP/SATA	
15	PM POWER & GND	
16	PCI EXPRESS x16	
17	HDMI	
18	80 PORT , TPM	
19	F_USB30 , R_USB30 , F_USB20	
20	A_VDD1V8 / A_VDDPS5 / FAN CONNECT	
21	ALC1150 CODEC	
22	AUDIO JACK	
23	ATX , FRONT PANEL	
24	POWER SEQUENCE	
25	PWM IR35201	

[illegible]


<http://www.repair1.ru/>

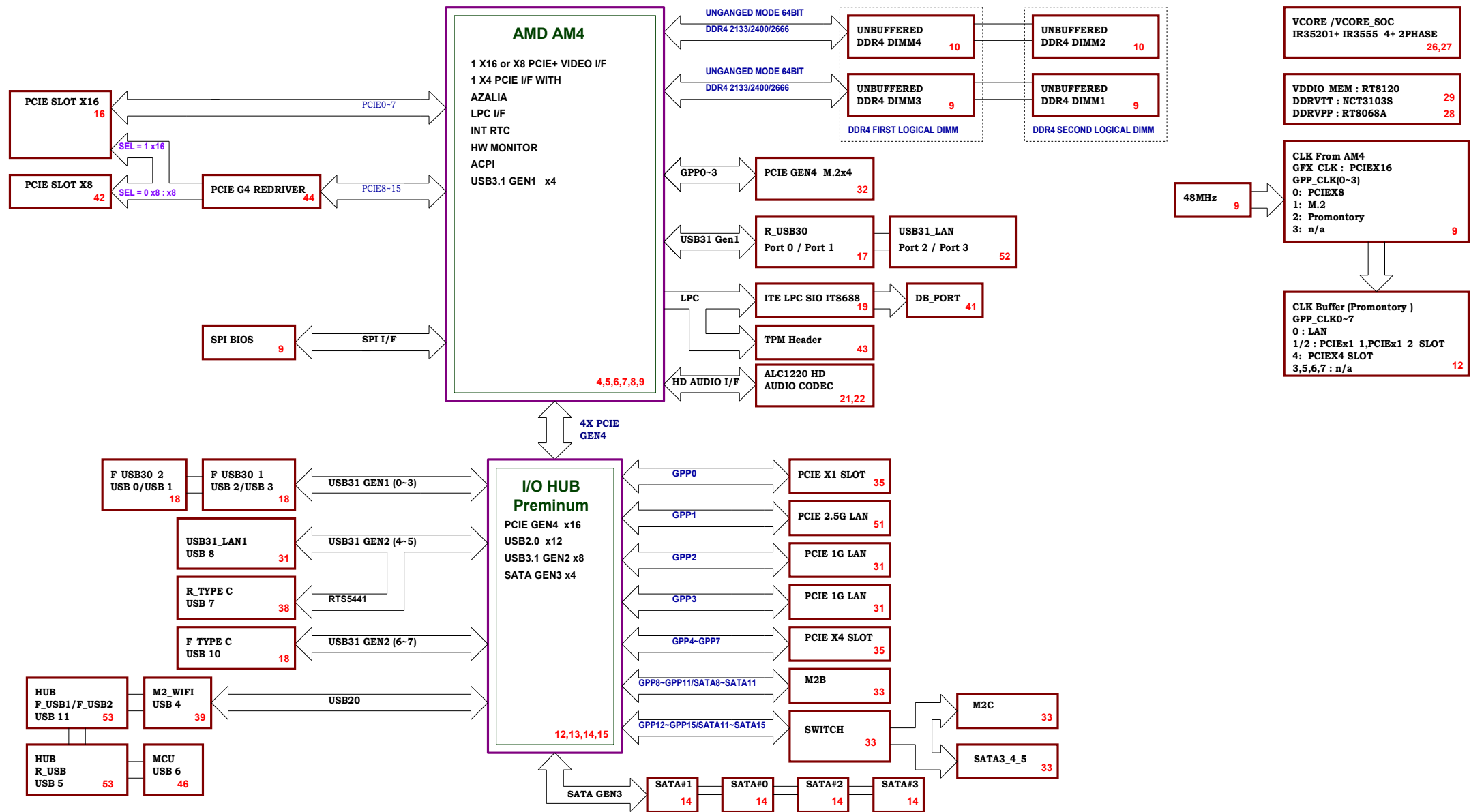
5	4	3	2	1
---	---	---	---	---

**P-Code: U98126-0**

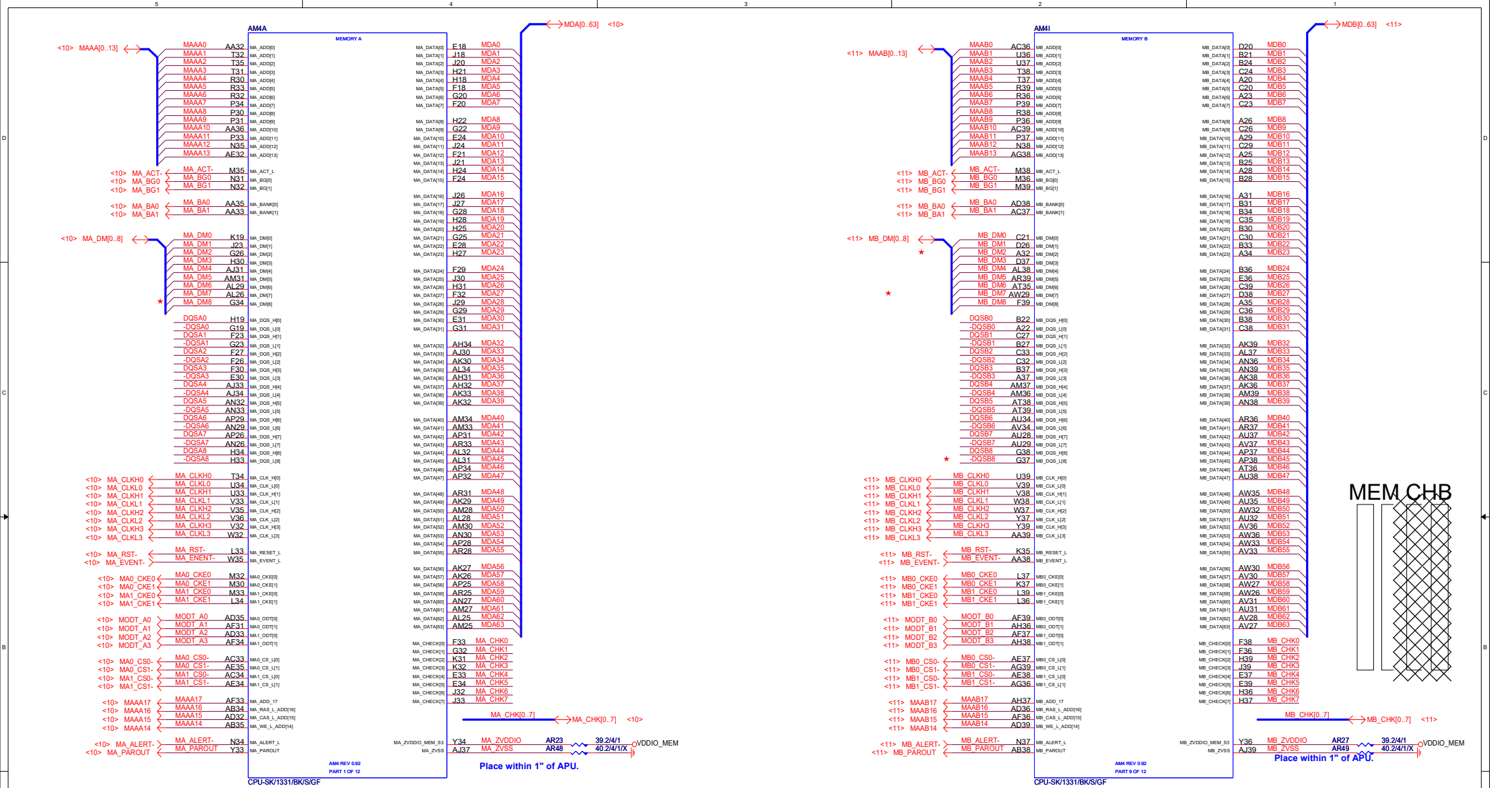
	SPI_CLK	LFRAME-	SYS_RST-	LPC_CLK0	LPC_CLK1
PULL HIGH	Internal clock mode	SPI ROM	Normal reset mode	PSP modify SPI page reg bits[24:24]	Use 48MHz crystal clock
PULL LOW	Extal clock mode	LPC ROM	Short reset mode	PSP not modify SPI page reg bits[24:24]	Use 100MHz extl clock input.

[illegible]

			
Title			
BOM & PCB HISTORY			
Size	Document Number	Rev	
Custom	B550 AORUS MASTER	1.01	
Date:	Monday, May 11, 2020	Sheet	2 of 56



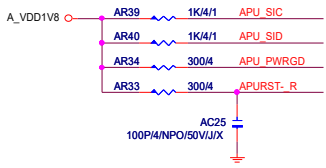
<http://www.repair1.ru/>



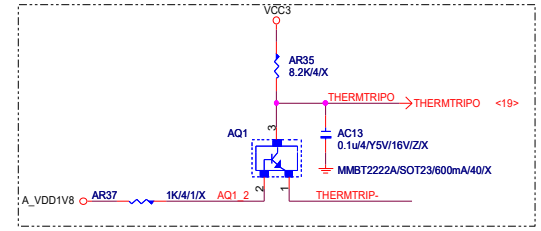
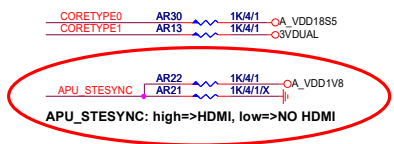
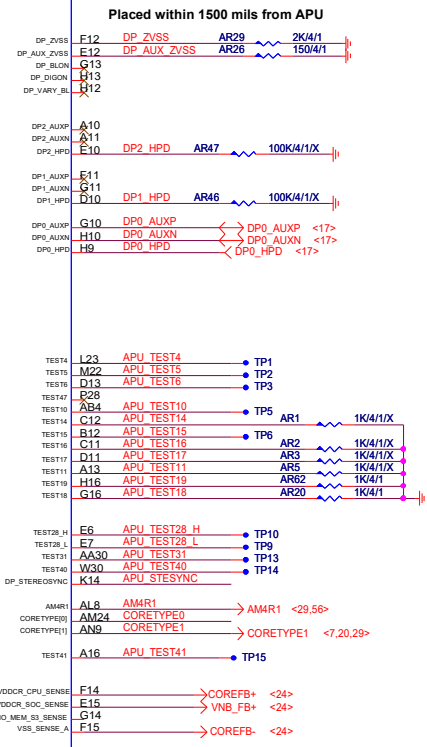
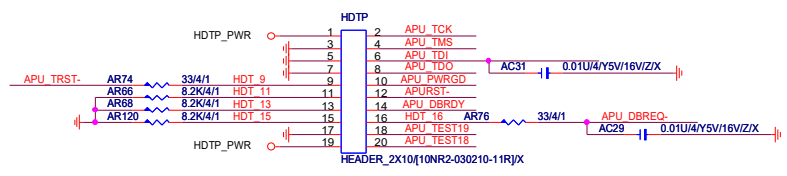
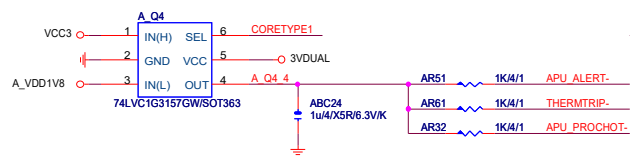
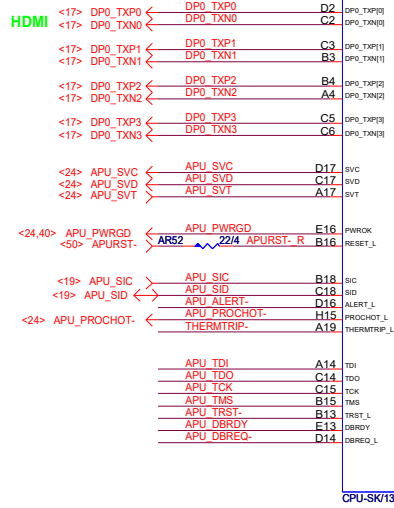
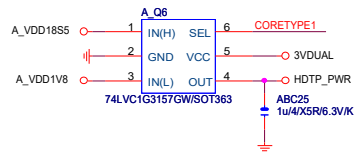
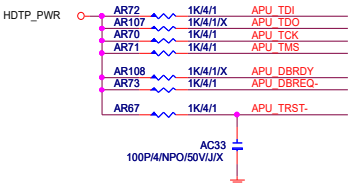
<http://www.repair1.ru/>

**GIGABYTE**

Title			APU DDR4		
Size		Document Number		Rev	
Custm		B550 AORUS MASTER		1.01	
Date:			Monday, May 11, 2020	Sheet	4 of 56



SVC	SVD	Boot voltage
0	0	1.1
0	1	1.0
1	0	0.9
1	1	0.8



CORETYPE 1	CORETYPE 0	Family / Model Numbers	AM4 APU TYPE
0 BR	0	Family 15 h / Models 60 h-6 Fh	TYPE 0
0 ST	1	Reserved	TYPE 1
1 ZP	0	Family 17 h / Models 00 h-0 Fh	TYPE 2
1 RV	1	Family 17 h / Models 10 h-1 Fh	TYPE 3

**GIGABYTE**

Title: CPU CONTROL

Size: Custom Document Number: B550 AORUS MASTER Rev: 1.01

Date: Monday, May 11, 2020 Sheet: 5 of 56

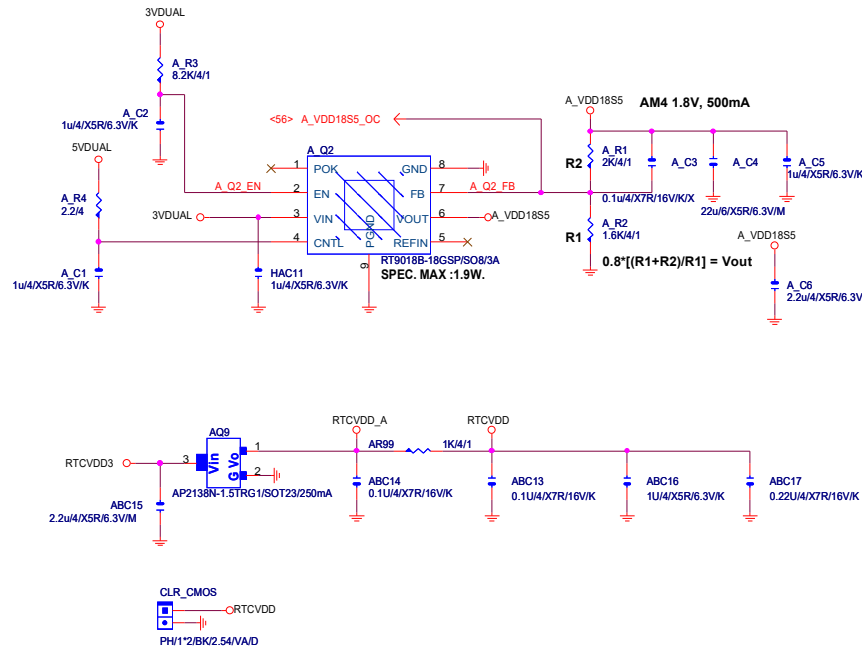
<http://www.repair1.ru/>




TEST0	TEST1	TEST2	Description
0	0	0	FCH TAP accessible from APU when TAPEN is asserted FCH JTAG pins overloaded for multiple functions, in this configuration the FCH JTAG are used as non-JTAG pins
0	0	1	Reserve
0	1	X	Reserve
1	TMS	0	FCH JTAG multi-function pins are configured as JTAG pins, in this configuration the FCH TAP can be accessed from FCH JTAG pins
1	TMS	1	Use on JTAG only, Yuba JTAG enable.

<http://www.repair1.ru/>

Title			Rev
AM4 MISC			1.01
Size	Document Number		
Custm	B550 AORUS MASTER		
Date:	Monday, May 11, 2020	Sheet	7 of 56

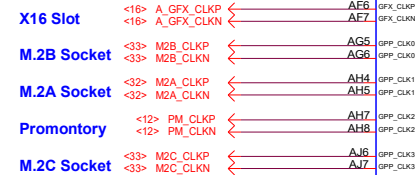


				
Title				
CPU POWER & GND				
Size	Document Number			Rev
Custom	B550 AORUS MASTER			1.01
Date:	Monday, May 11, 2020		Sheet	8 of 56



	SPI_CLK	LFRAME-	SYS_RST-	LPC_CLK0	LPC_CLK1
PULL HIGH	Internal clock mode	SPI ROM	Normal reset mode	PSF modify SPI page reg bits[24:24]	Use 48MHz crystal clock
PULL LOW	Extal clock mode	LPC ROM	Short reset mode	PSF not modify SPI page reg bits[24:24]	Use 100MHz extl clock input.

**Bule: default**



## ★1.8V SPI ROM USE



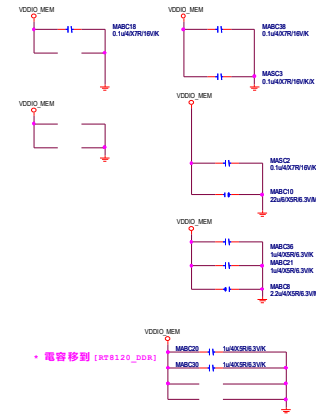
### Fix flash BIOS fail Issue 1K to 330 ohm



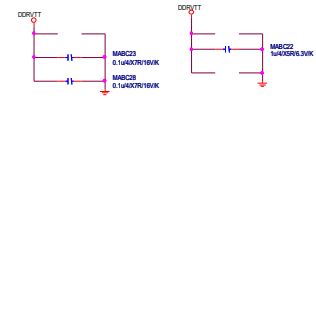


<http://www.repair1.ru/>

DDR12V Decouple



DDRVRTT Decouple

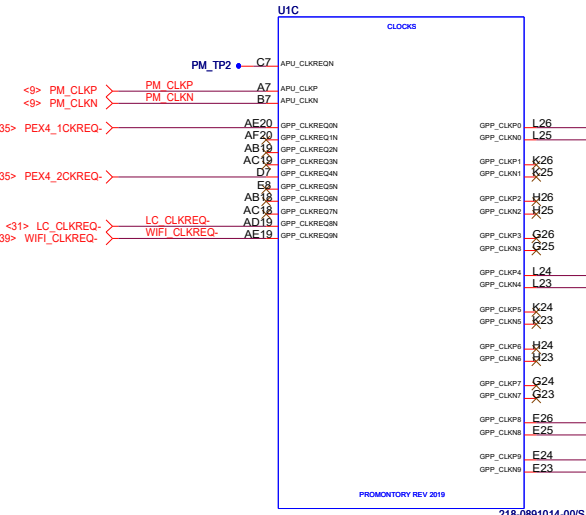


GIGABYTE		
File	DDR4 CHANNEL A	
Size	Document Number	Rev
Custom	B550 AORUS MASTER	1.01
Date	Monday, May 11, 2020	Sheet 30 of 36

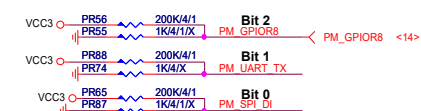
CHANNEL A0  
SA2:0=000

CHANNEL A1  
SA2:2=010

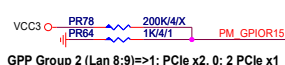




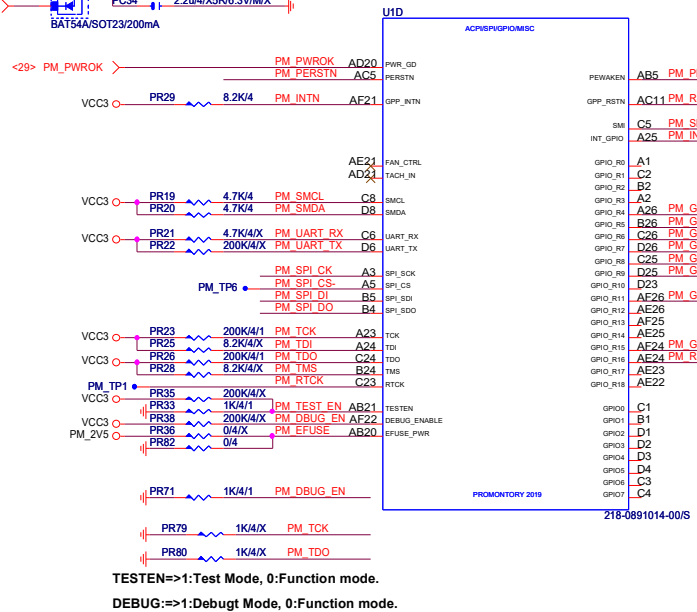
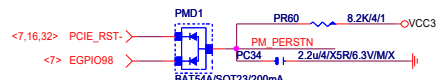
Bit 2	Bit 1	Bit 0	GPP Group 0 (Lanes 3:0)
0	0	0	4 PCIe x1
0	0	1	2 PCIe x1 + 1 PCIe x2
0	1	0	1 PCIe x2 + 2 PCIe x1
0	1	1	1 PCIe x2 + 1 PCIe x2
1	1	1	1 PCIe x4
Others: Reserved			



Bit 2	Bit 1	Bit 0	GPP Group 1 (Lanes 7:4)
0	0	0	4 PCIe x1
0	0	1	2 PCIe x1 + 1 PCIe x2
0	1	0	1 PCIe x2 + 2 PCIe x1
0	1	1	1 PCIe x2 + 1 PCIe x2
1	1	1	1 PCIe x4
Others: Reserved			



GPP Group 2 (Lan 8:9)=>1: PCIe x2, 0: 2 PCIe x1



TESTEN=>1:Test Mode, 0:Function mode.  
DEBUG=>1:Debug Mode, 0:Function mode.



GPIOR4 1: GPP clock source from APU\_CLKP/N;  
0: GPP clock source from Crystal.

GPIOR5=>1 : USBC SSC disable, 0: USBC SSC enable

GPIOR6=>1: SATA SSC disable, 0: SATA SSC enable



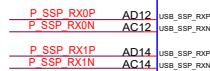
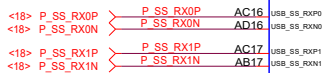
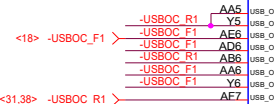
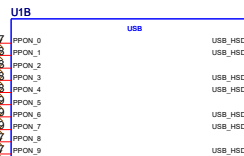
PM\_PCIE\_RST2- PR41 8.2K/4 -> VCC3



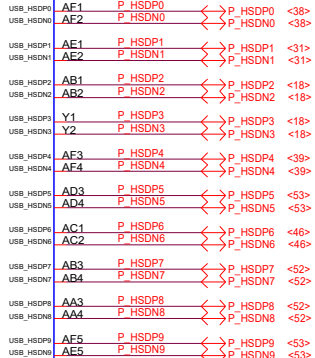
<http://www.repair1.ru/>

<b>GIGABYTE</b>				ANIS 7848371
Title <b>PROMONTORY CPU/CLK/MISC</b>				
Size	Custom	Document Number	Rev	
		<b>B550 AORUS MASTER</b>	<b>1.01</b>	
Date:	Monday, May 11, 2020	Sheet	12	of 56

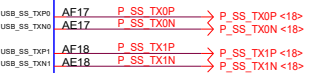
USB port power control 9:0  
(VCC3). Output.



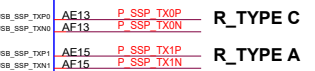
USB32G2	USB20	USB_OC
0	0	0
1	1	1
0	2	2
1	3	3
	4	4
	5	5
	6	6
	7	7
	8	7
	9	7



### USB 3.1 Gen 1



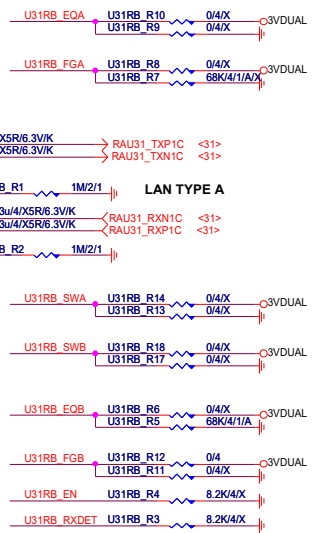
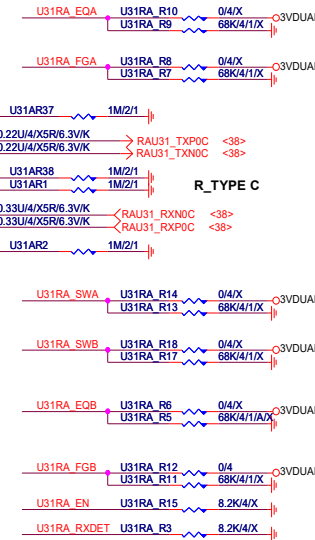
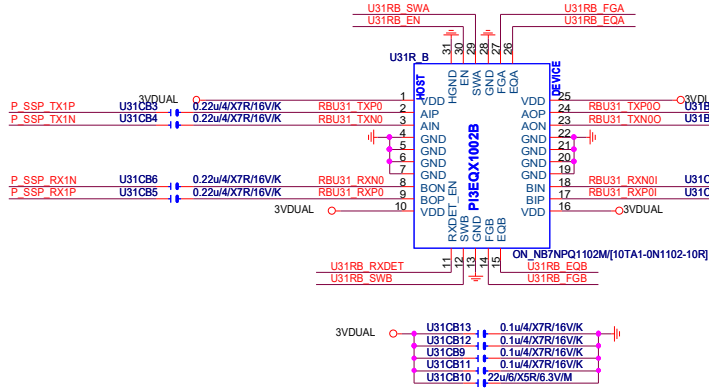
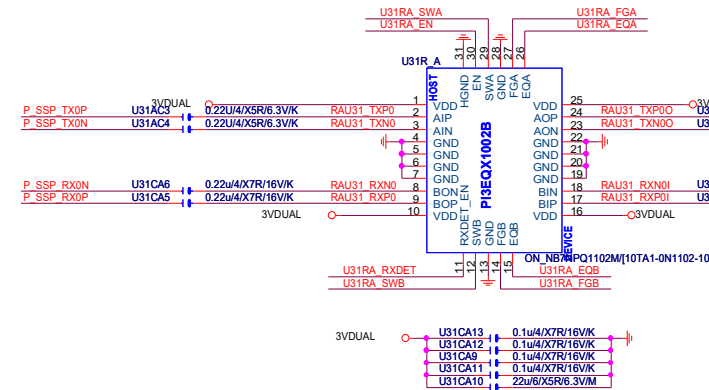
### USB 3.1 Gen 2



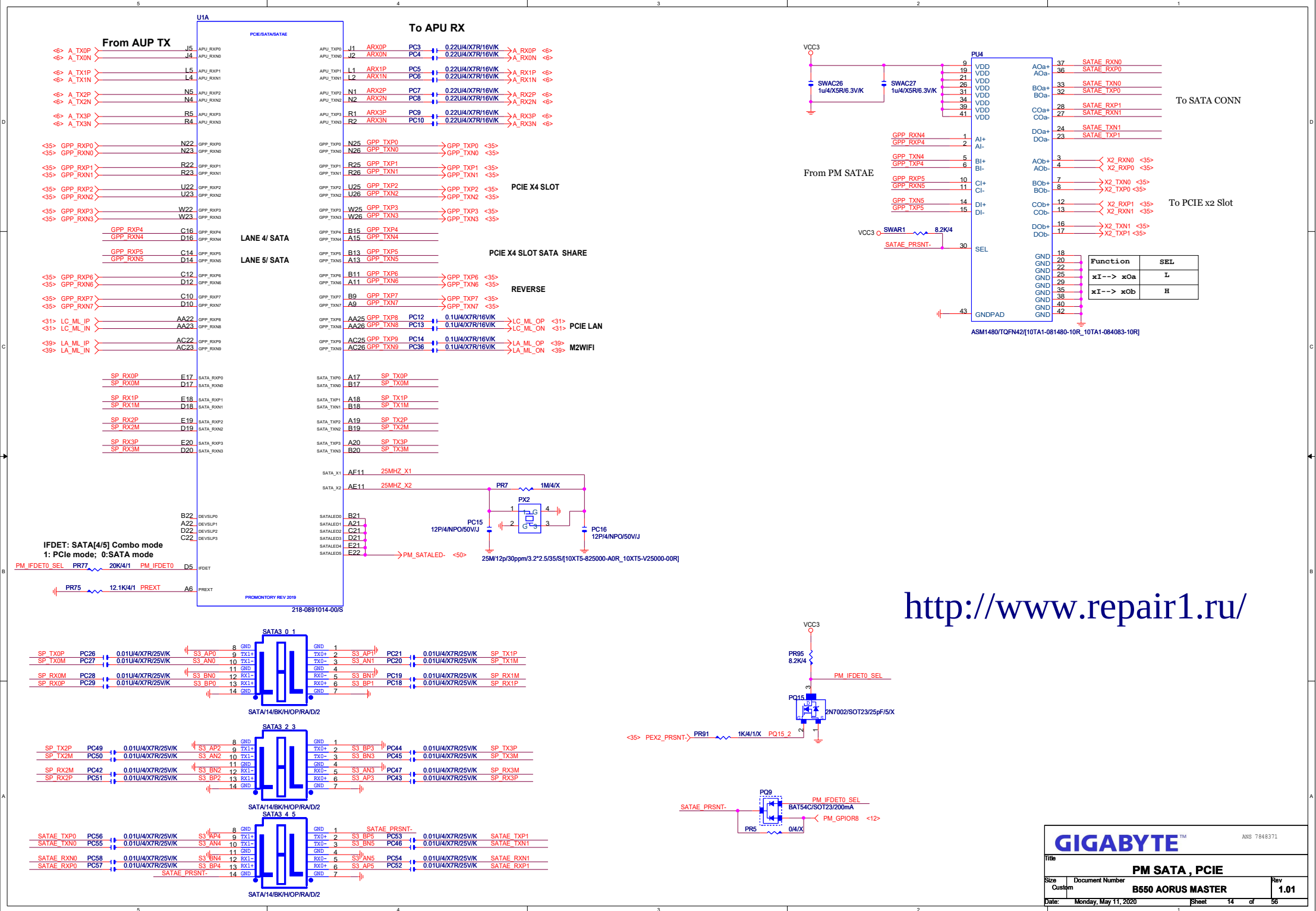
R\_TYPE C LAN  
R\_TYPE A LAN  
F\_USB30 OK  
F\_USB30 OK  
M2 WIFI OK  
R\_USB 20 HUB OK  
MCU OK  
R\_USB20 OK  
R\_USB20 OK  
F\_USB 20 HUB OK

F\_USB30 OK  
F\_USB30 OK

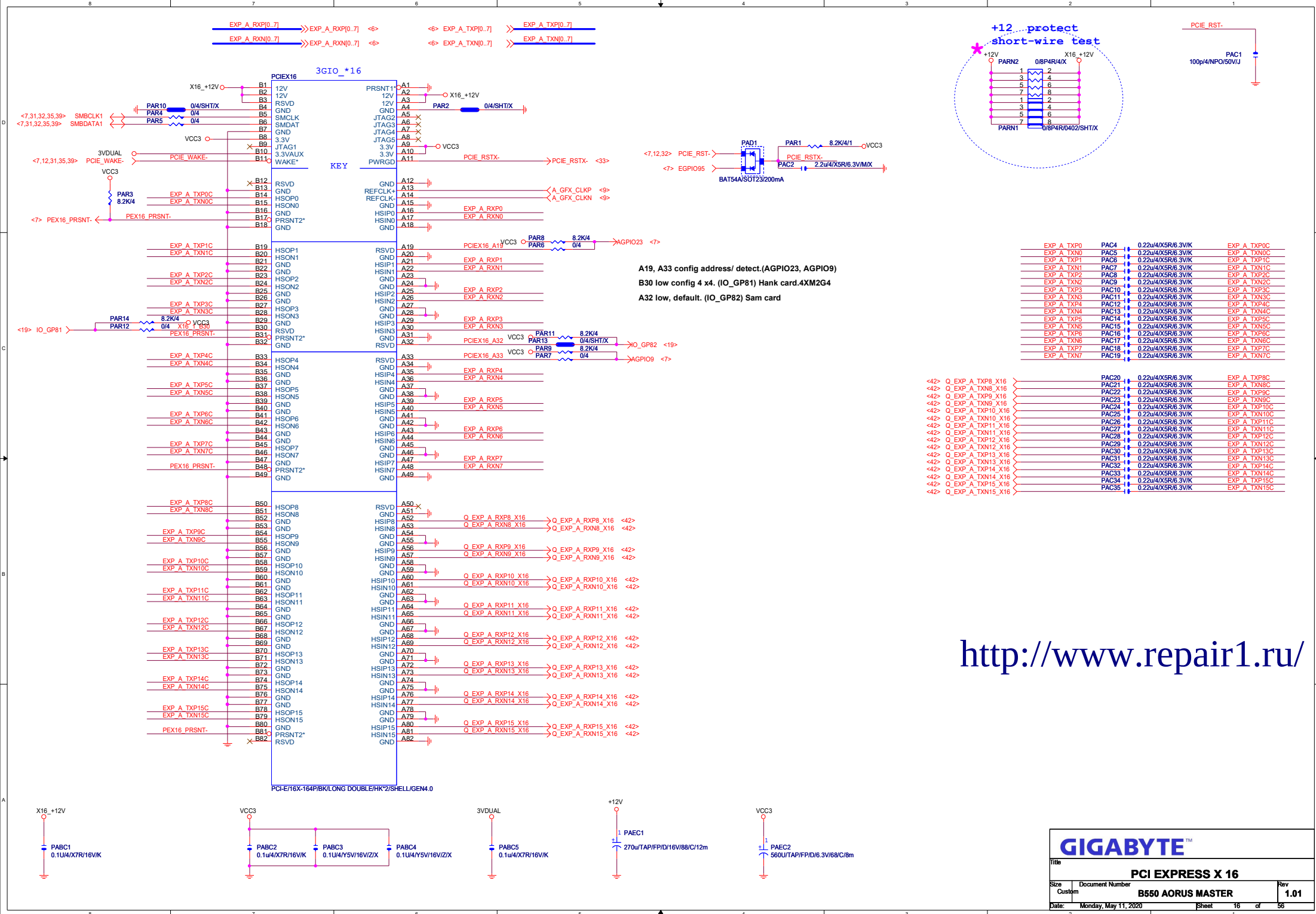
<http://www.repair1.ru/>



<b>GIGABYTE</b> ™			
ANS 7848371			
<b>PM USB</b>			
Size	Document Number	Rev	
Custm	<b>B550 AORUS MASTER</b>	<b>1.01</b>	
Date:	Monday, May 11, 2020	Sheet	13 of 56

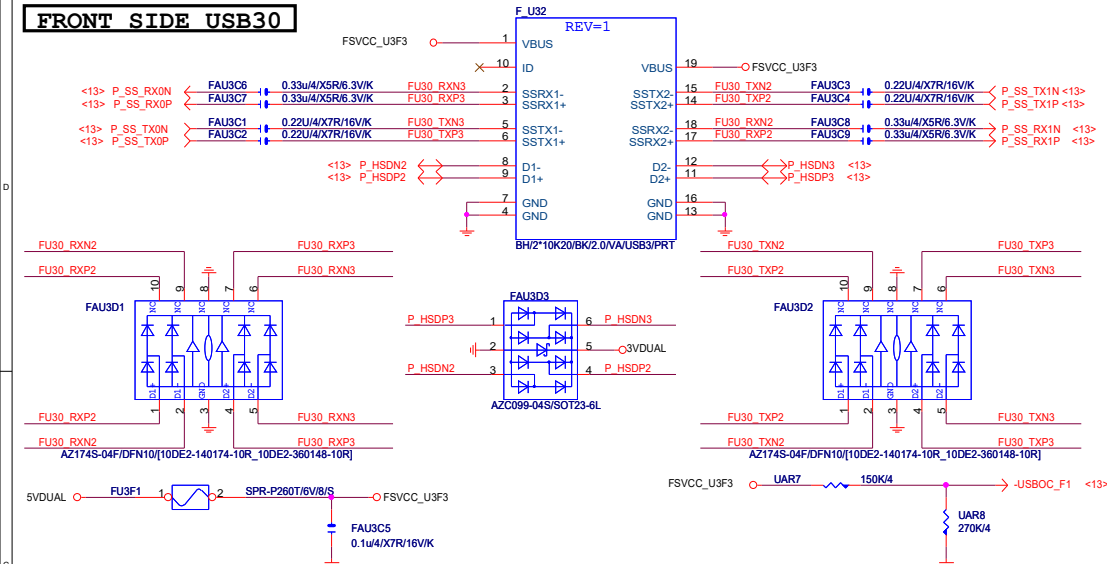




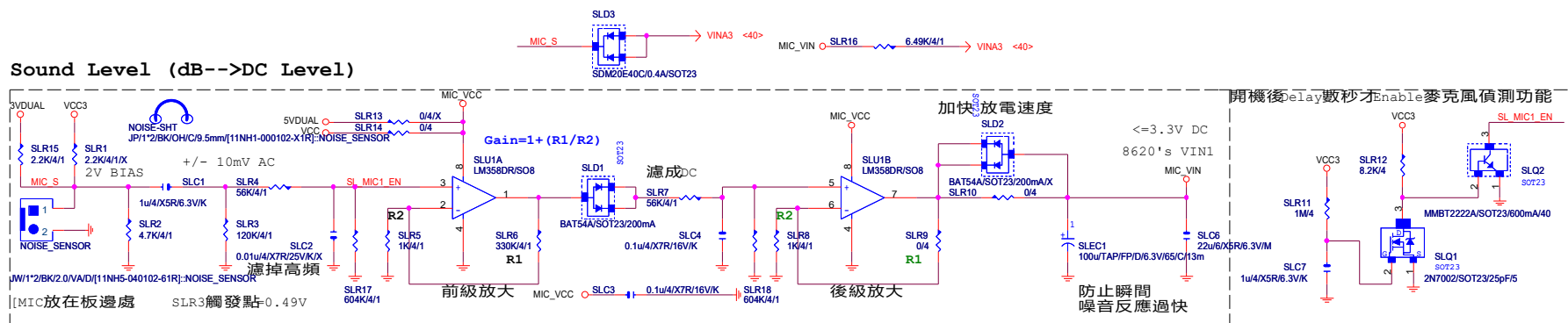




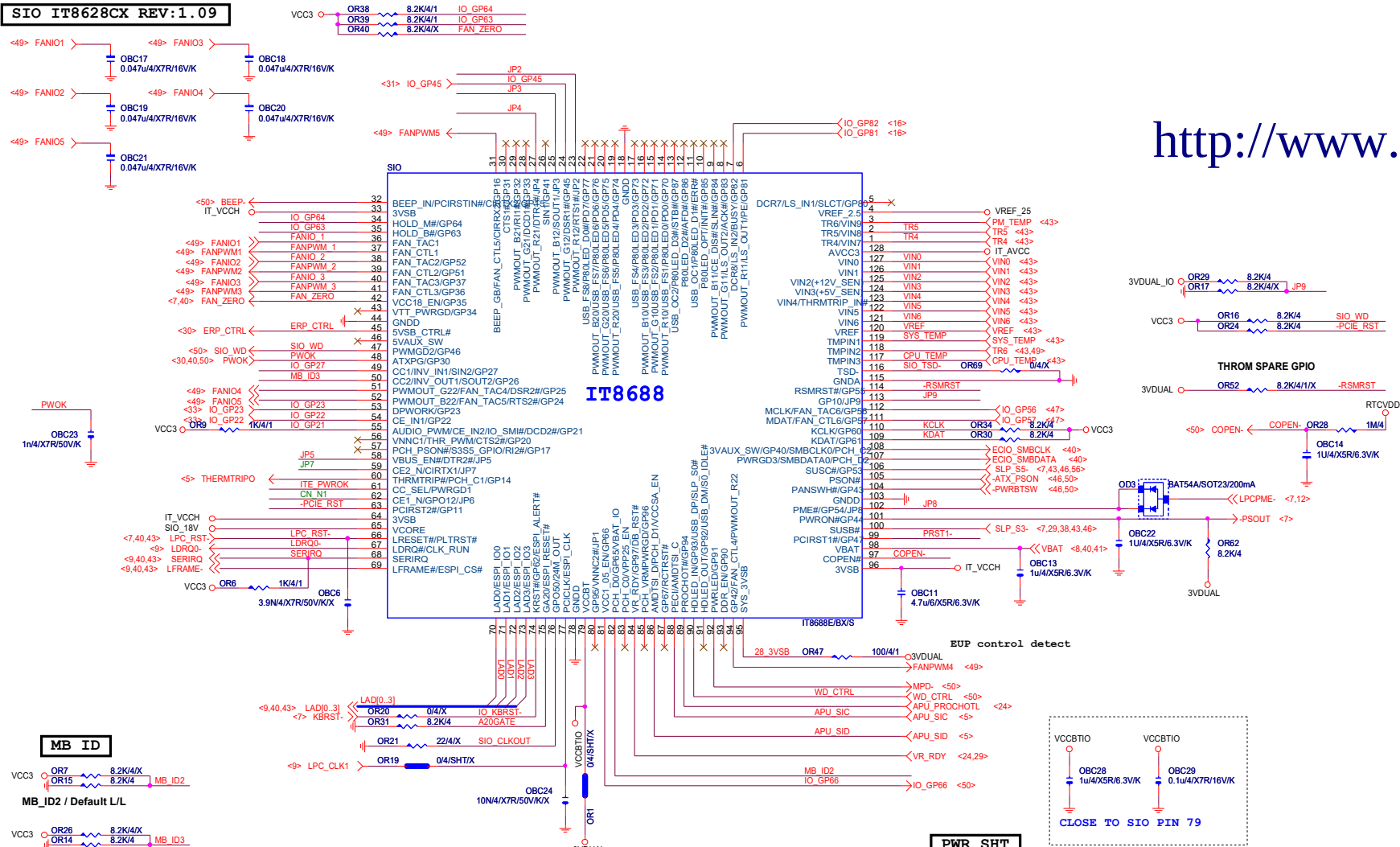
FRONT SIDE USB30



<http://www.repair1.ru/>

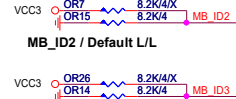


# SIO IT8628CX REV:1.09



<http://www.repair1.ru/>

## MB ID



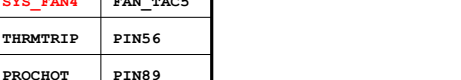
## FAN TABLE

CPU_FAN	FAN_CTL1 FAN_TAC1
SYS_FAN1	FAN_CTL2 FAN_TAC2
SYS_FAN2	FAN_CTL3 FAN_TAC3
SYS_FAN3	FAN_CTL4 FAN_TAC4
OPT_FAN or SYS_FAN4	FAN_CTL5 FAN_TAC5
THRMTRIP	PIN56
PROCHOT	PIN89

## EMI

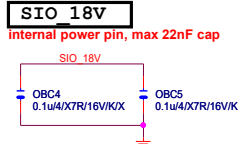


## SIO CAP



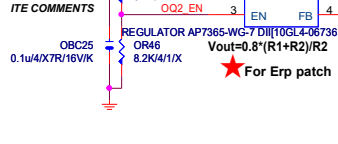
## Power leakage

### SIO 18V

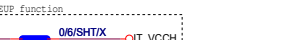


## Power leakage

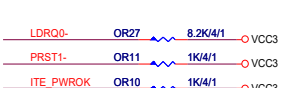
### SIO 18V



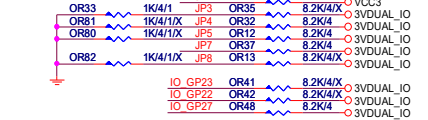
## PWR SHT



## SIO PU



## SIO STRAP



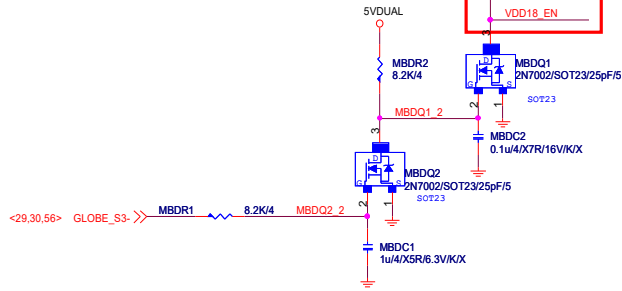
JP2	1	Disable WDT to rest PWROK
	0	Enable WDT to rest PWROK
JP3		Dual-BIOS CS pin mode select bit "0"
		See the below table
JP4	1	LPC/ESPI power VCCBT = 3.3V
	0	LPC/ESPI power VCCBT = 1.8V
JP5	1	LPC I/F
	0	ESPI I/F
JP6	1	Enable Dual BIOS Function (for GigaByte Only)
	0	Disable Dual BIOS Function (for GigaByte Only)
JP7		Dual-BIOS CE pin mode select bit "*"
		See the below table
JP7	1 1	CE pin disable (Hold pin mode)
	1 0	CE mode 1
JP3	0 1	CE mode 2
	0 0	CE mode 3

## GIGABYTE

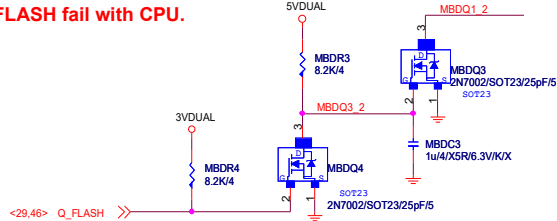
ITE 8686 LPC IO, TPM, KB/MS			
Size	Document Number	Rev	
Custom	B550 AORUS MASTER	1.01	
Date:	Monday, May 11, 2020	Sheet	19 of 56

PWR\_SEQ

上件  
MB\_DR30  
8.2K/4  
VDD18\_EN

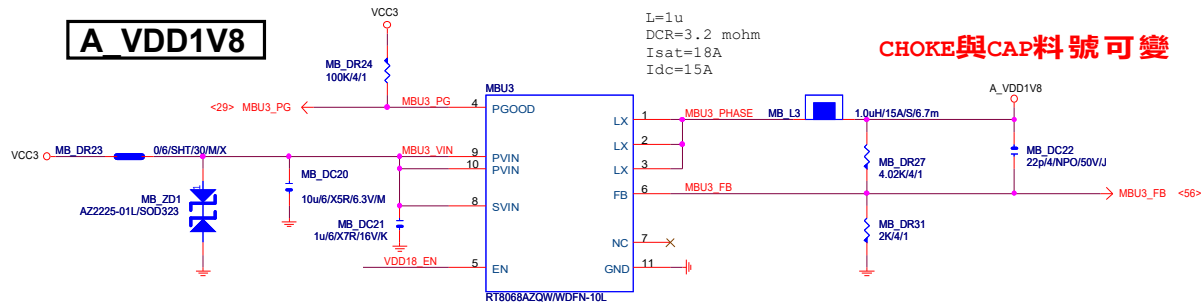


Fix Q\_FLASH fail with CPU.

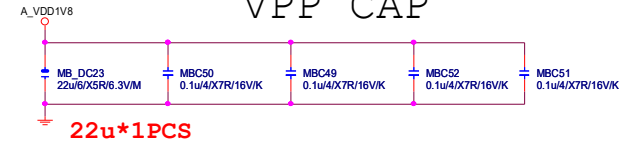


REV:0.4

A\_VDD1V8

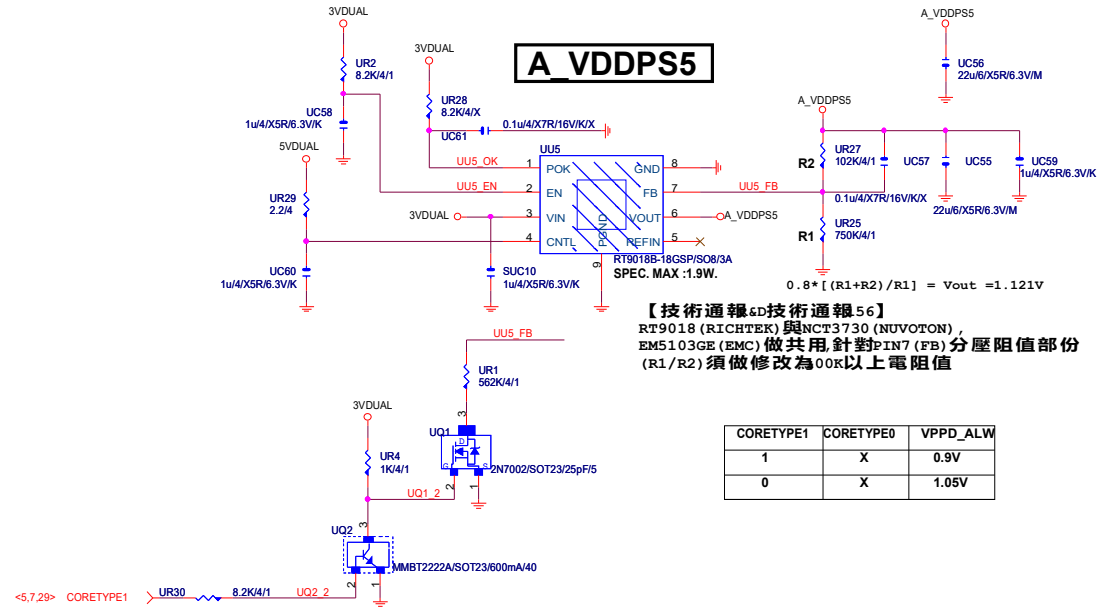


CHOKE與CAP料號可變



GIGABYTE			
Title			
FAN, A_VDD1V8			
Size	Document Number	Rev	
Cuskm	B550 AORUS MASTER	1.01	
Date:	Monday, May 11, 2020	Sheet	20 of 56

A\_VDDPS5

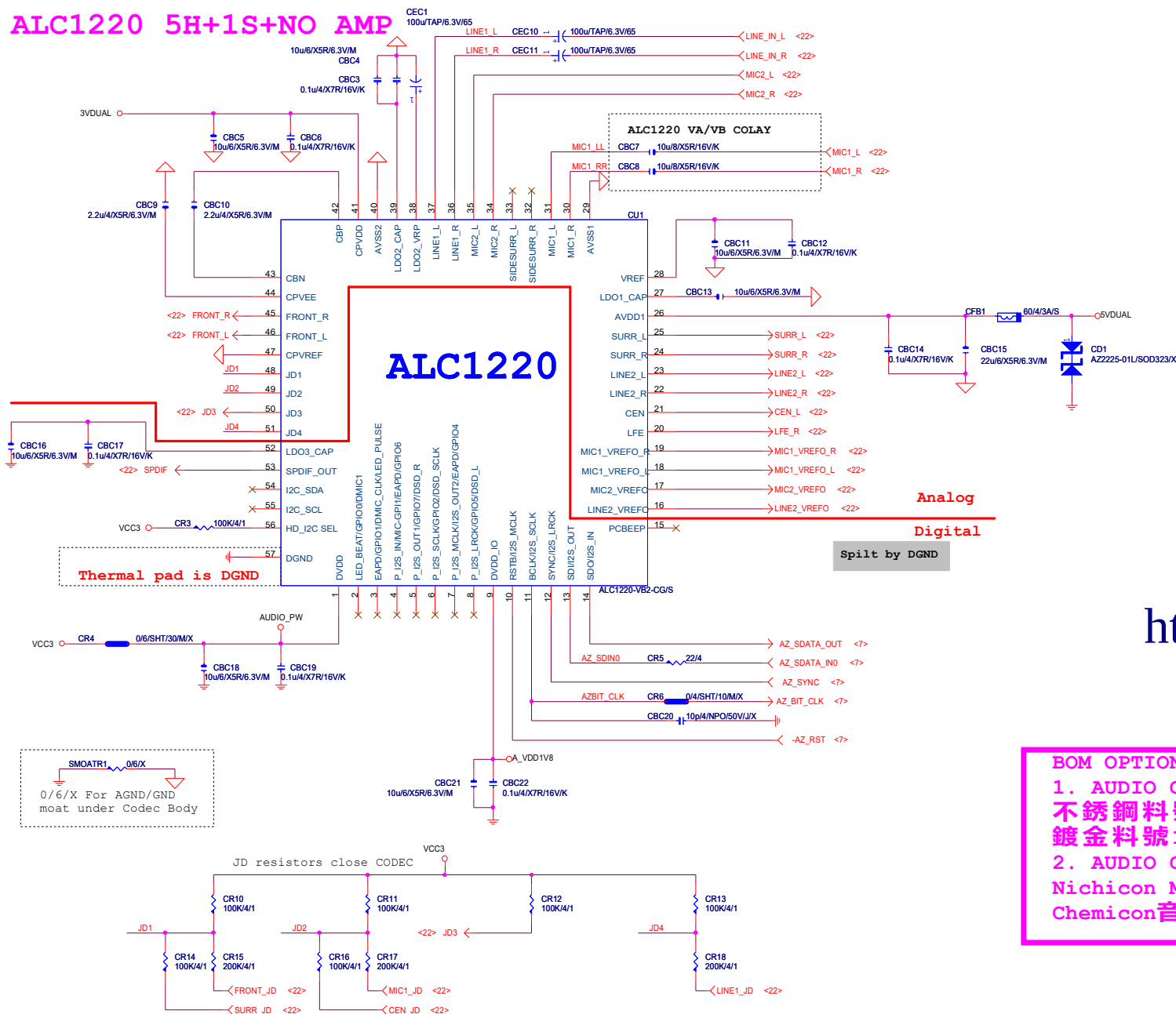


【技術通報&技術通報56】  
RT9018 (RICHTER) 與 NCT3730 (NUVOTON),  
EM5103GE (EMC) 做共用, 針對 PIN7 (FB) 分壓阻值部份  
(R1/R2) 須做修改為 00K 以上電阻值

CORETYPE1	CORETYPE0	VPPD_ALW
1	X	0.9V
0	X	1.05V

<http://www.repair1.ru/>

## ALC1220 5H+1S+NO AMP



LAYOUT注意:螺絲孔下ND方式

1. MH1下DGND

2. MH2一律改為solate

○ MH1      MH2 ○

DGND      Isolate

LAYOUT注意:是否要加  
AGND切割線

音效區域印刷

<http://www.repair1.ru/>

BOM OPTION :

1. AUDIO CONNECT

不銹鋼料號11NR6-403025-A3R

鍍金料號11NR6-403025-92R

2. AUDIO CAP

Nichicon MW音效電容: 11CE1-651000-12R

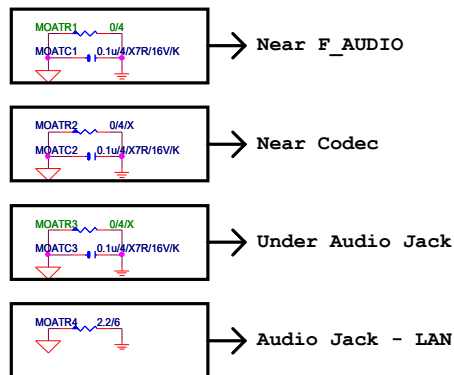
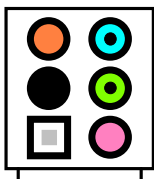
Chemicon音效電容: 11CE2-651000-05R

GIGABYTE™

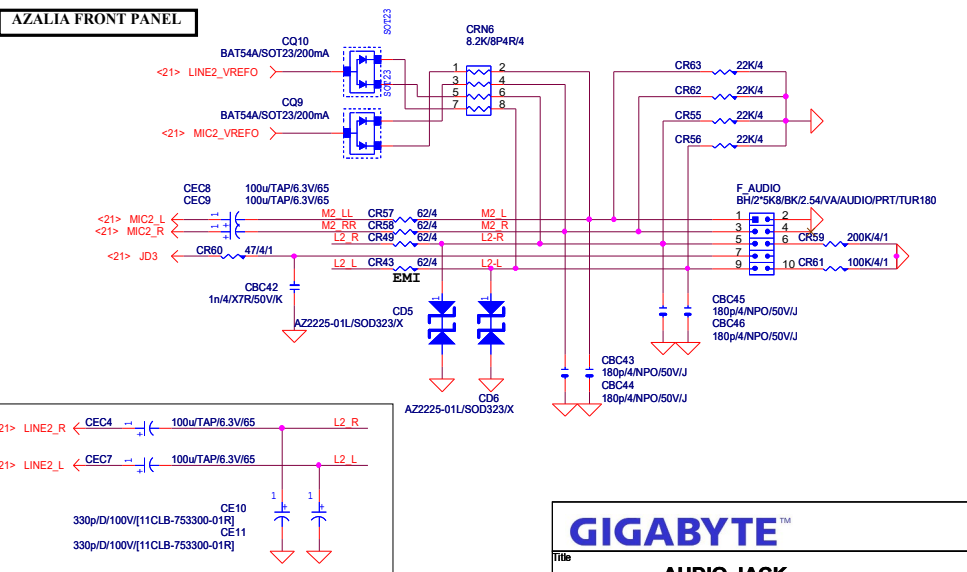
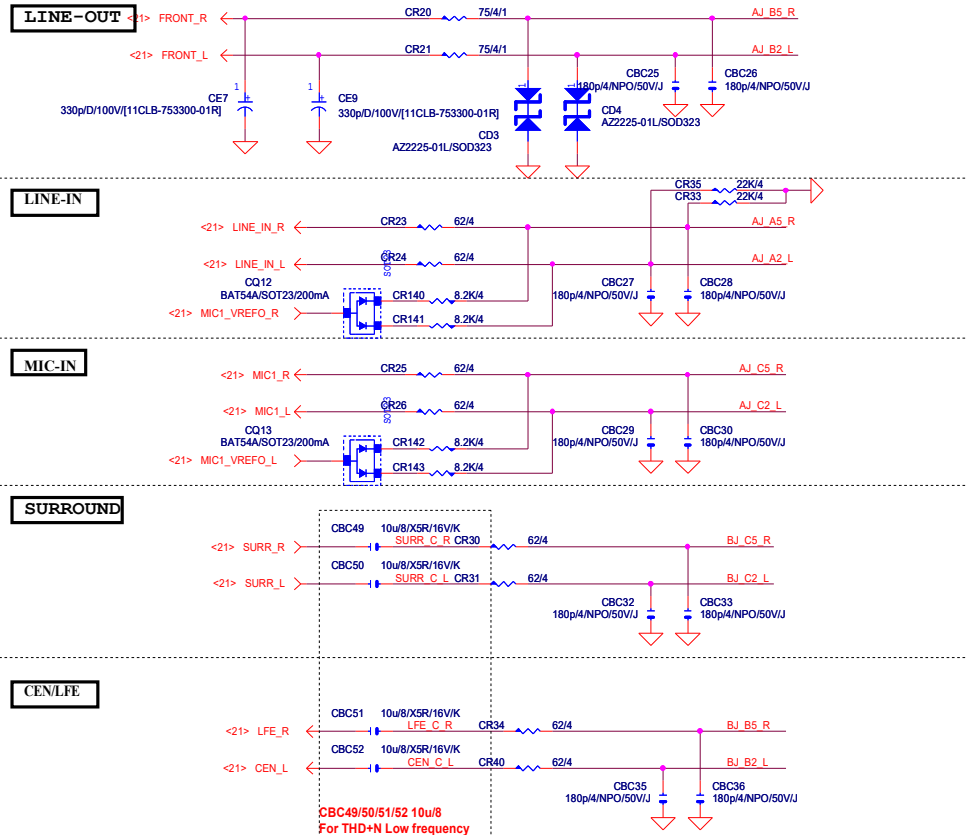
Title			
ALC1220 REAR			
Size	Document Number	Rev	
Cuskm	B550 AORUS MASTER	1.01	
Date:	Monday, May 11, 2020	Sheet	21 of 56

Rev 2.05

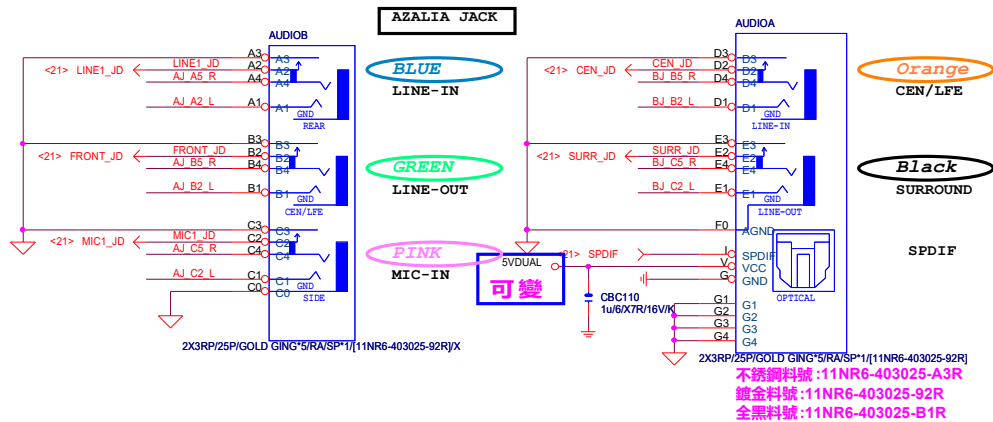
AZALIA JACK



<http://www.repair1.ru/>



GIGABYTE™			
Title			
AUDIO JACK			
Size	Document Number	Rev	
Cust	B550 AORUS MASTER	1.01	
Date:	Monday, May 11, 2020	Sheet	22 of 56



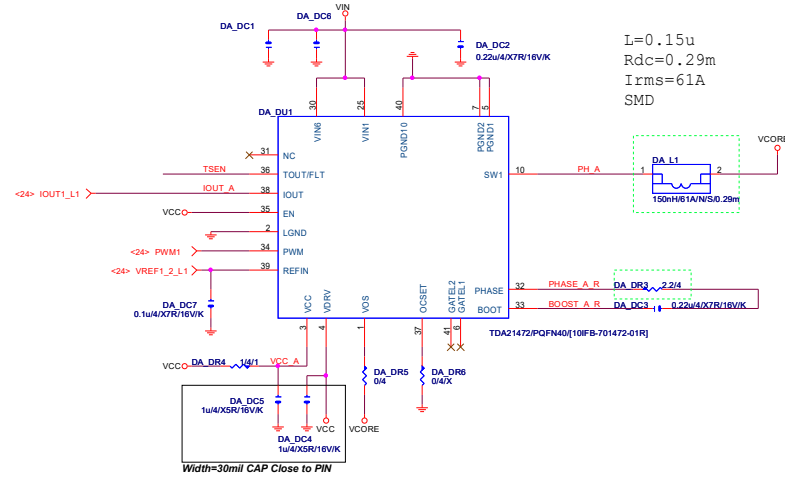


<b>GIGABYTE</b> ™			
Title <b>DIGITAL LED</b>			
Size Custom	Document Number <b>B550 AORUS MASTER</b>		Rev <b>1.01</b>
Date: Monday, May 11, 2020	Sheet 1	23 of	56



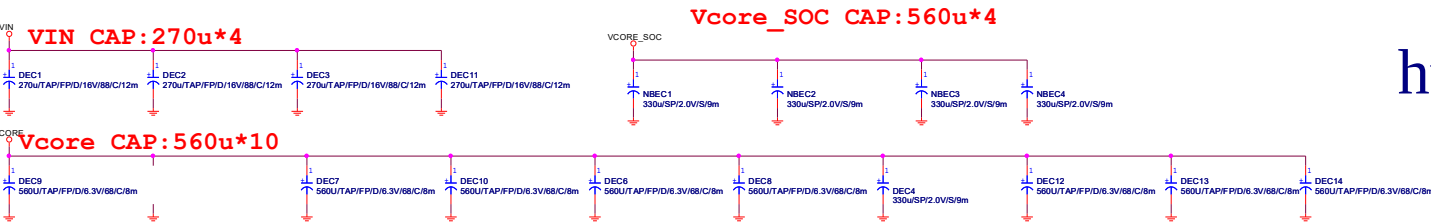
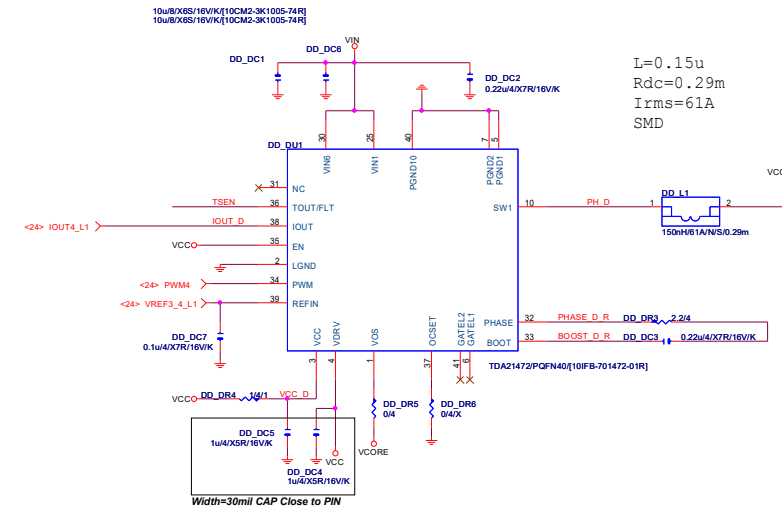
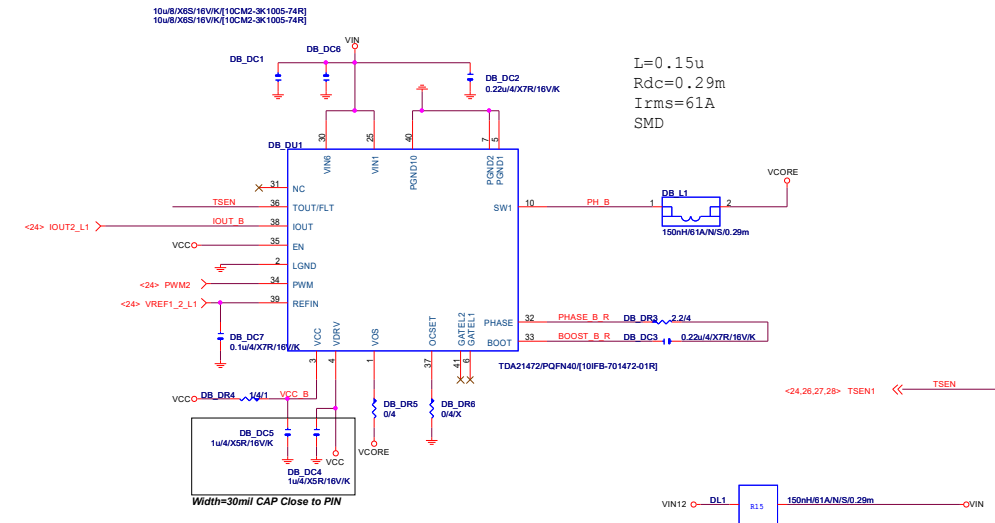
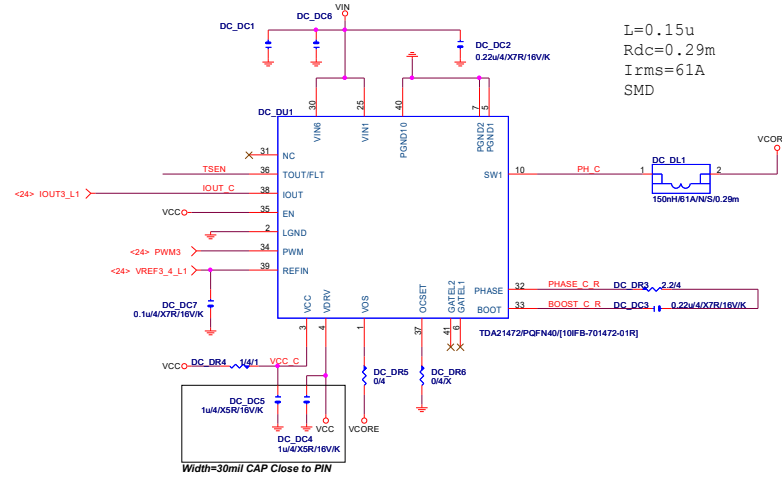
## VCORE PHASE1\_2

10u/8/X8S/16V/K[10CM2-3K1005-74R]  
10u/8/X8S/16V/K[10CM2-3K1005-74R]



## VCORE PHASE3\_4

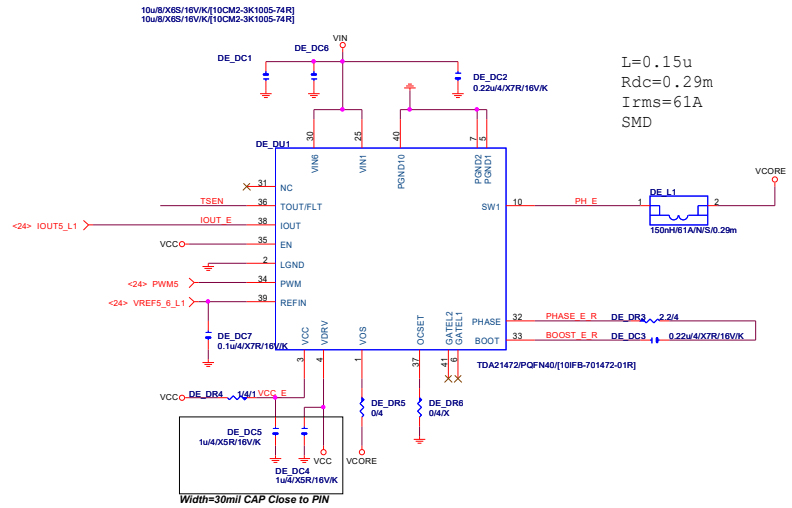
10u/8/X8S/16V/K[10CM2-3K1005-74R]  
10u/8/X8S/16V/K[10CM2-3K1005-74R]



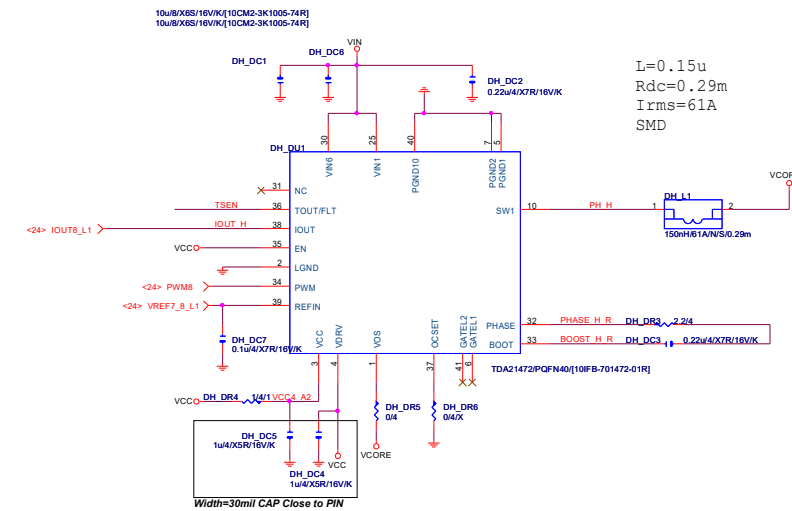
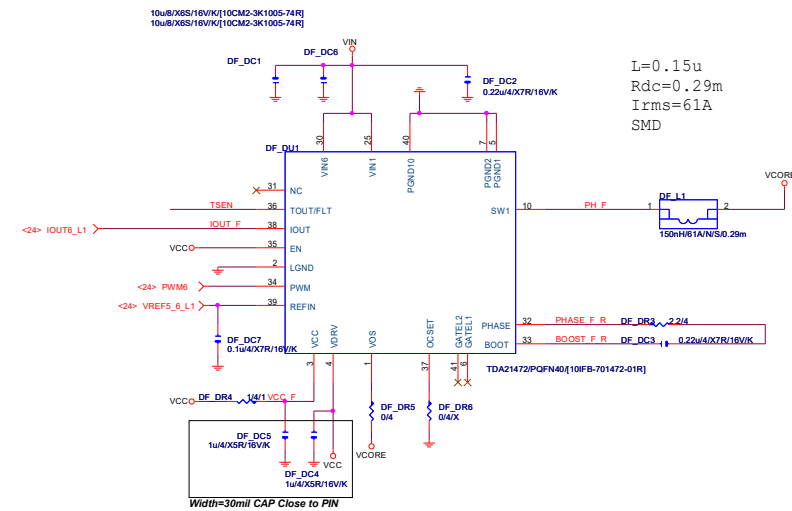
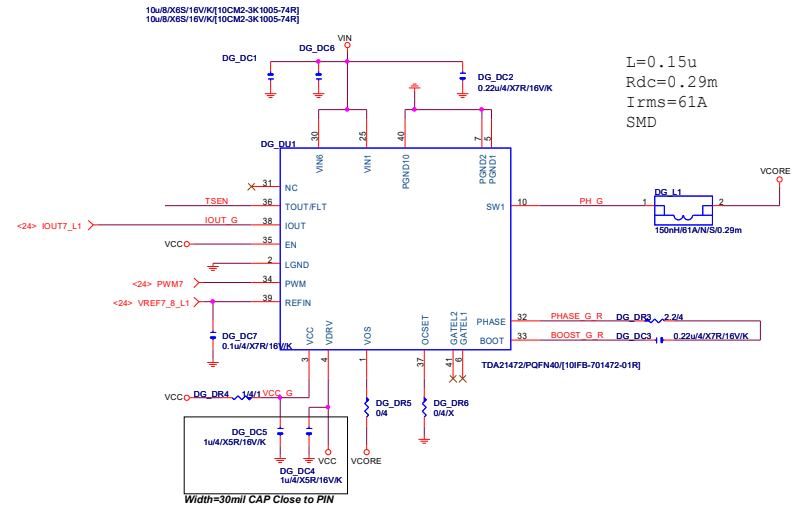
<http://www.repair1.ru/>

REV:0.11

## VCORE PHASE5\_6



## VCORE PHASE7\_8


<http://www.repair1.ru/>

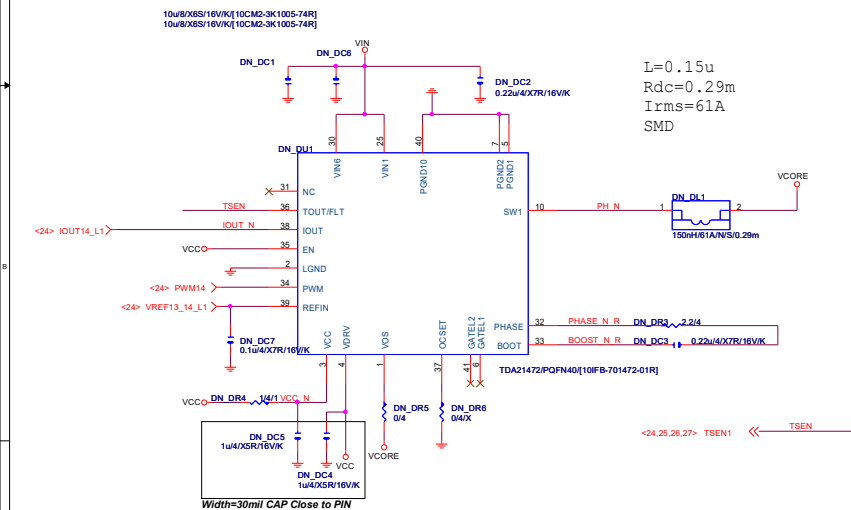
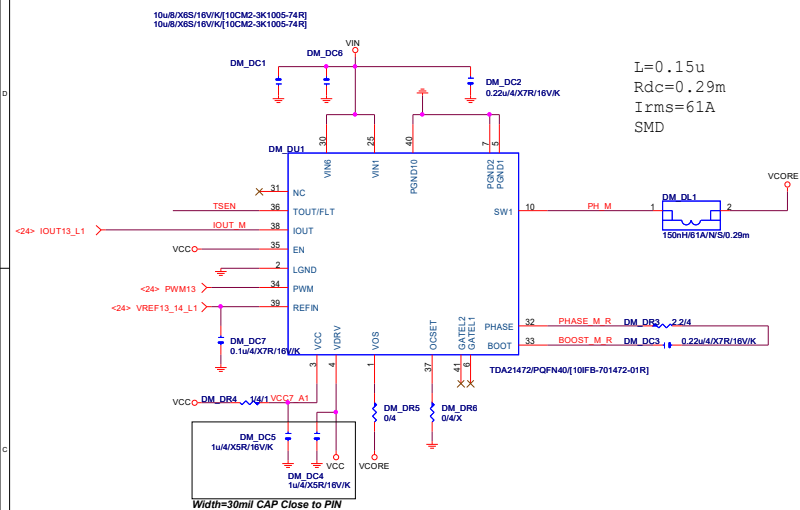
&lt;&lt;24,25,27,28&gt; TSEN1 &lt;&lt; TSEN

GIGABYTE™

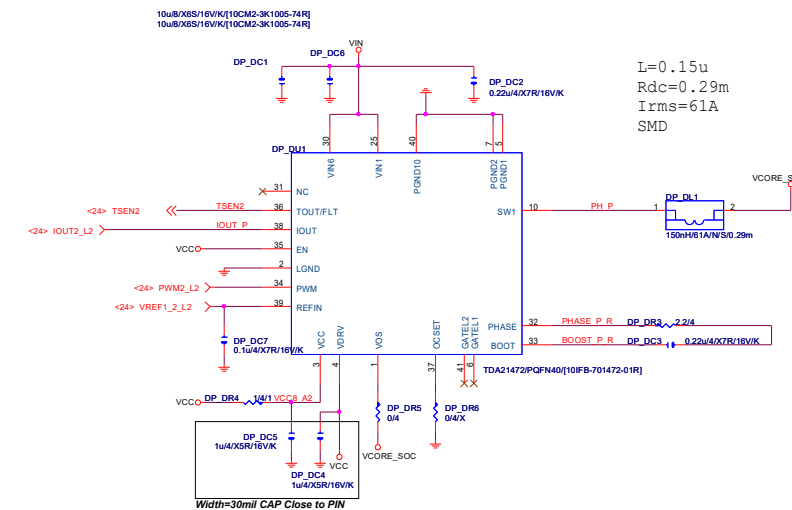
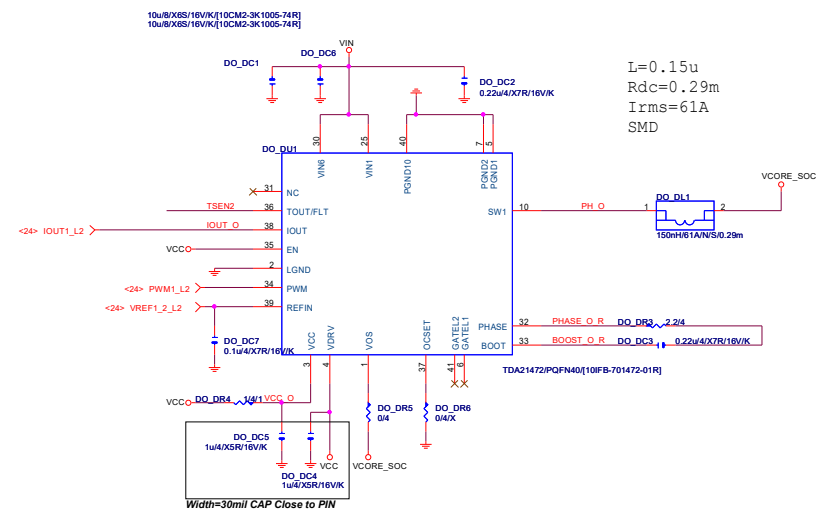
Title		IR3555 MOS-2	
Size		Document Number	
Custom		B550 AORUS MASTER	
Date		Monday, May 11, 2020	Sheet 28 of 56
		Rev	1.01

Date: Monday, May 11, 2020 Sheet 27 of 58

## VCORE PHASE13\_14



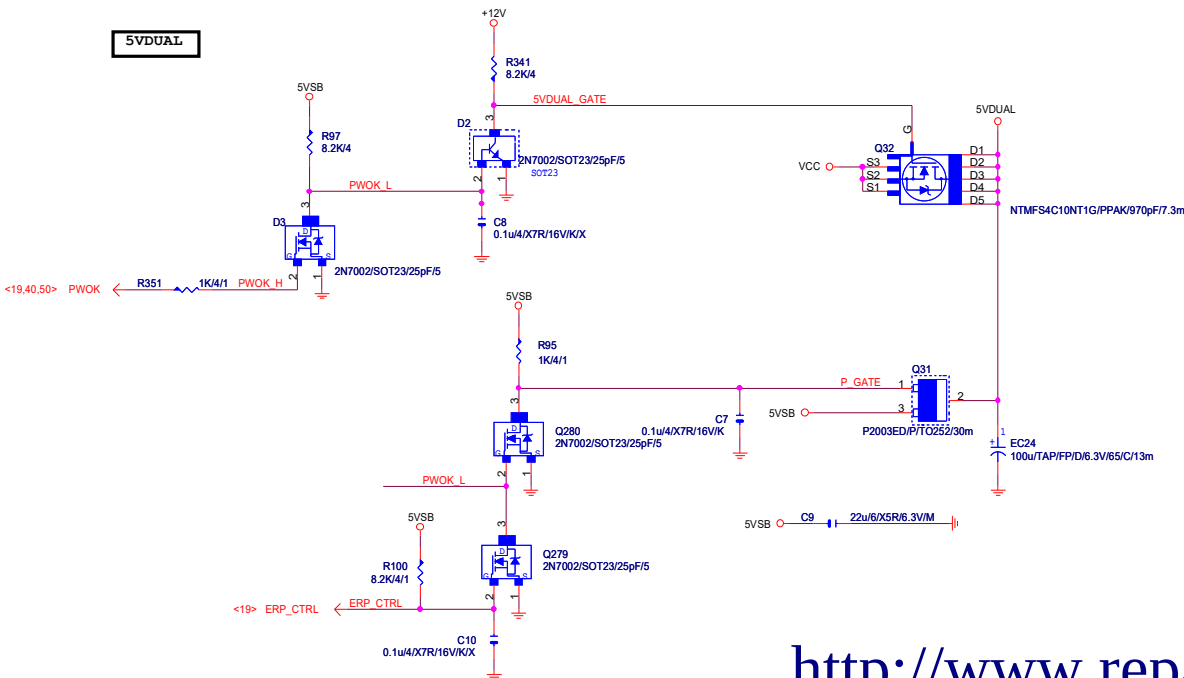
## VSOC PHASE1\_2

**GIGABYTE™**

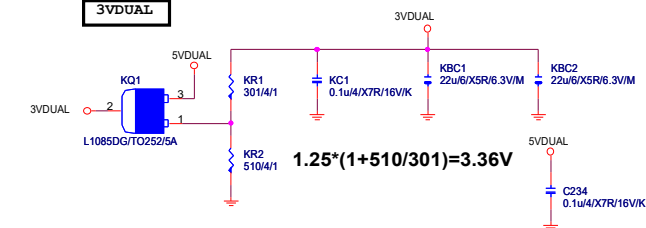
Title		IR3555 MOS-1	
Size	Document Number	Rev	
Custom	B550 AORUS MASTER	1.01	
Date	Monday, May 11, 2020	Sheet	28 of 56



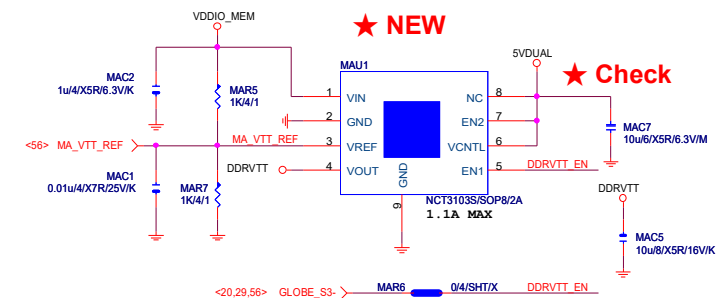
# 5VDUAL



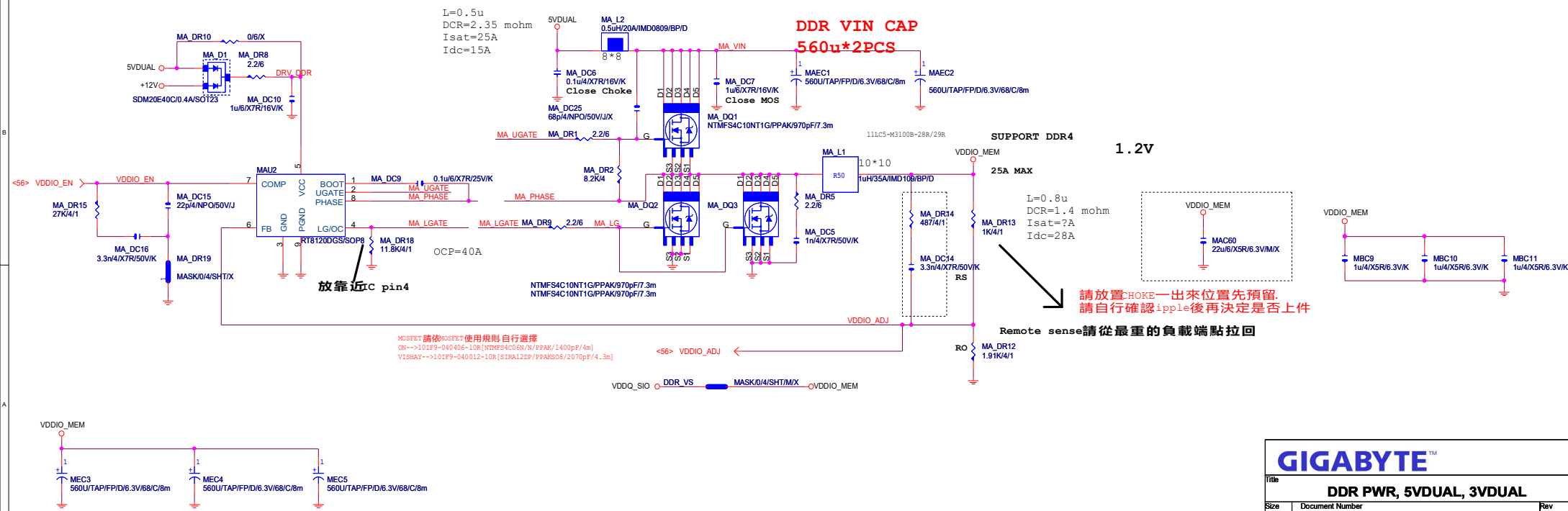
# 3VDUAL



# DDRVTT

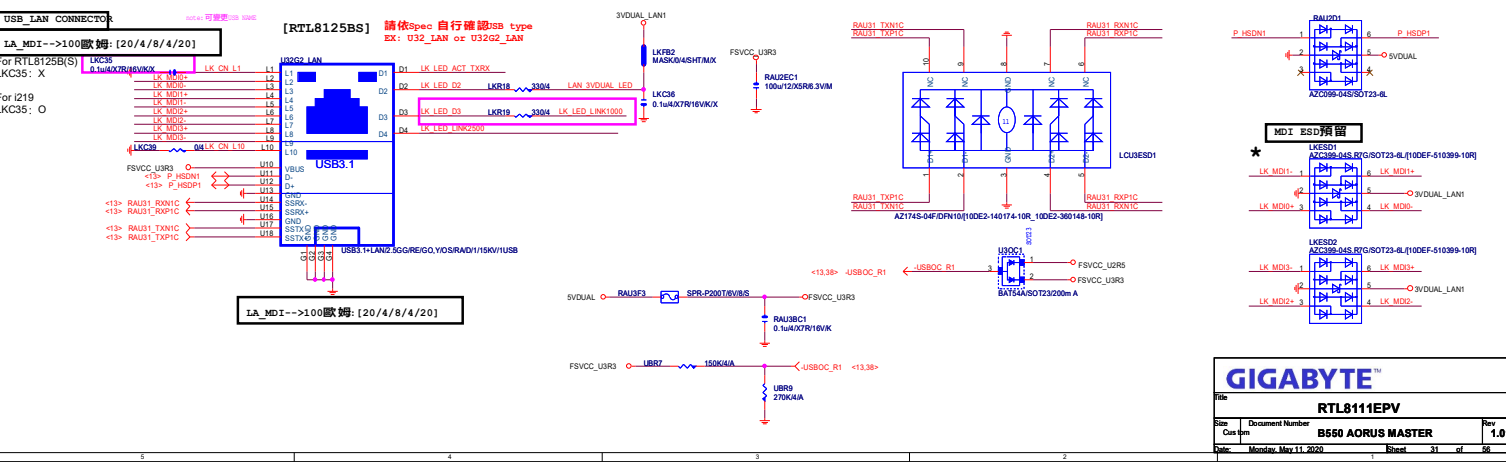
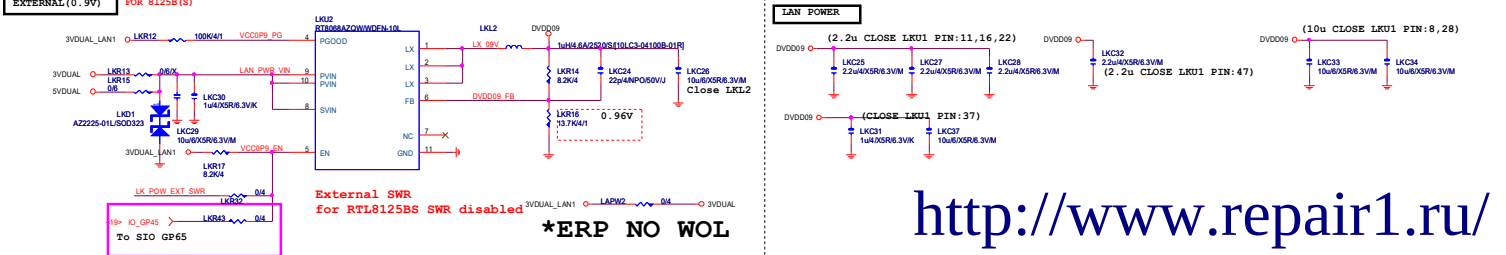
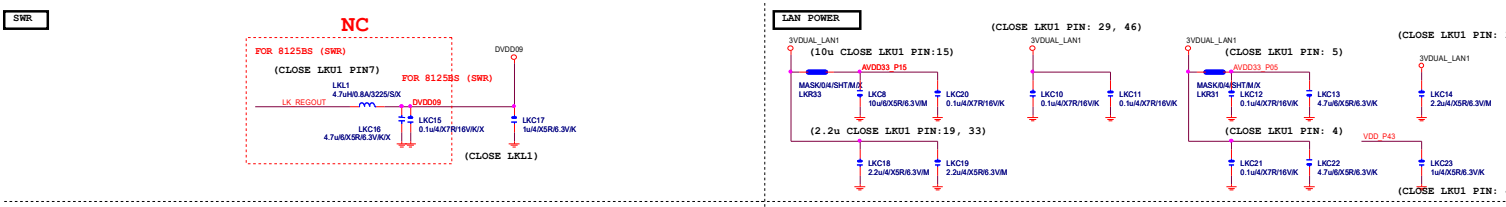
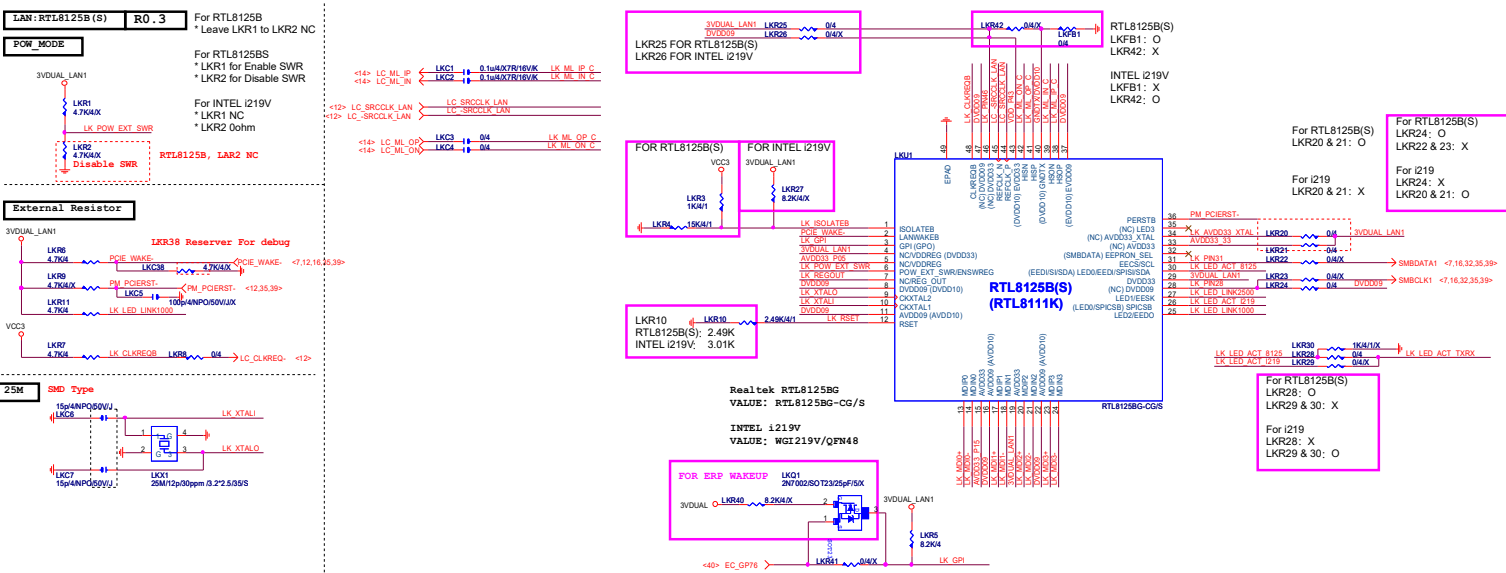


# VDDIO\_MEM



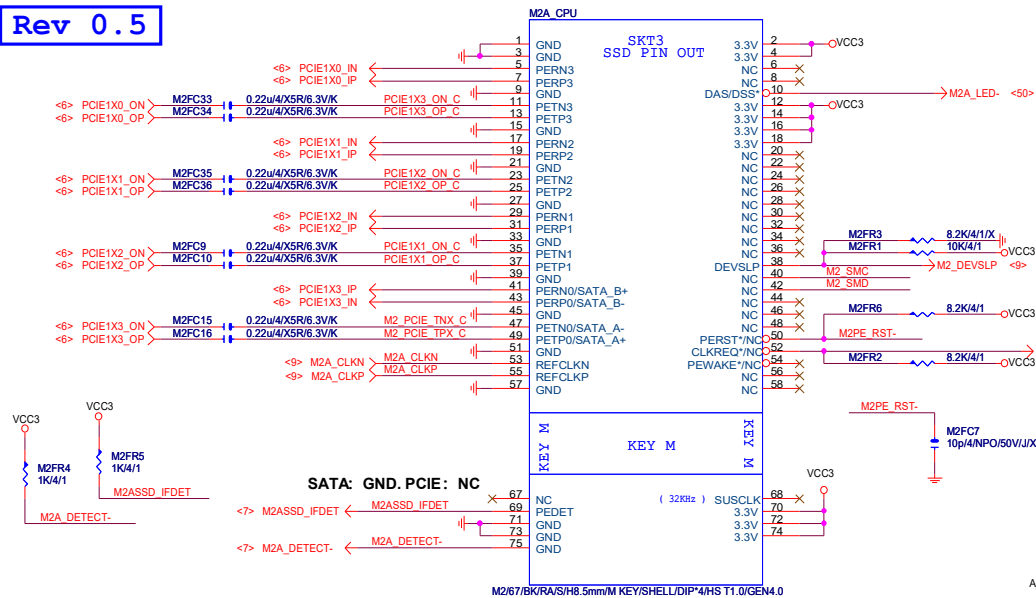
GIGABYTE

Title			
DDR PWR, 5VDUAL, 3VDUAL			
Size	Document Number	Rev	
Cuskm	B550 AORUS MASTER	1.01	
Date:	Monday, May 11, 2020	Sheet	30 of 56

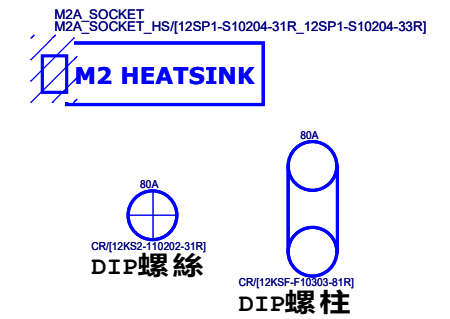
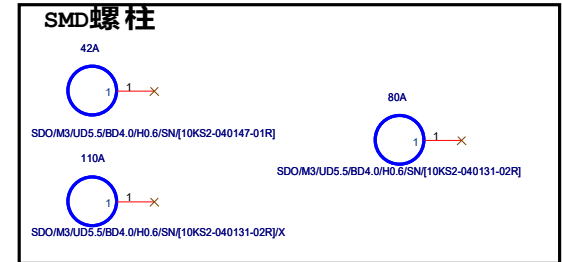
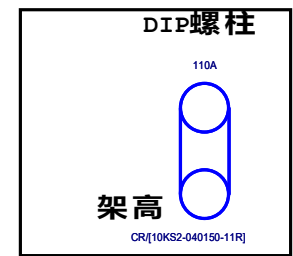
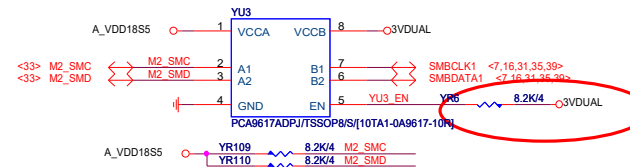


<http://www.repair1.ru/>

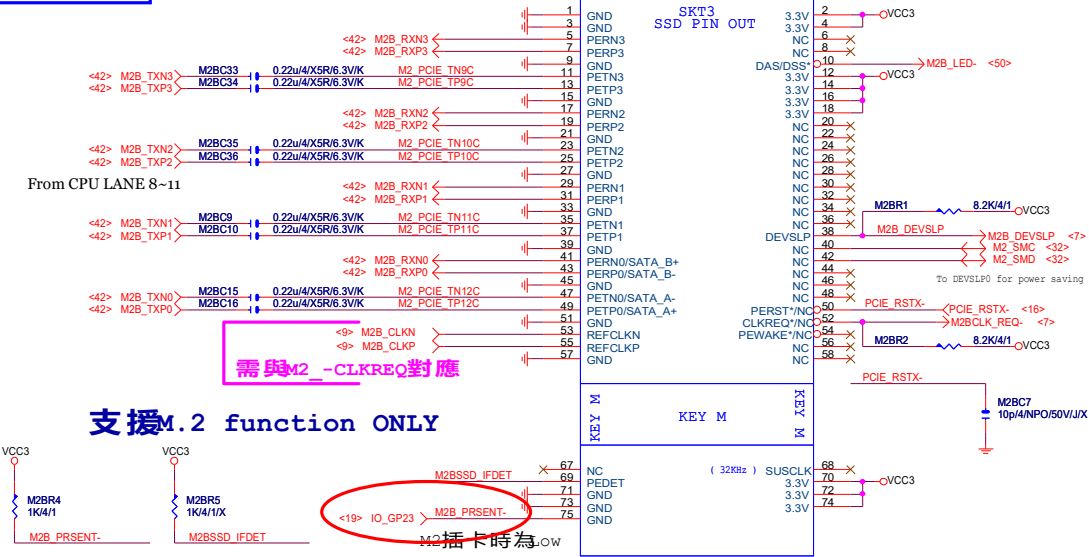
Rev 0.5



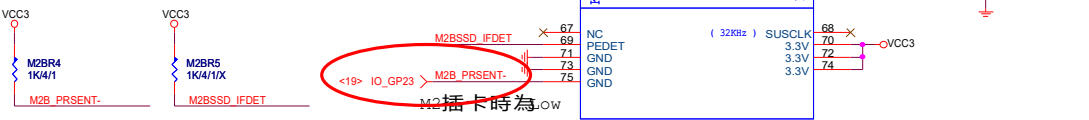
8.5H 鐵殼  
Footprint :M2\_110(DIP)



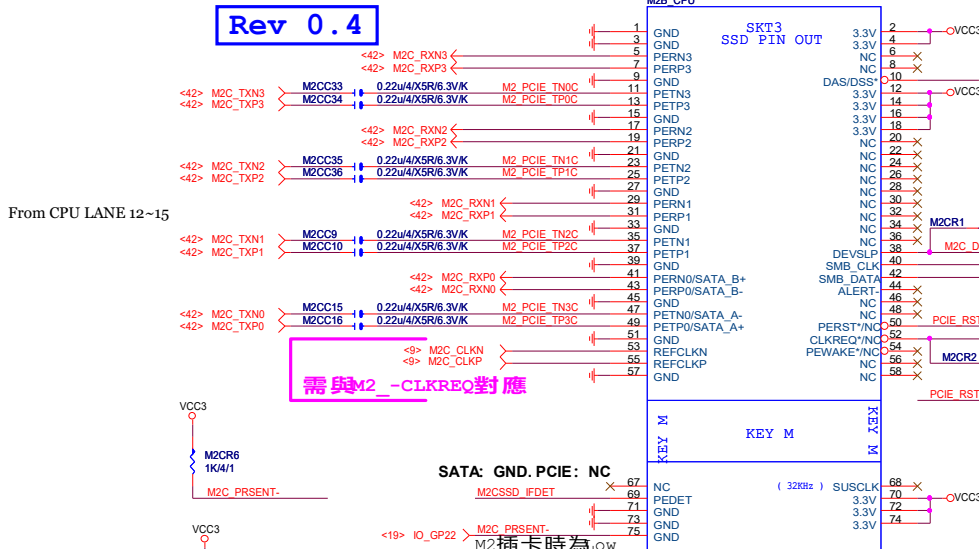
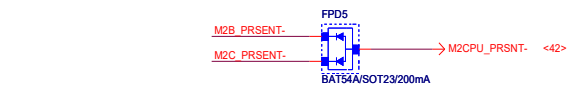
<http://www.repair1.ru/>



支援M.2 function ONLY

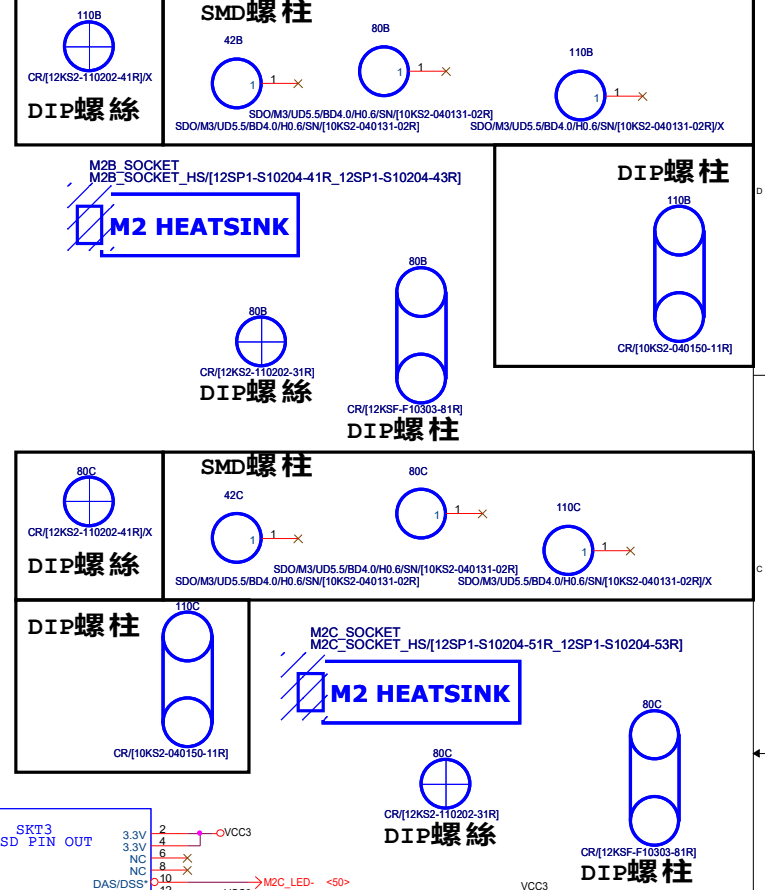


架高 M.2 FOOTPRINT :M2\_80



SATA: GND, PCIE: NC

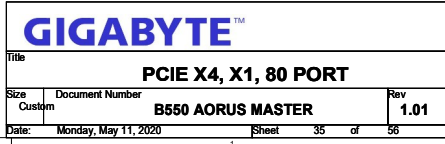
架高 M.2 FOOTPRINT :M2\_80



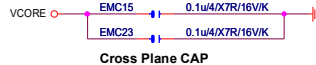
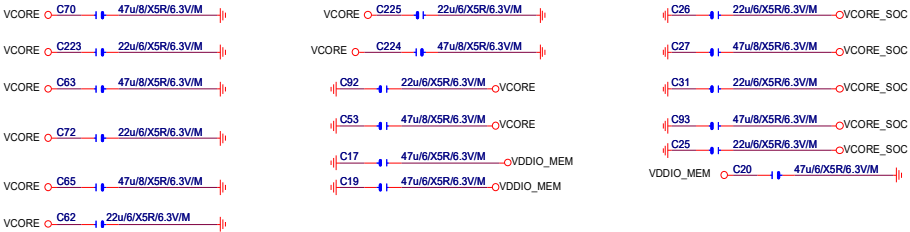
<http://www.repair1.ru/>



<b>GIGABYTE</b> ™			
Title <b>KB_USB_DEC PWR</b>			
Size	Document Number		Rev
Custom	<b>B550 AORUS MASTER</b>		<b>1.01</b>
Date:	Monday, May 11, 2020	Sheet 34 of 56	

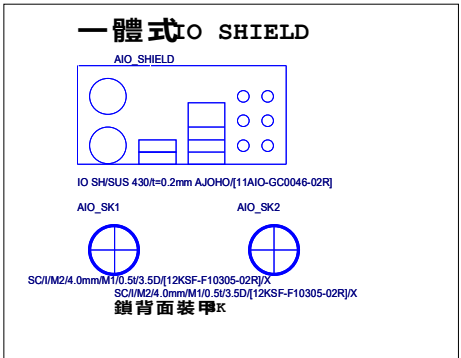
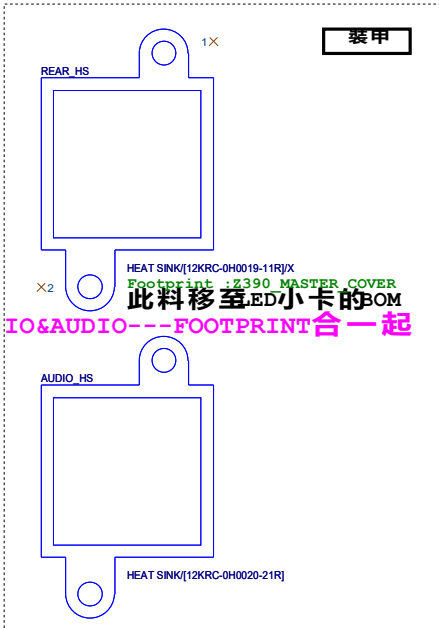
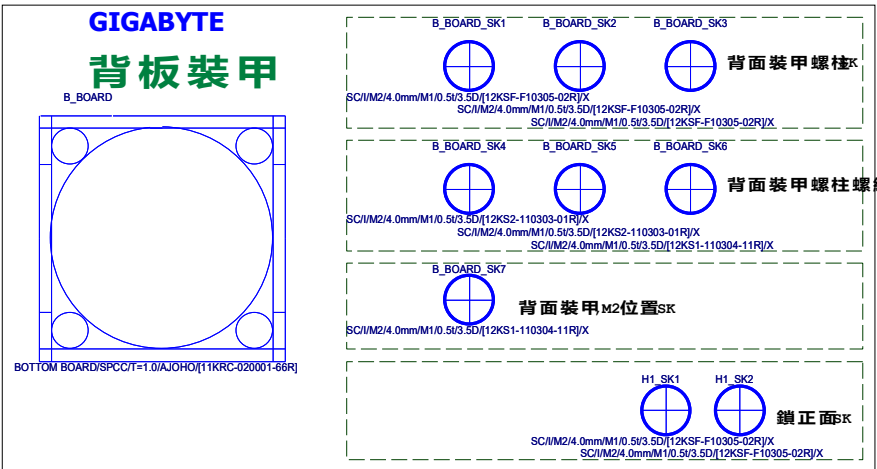
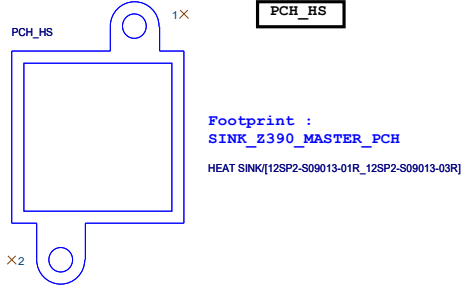
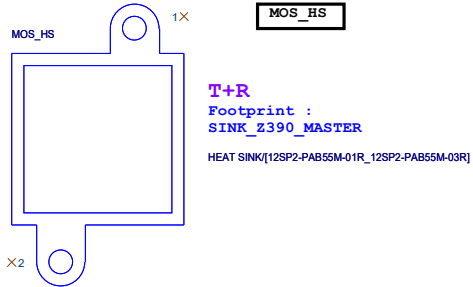


CPU TOP CAVITY

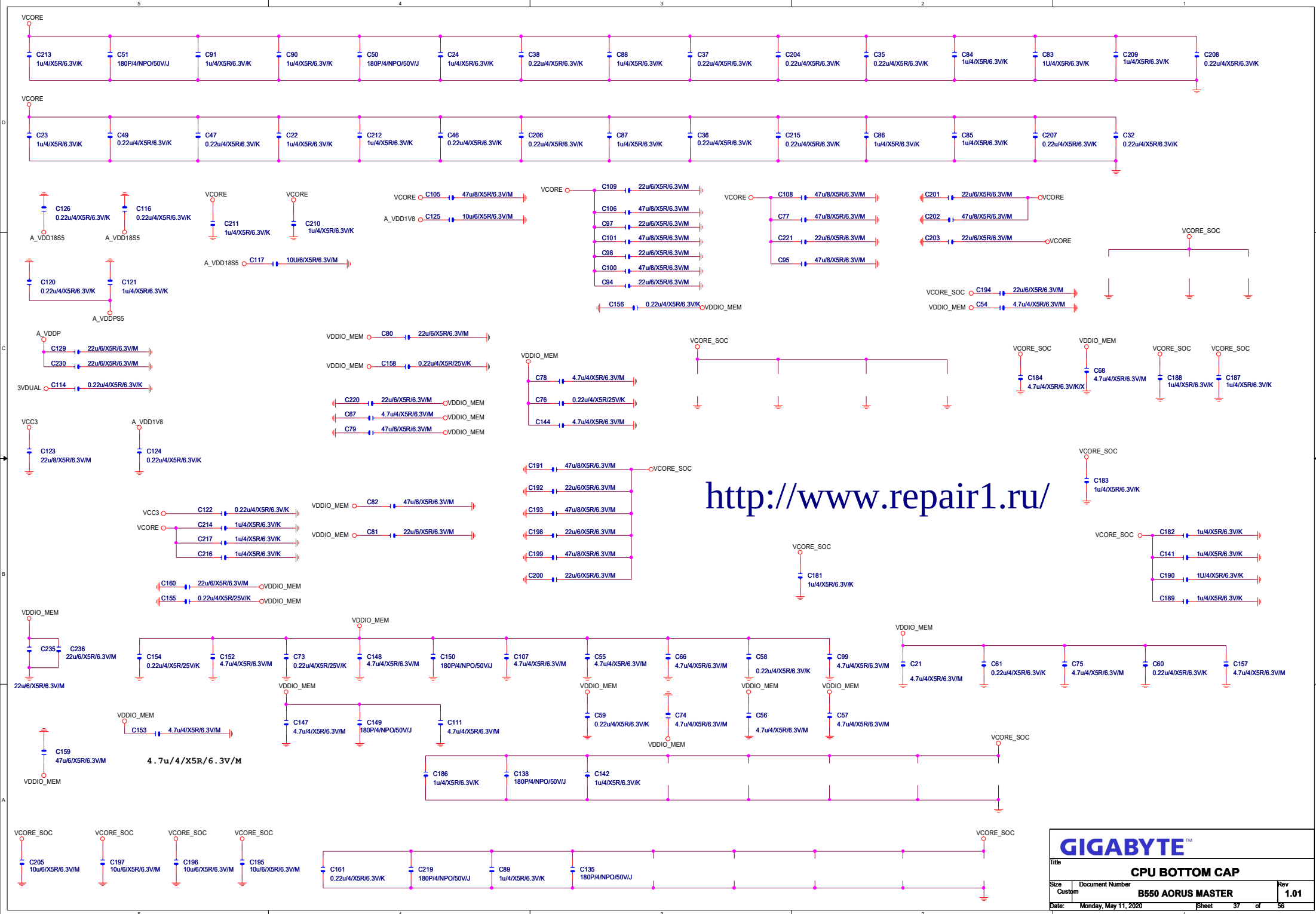


<http://www.repair1.ru/>

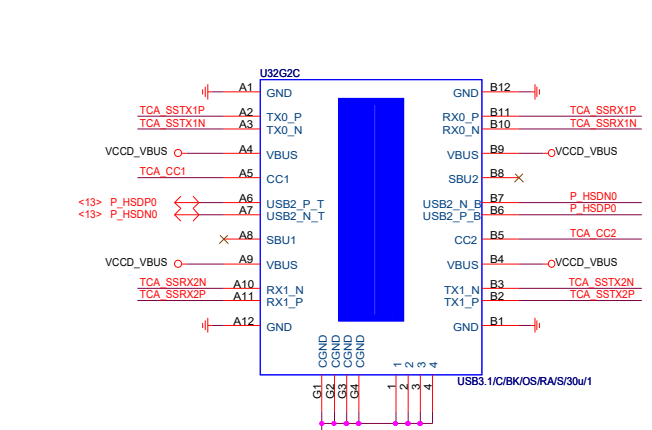
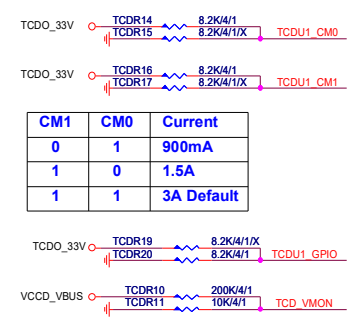
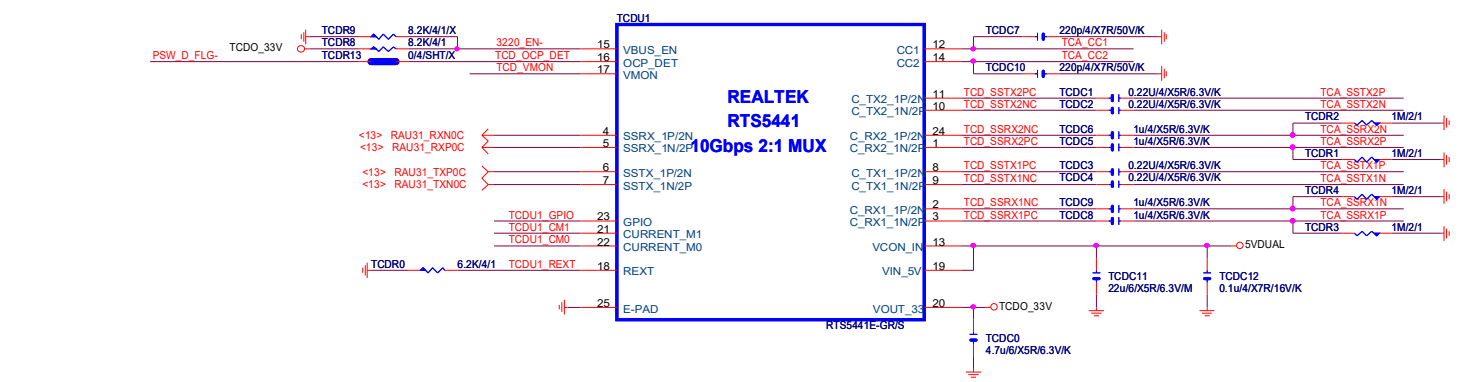
裝甲HEATSINK 分成四大部份



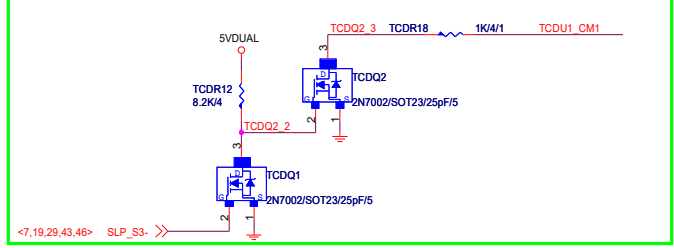
GIGABYTE™			
Title			
CPU TOP CAP			
Size	Document Number	Rev	
Cuskm	B550 AORUS MASTER	1.01	
Date:	Monday, May 11, 2020	Sheet	36 of 56



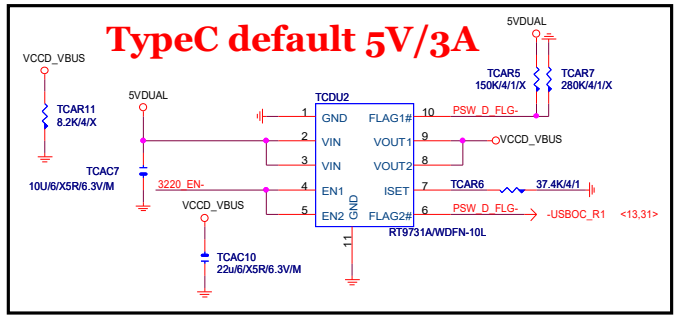
REALTEK CC LOGIC AND MUX RTS5441



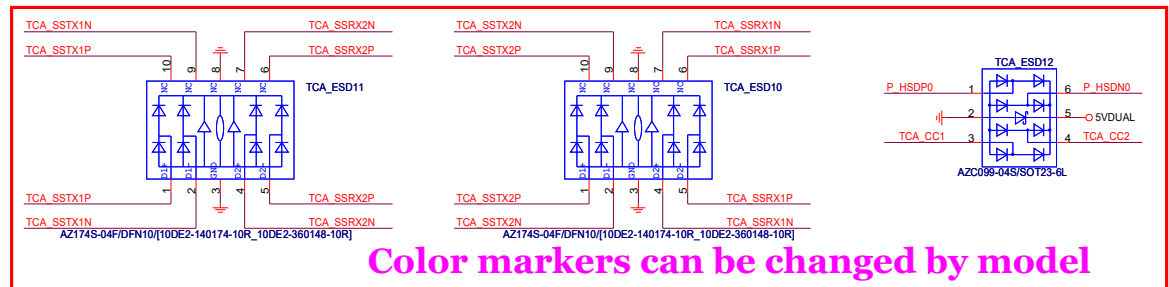
USB2.0 can be used the same source



For VBUS current limit at 900mA on S3



TypeC default 5V/3A



Color markers can be changed by model

<http://www.repair1.ru/>

**GIGABYTE**<sup>TM</sup>

Title

**M.2 SOCKET**

Size

Custm

Document Number

**B550 AORUS MASTER**

Rev

**1.01**

Date

Monday, May 11, 2020

Sheet

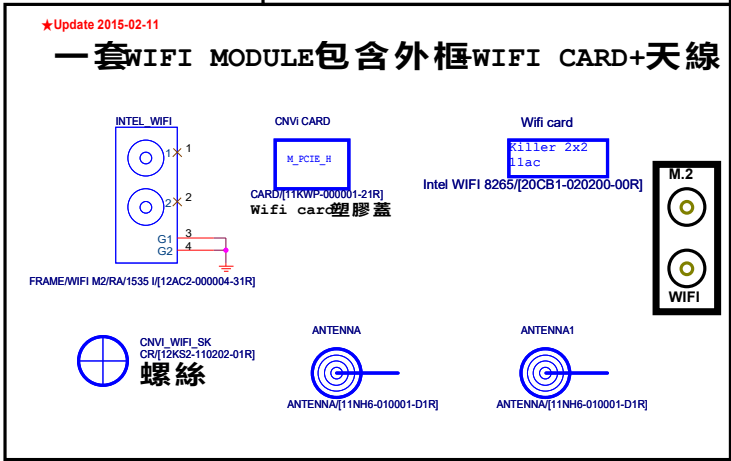
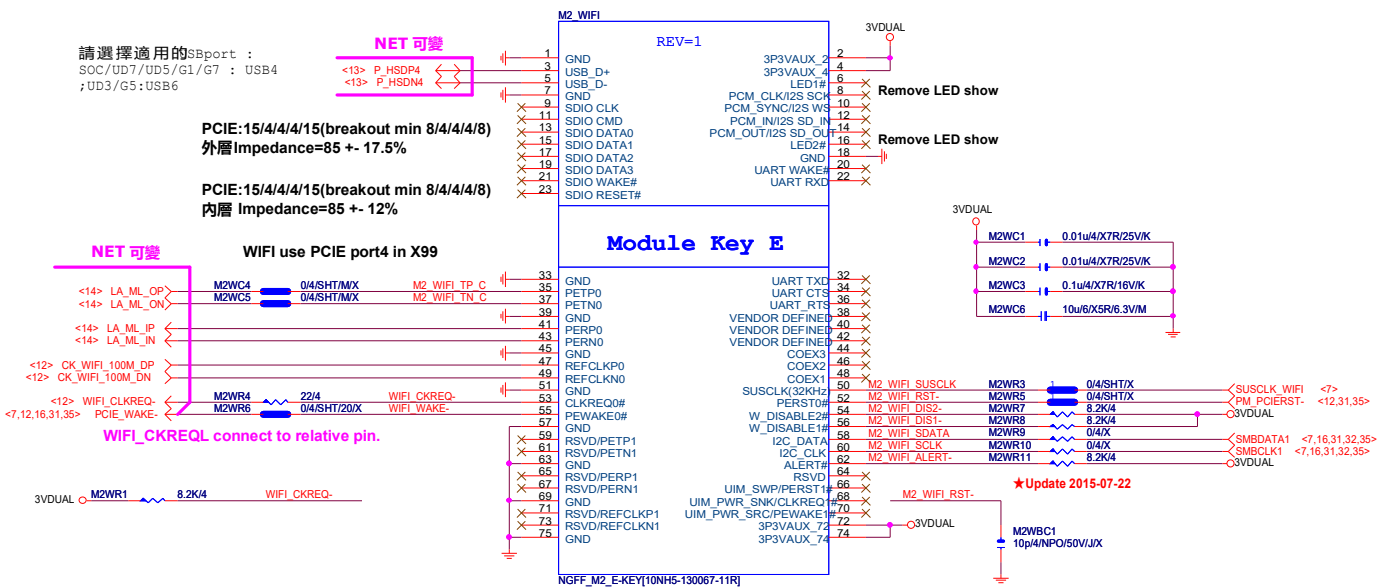
38

of

56

Rev: 0.2

FOR M.2 WIFI MODULE @ REAR PANEL



<http://www.repair1.ru/>

**Footprint Notice.**

★Update 2015-07-22

★Footprint for 直立式SMD: WIFI-EKEY

**Footprint Notice.**

★Update 2015-07-22

★Footprint for 橫式SMD: NGFF-E-75P-2

**GIGABYTE**

Model: M.2/U.2 SWITCH

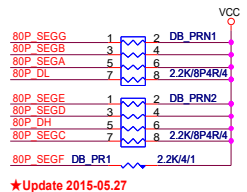
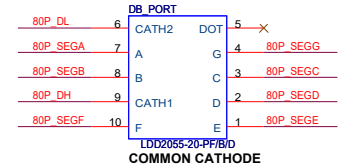
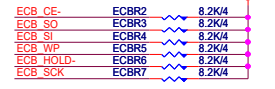
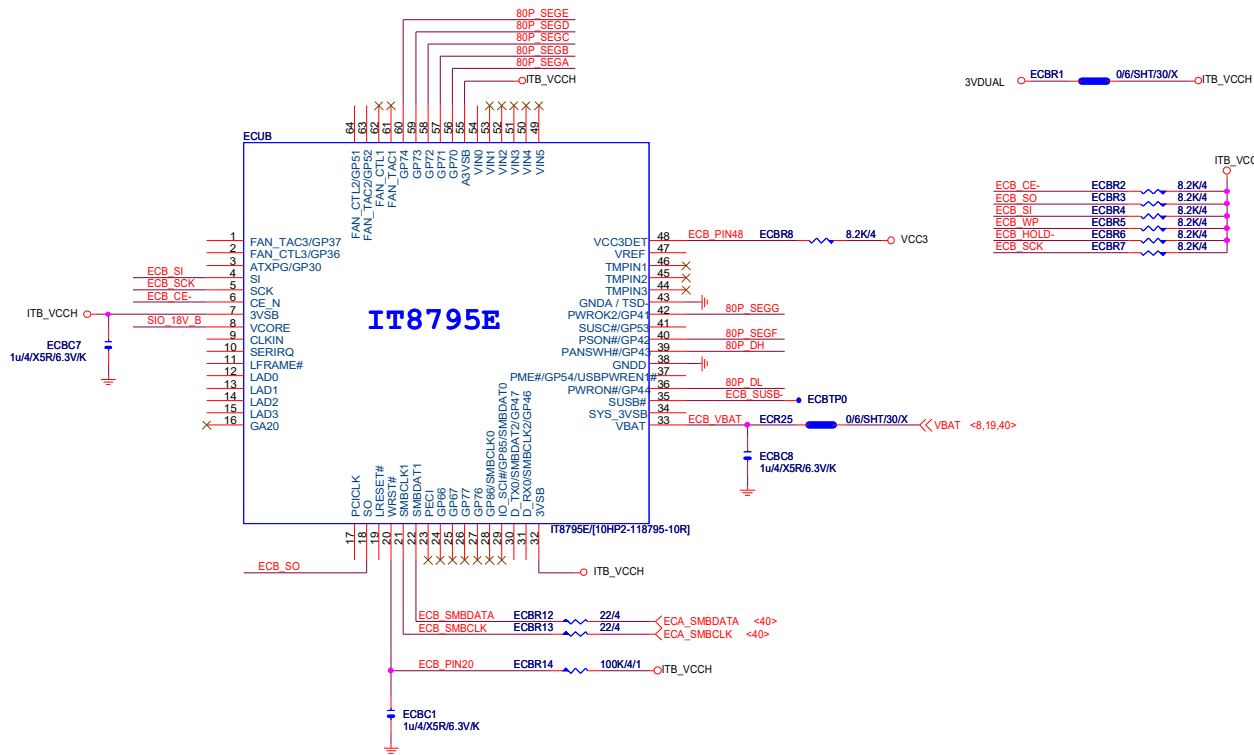
Document Number: B550 AORUS MASTER

Rev: 1.01

Date: Monday, May 11, 2020

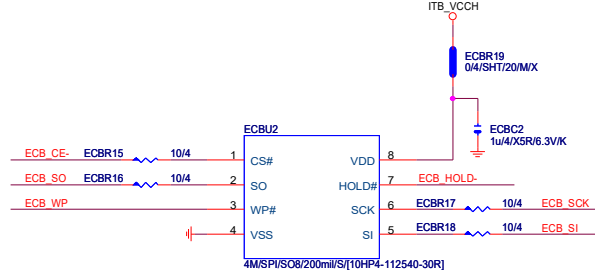
Sheet: 39 of 56





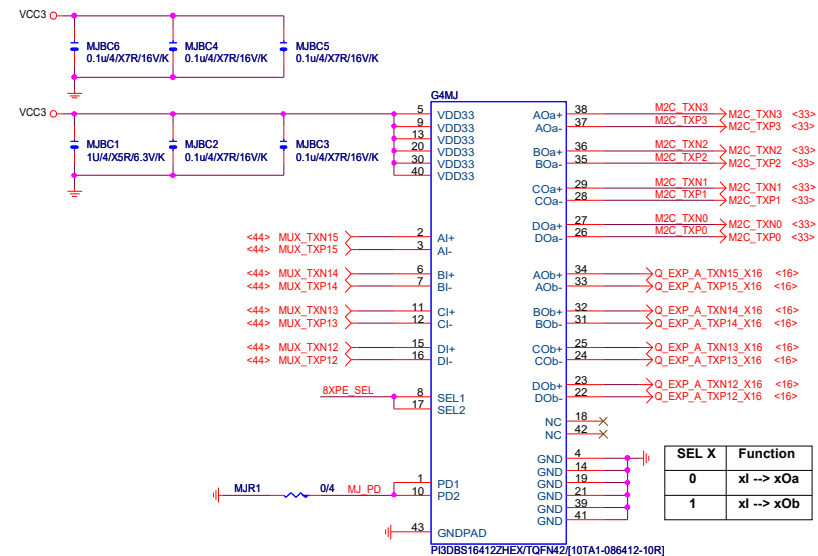
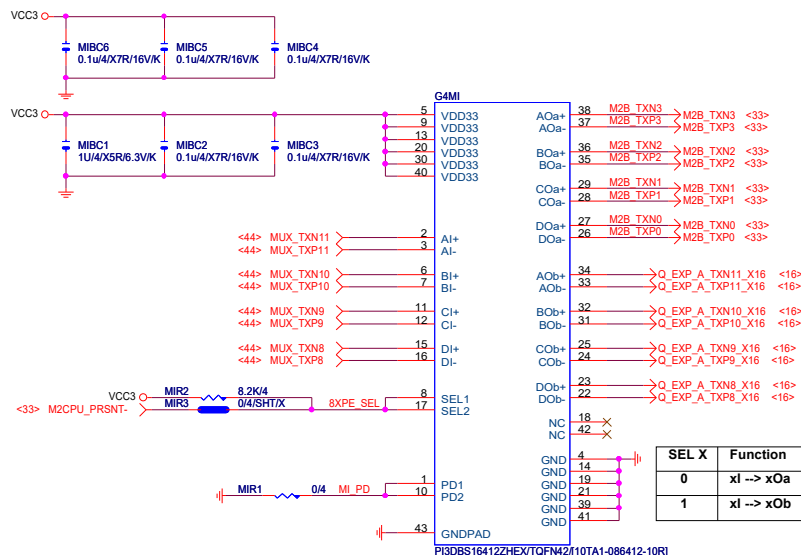
★ Update 2015-05.27

<http://www.repair1.ru/>



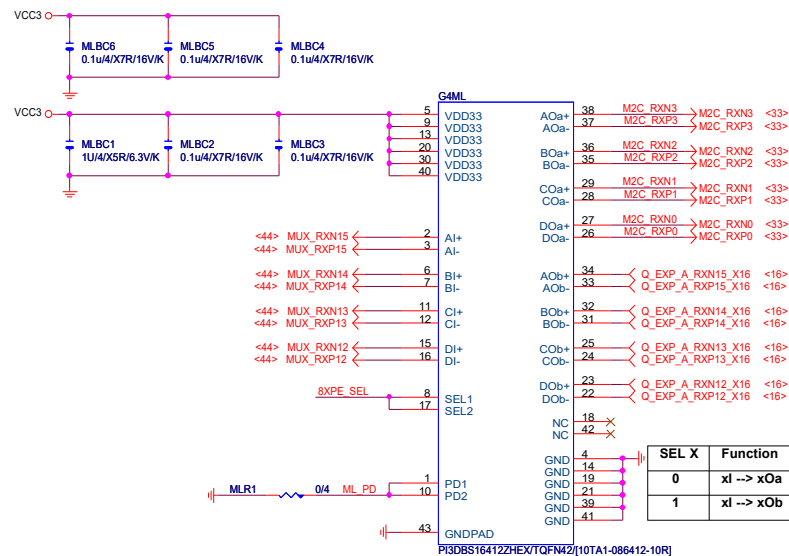
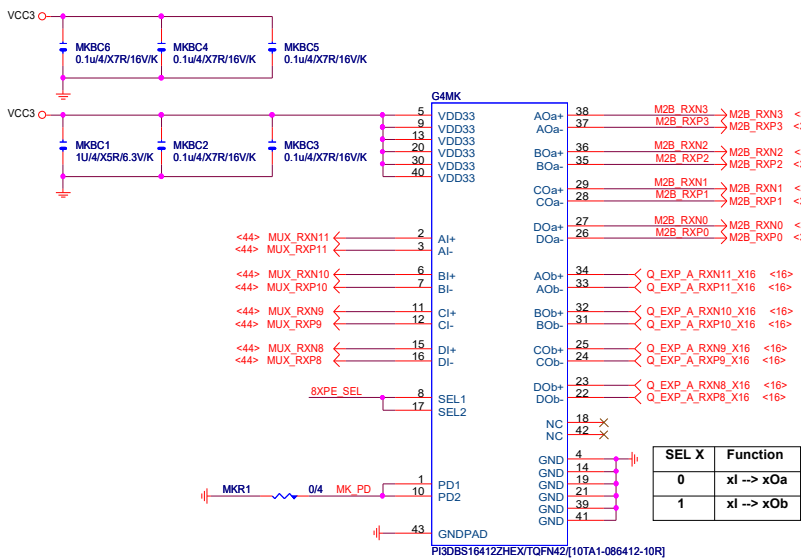
<b>GIGABYTE</b>			
<b>M.2 SOCKET</b>			
Size	Document Number	Rev	
Cuskm	<b>B550 AORUS MASTER</b>	<b>1.01</b>	
Date:	Monday, May 11, 2020	Sheet	41 of 56

<http://www.repair1.ru/>

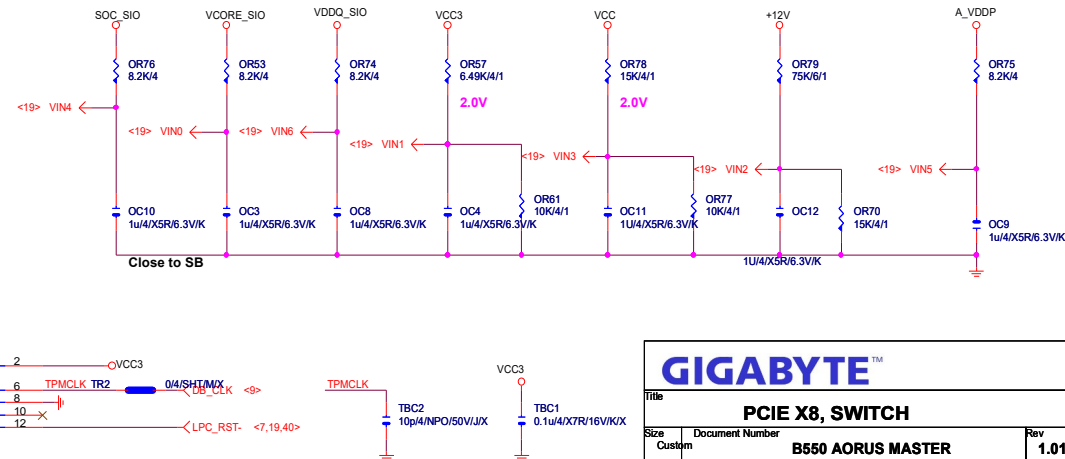
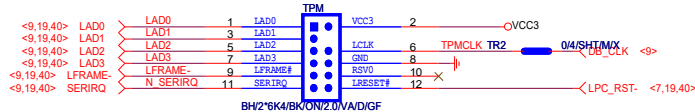
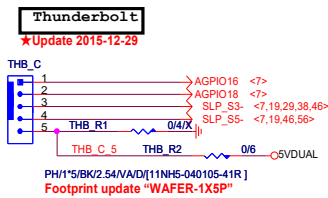
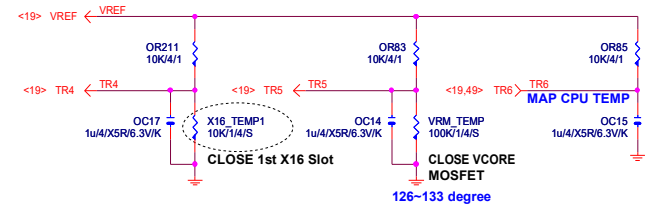
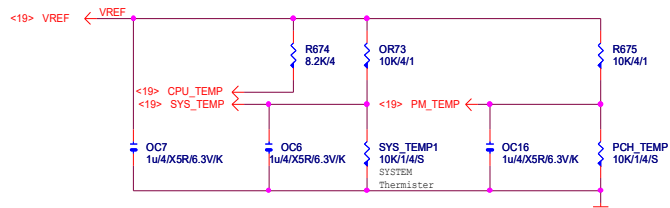


EXP\_A\_RXP[8..15] --> EXP\_A\_RXP[8..15] <6,44>  
EXP\_A\_RXN[8..15] --> EXP\_A\_RXN[8..15] <6,44>

<6,44> EXP\_A\_TXP[8..15] --> EXP\_A\_TXP[8..15]  
<6,44> EXP\_A\_TXN[8..15] --> EXP\_A\_TXN[8..15]

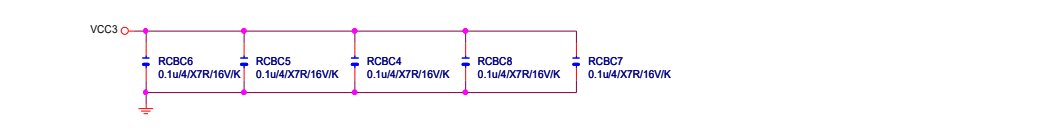
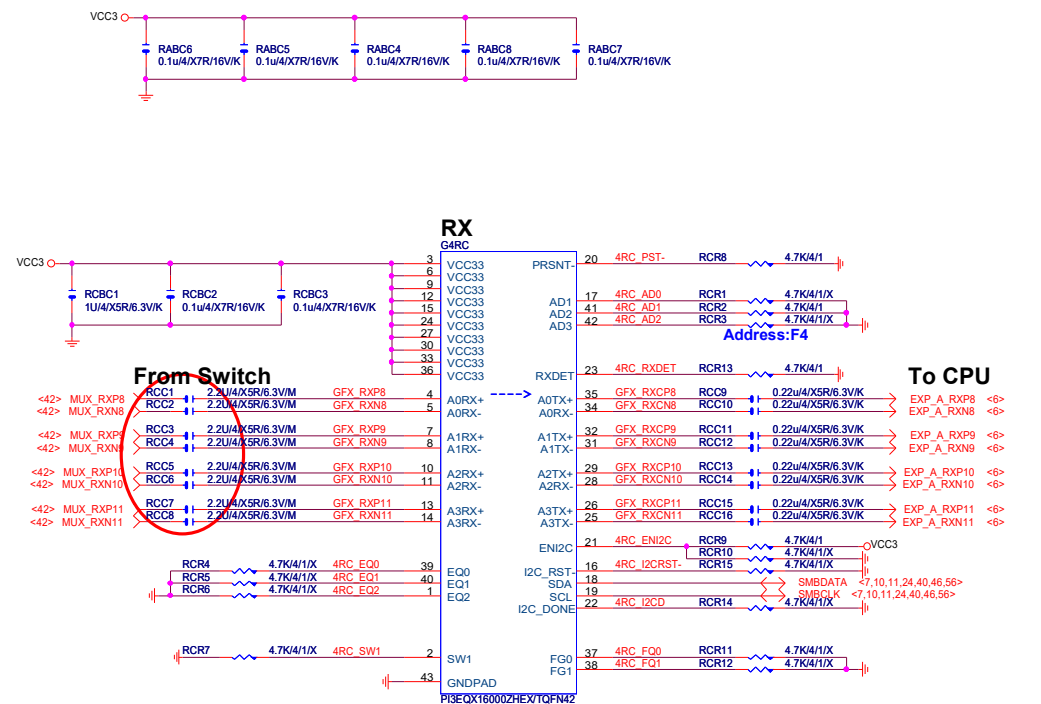
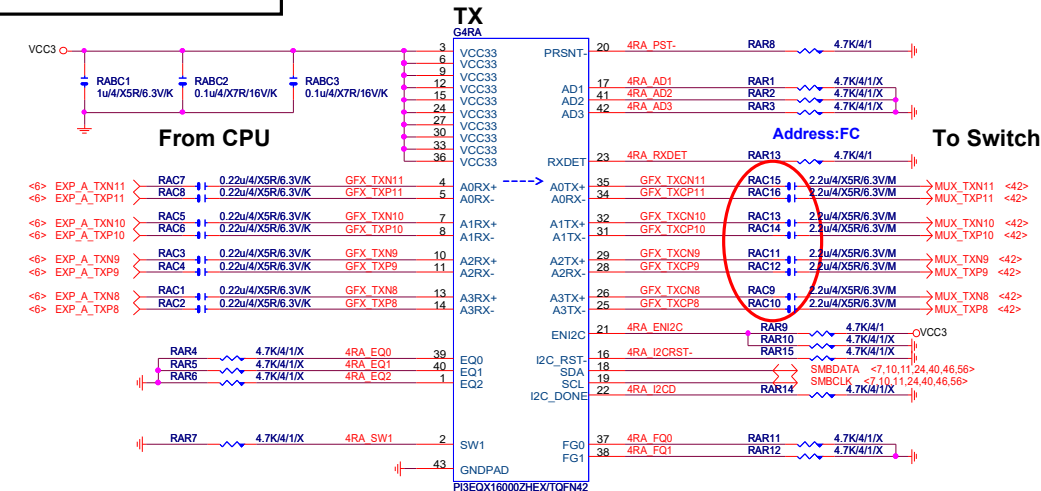


<http://www.repair1.ru/>



<b>GIGABYTE</b>			
<b>PCIE X8, SWITCH</b>			
Size	Document Number	Rev	
Cuskm	<b>B550 AORUS MASTER</b>	<b>1.01</b>	
Date:	Monday, May 11, 2020	Sheet	43 of 56

# PCIe GEN 4 REDRIVER



<http://www.repair1.ru/>

**GIGABYTE™**

Title				DIGITAL LED	
Size	Document Number	B550 AORUS MASTER		Rev	1.01
Date:	Monday, May 11, 2020	Sheet	44	of	56

Rev: 0.1

**GIGABYTE**<sup>TM</sup>

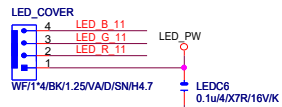
Title			
ASM 2142 U31			
Size	Document Number		Rev
Custom	B550 AORUS MASTER		1.01
Date:	Monday, May 11, 2020	Sheet	45 of 56



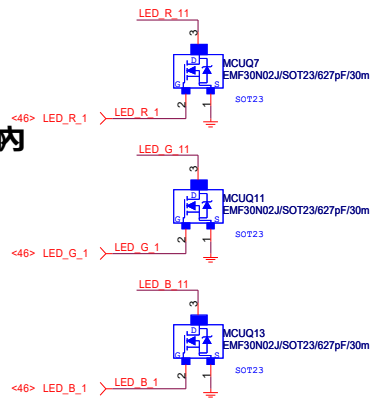
## 第一區 LED

## 第一區 LED CONTROL

FOR 後窗裝甲led connect  
(LED\_COVER放在後窗裝甲內)



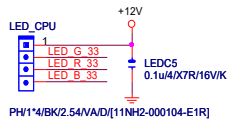
\*Footprint : LED-1X4-1P25MM



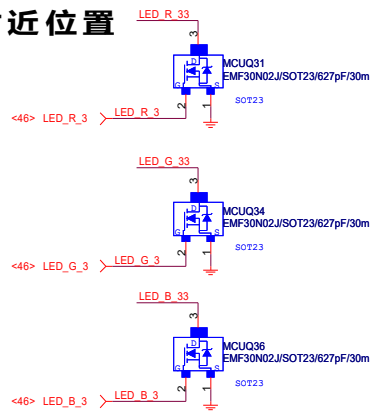
## 第三區 LED

## 第三區 LED CONTROL

燈條 LED (LED\_CPU放在CPU附近位置)

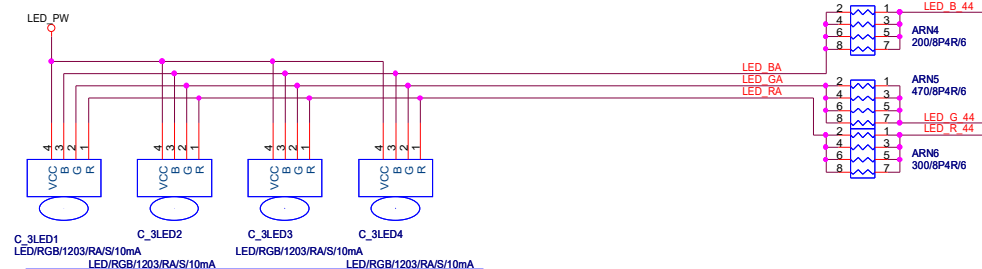


Footprint "PH1X4-FAN-AMD-L"



## 第四區 LED

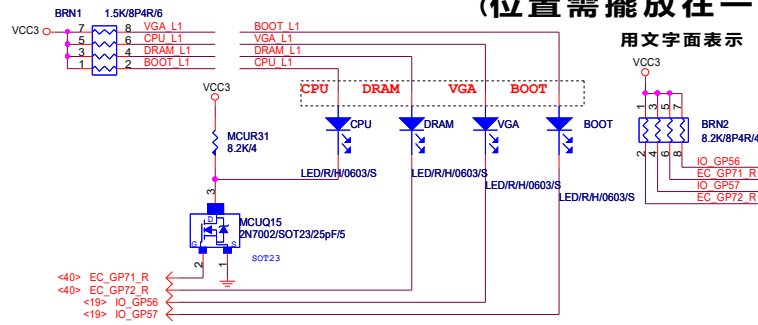
FOR AUDIO 正板測發光LED\*4 (位置在正板依據AUDIO\_HS設計擺放)



FOOTPRINT: LED-4P-RGB-SIDE-1

DEBUG PORT LED \*4  
(位置需擺放在一起)

用文字面表示



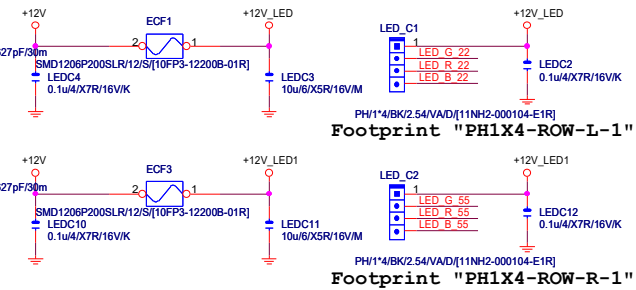
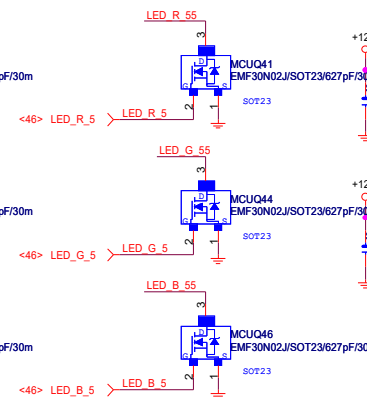
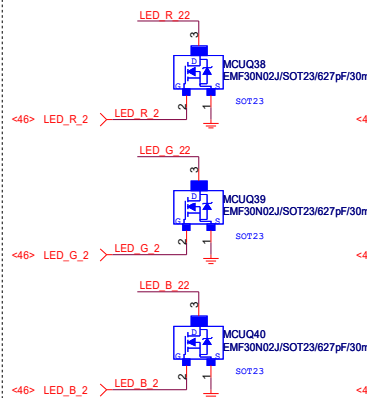
## 第五區 LED

## 第二區 LED CONTROL

## 第五區 LED CONTROL

燈條 LED (LED\_C1放在PCB左邊板邊位置)

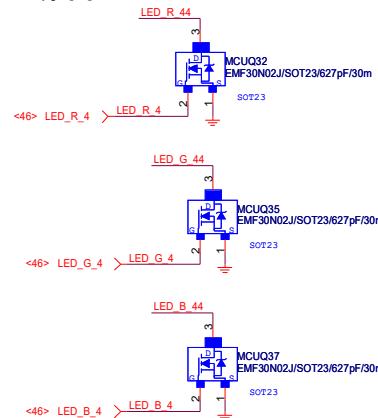
燈條 LED (LED\_C2放在PCB右邊板邊位置)



Footprint "PH1X4-ROW-L-1"

Footprint "PH1X4-ROW-R-1"

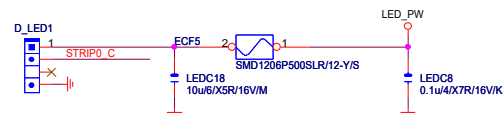
## 第四區 LED CONTROL



<http://www.repair1.ru/>

## 第六區 LED (靠近左上板邊位置)

### Digital LED Strip1

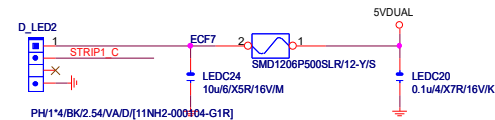


PH1\*4/BK/2.54/VA/D/[11NH2-000104-G1R]

Footprint PH1X4-CUT3-LED-L

## 第七區(靠近右下PU板邊位置)

### Digital LED Strip2

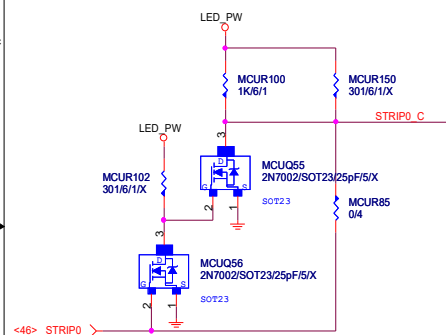


PH1\*4/BK/2.54/VA/D/[11NH2-000104-G1R]

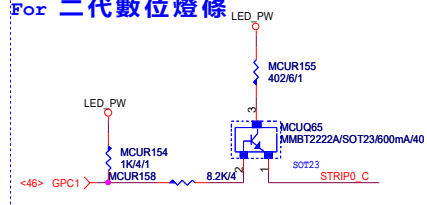
Footprint "PH1X4-CUT3-LED-R"

(for pin-name 與 model-name 同方向)

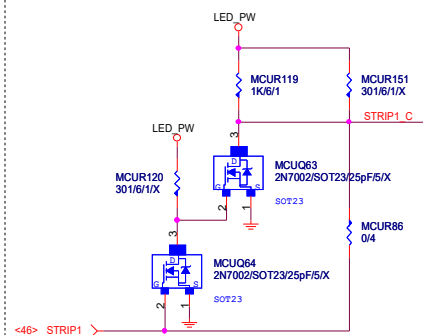
### 燈條 Level shift



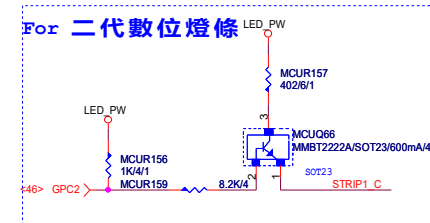
### For 二代數位燈條



### 燈條 Level shift



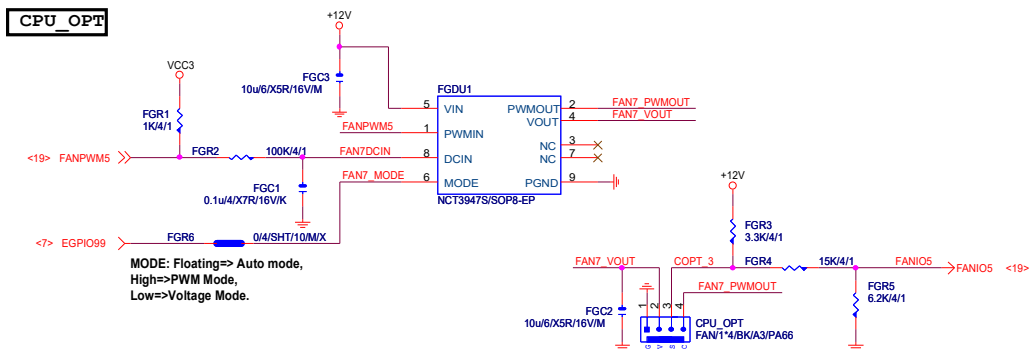
### For 二代數位燈條



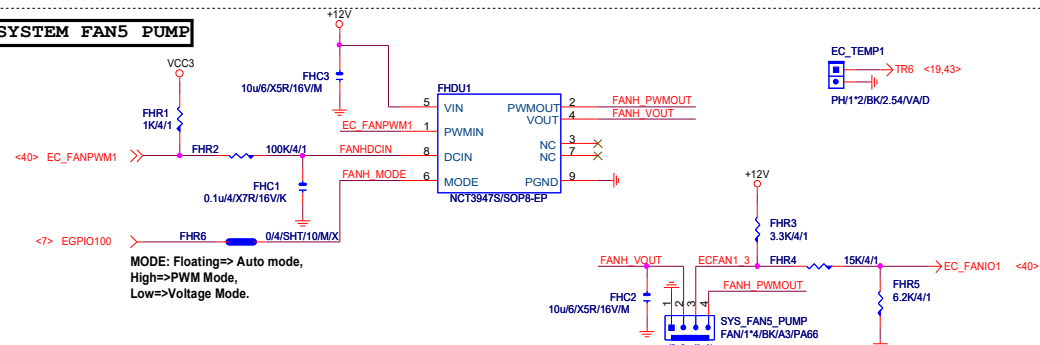
<http://www.repair1.ru/>

**GIGABYTE**

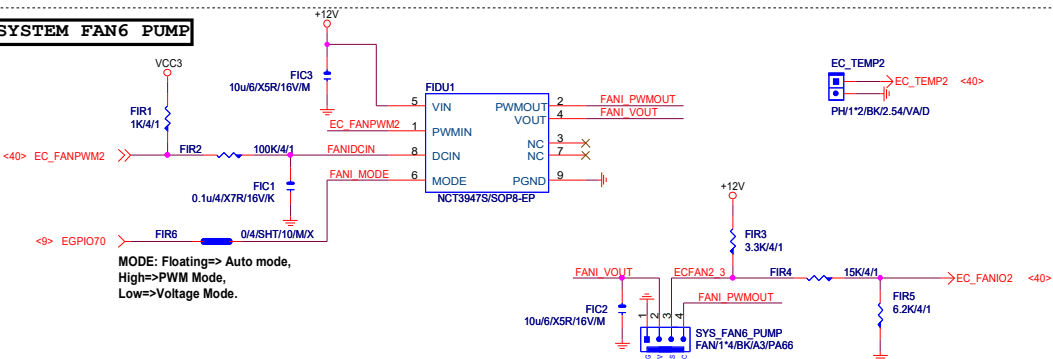
Title			
PCH / IO / HS / LED_C LED			
Size	Document Number	Rev	
Cuskm	B550 AORUS MASTER	1.01	
Date:	Monday, May 11, 2020		Sheet 48 of 56



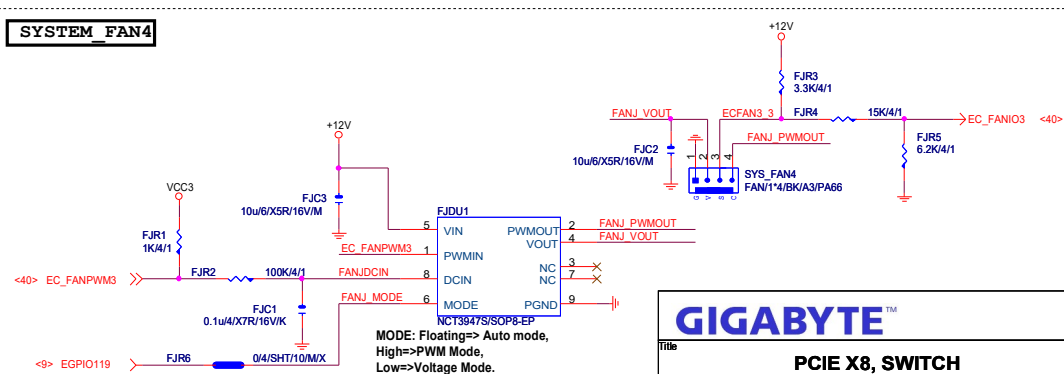
## SYSTEM FAN5 PUMP



## SYSTEM FAN6 PUMP



## SYSTEM FAN4



Title			
PCIE X8, SWITCH			
Size	Document Number	Rev	
Custom	B550 AORUS MASTER	1.01	
Date:	Monday, May 11, 2020	Sheet 49 of 56	

**KBL FAN LOCATION MAP REFER TO PAGE.27**

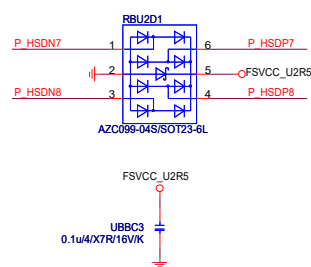
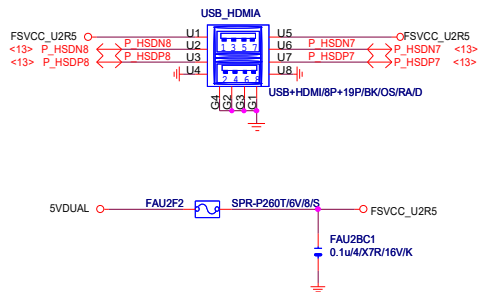
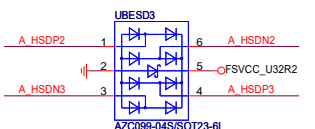
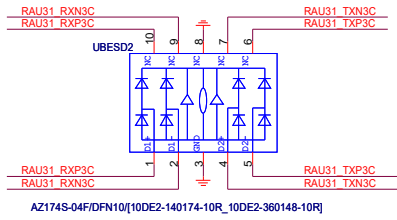
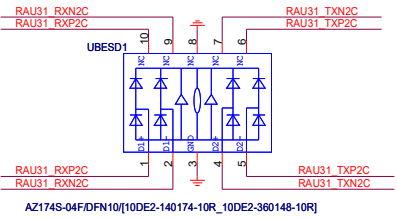
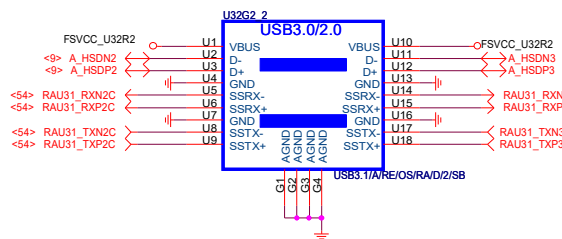
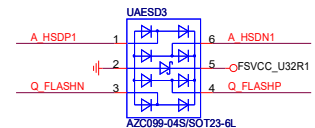
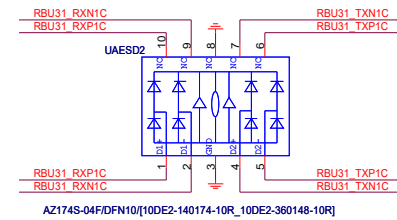
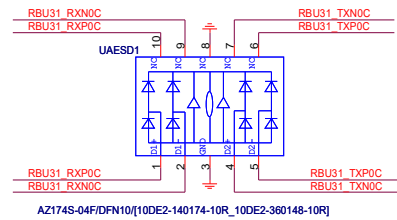
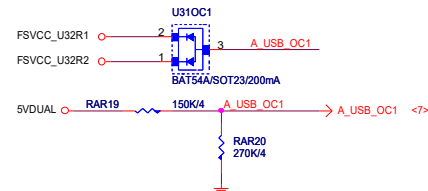
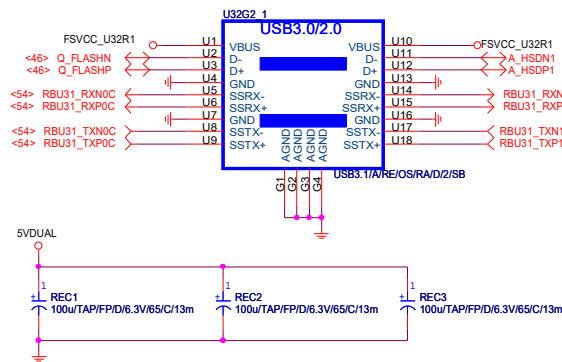




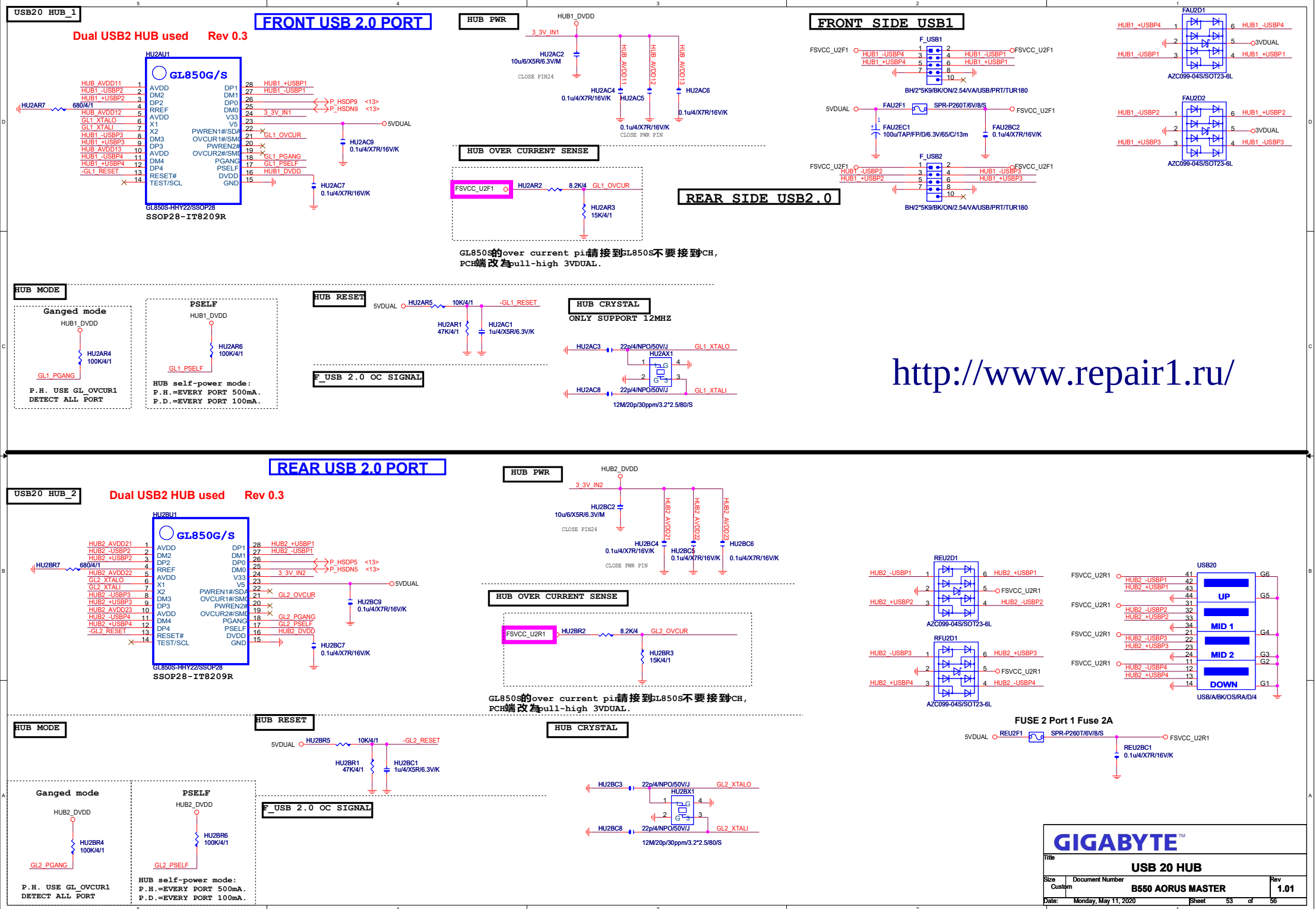
<b>GIGABYTE</b> ™			
Title			
Realtek RTL8125			
Size	Document Number		Rev
Custom	B550 AORUS MASTER		1.01
Date:	Monday, May 11, 2020	Sheet	51 of 56

Rev: 0.81

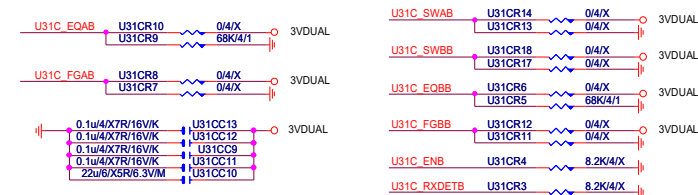
## R\_USB30 PORT



<http://www.repair1.ru/>

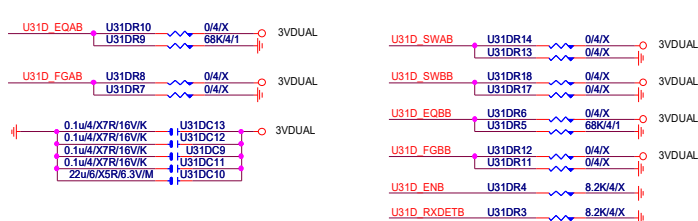


**CPU**

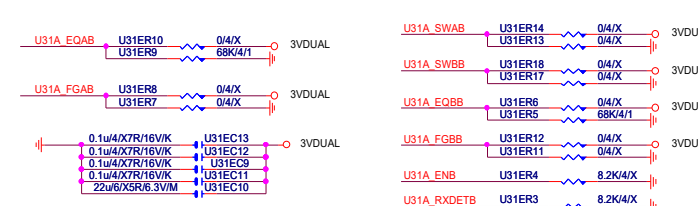


1. Differential Pair can't be swaped
2. Redriver to Connector Length min. 500mil

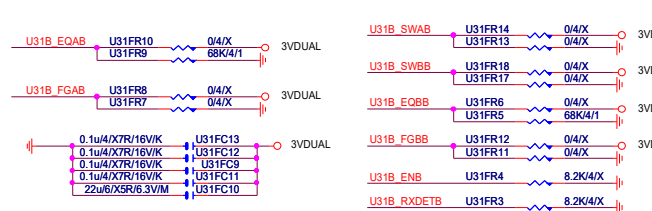
**CPU**



**CPU**



**CPU**



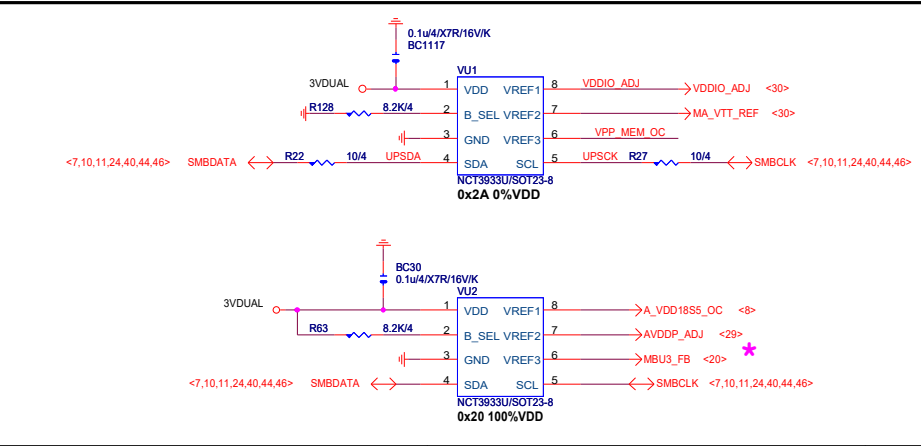
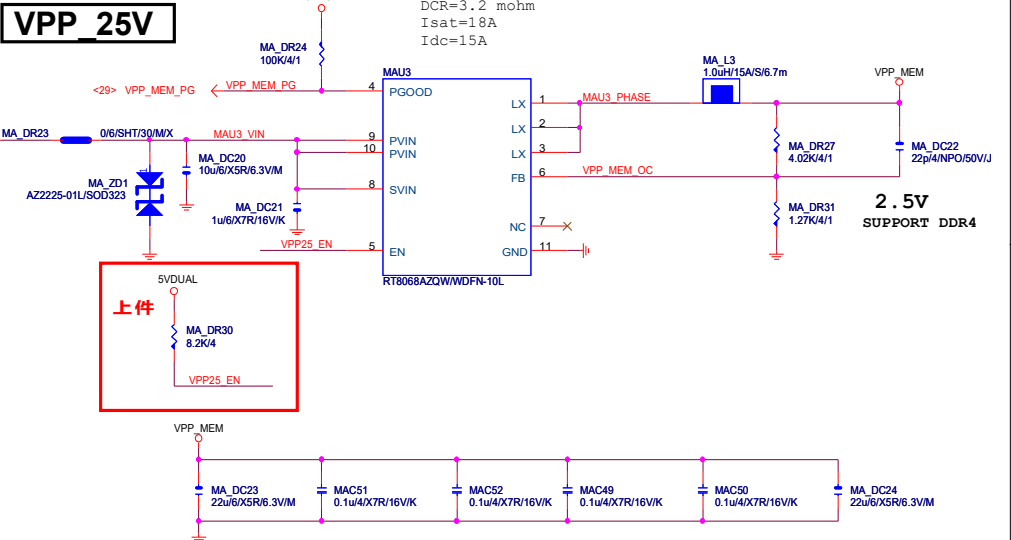
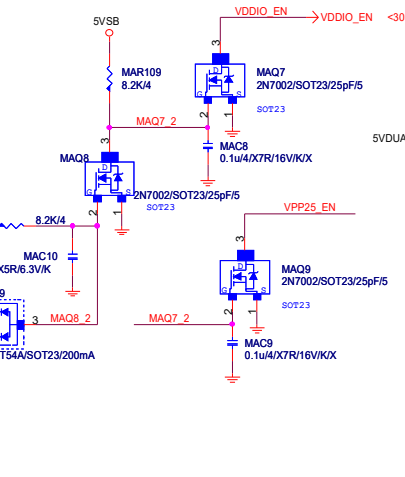
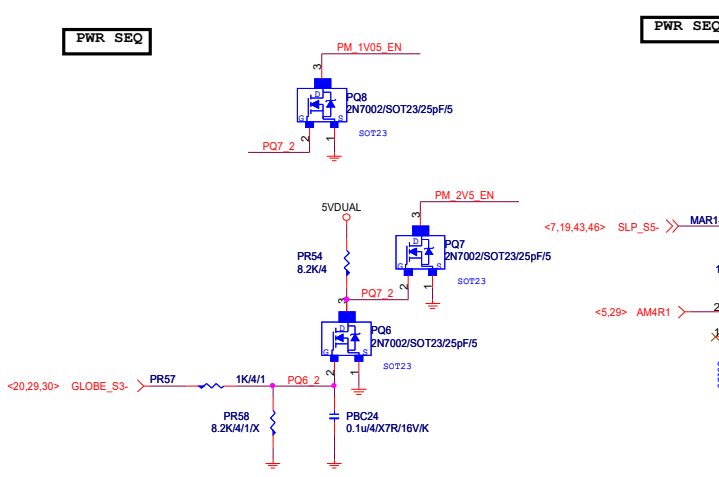
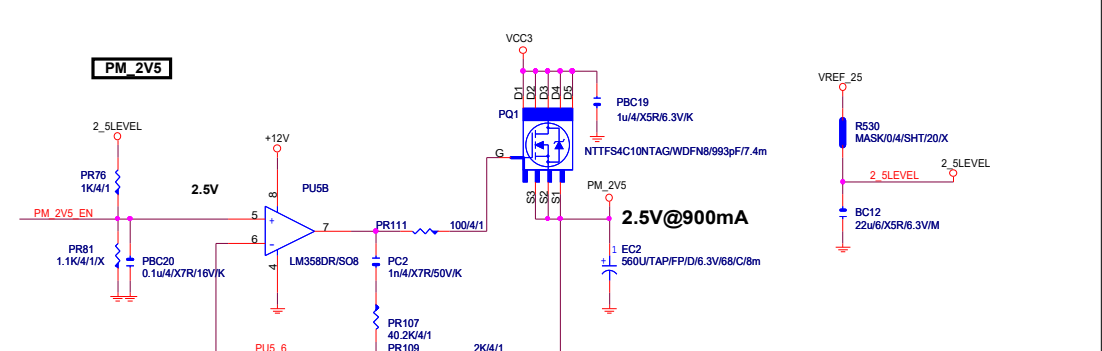
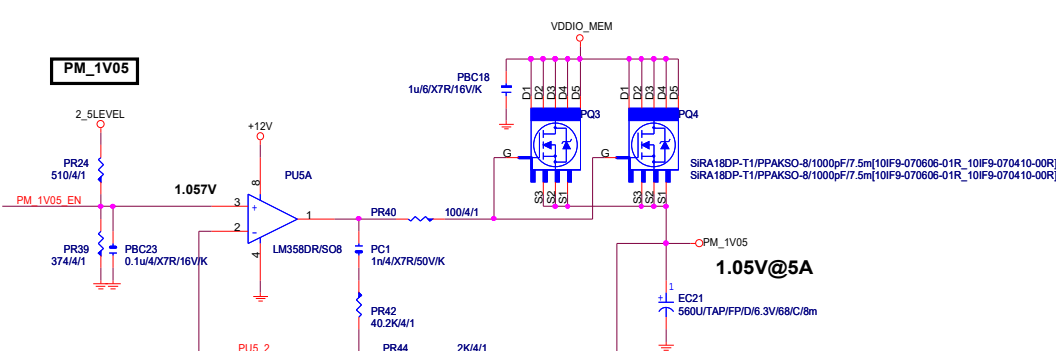
<http://www.repair1.ru/>

## GIGABYTE

Size Custom	Document Number <b>B550 AORUS MASTER</b>	Rev <b>1.01</b>
Date: Monday, May 11, 2020	Sheet 54 of 56	



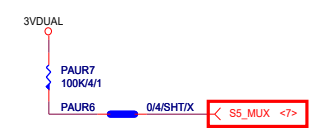
<b>GIGABYTE</b> ™			
Title <b>X570 POWER</b>			
Size Custom	Document Number <b>B550 AORUS MASTER</b>		Rev <b>1.01</b>
Date: Monday, May 11, 2020	Sheet 1	of 55	55



<http://www.repair1.ru/>

Address	0x2A	0x28	0x26	0x24	0x22	0x20
R1 (kΩ)	open	3.9	3	2.2	1.3	10
R2 (kΩ)	10	1.3	2.2	3	3.9	open
ADD_SEL Voltage (% of VCC)	0	25	42	58	75	100

Table1. Recommended Slave Address Setting



**PM POWER**

**B550 AORUS MASTER**

Rev 1.01

Document Number

Monday, May 11, 2020

Sheet 56 of 56